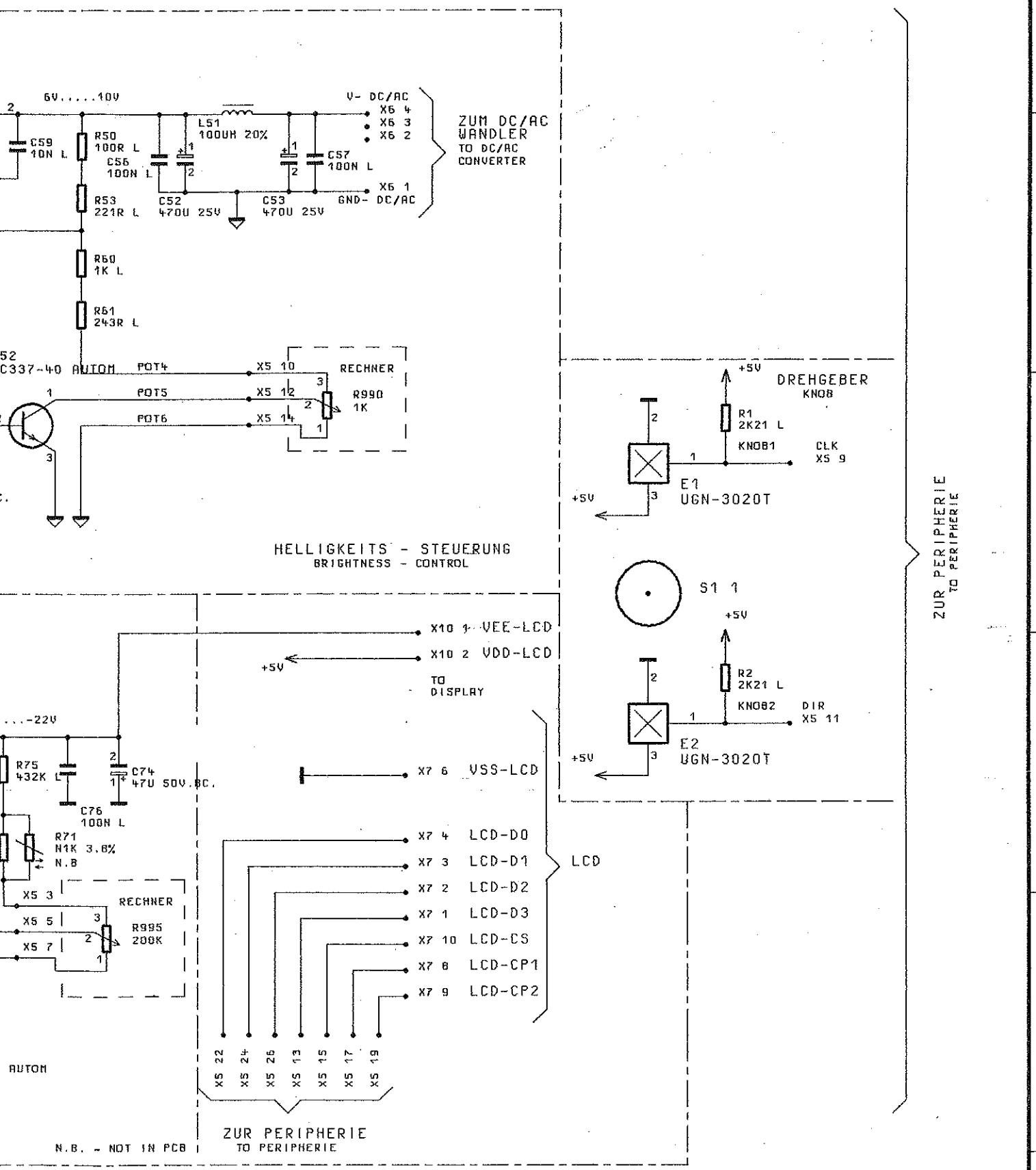
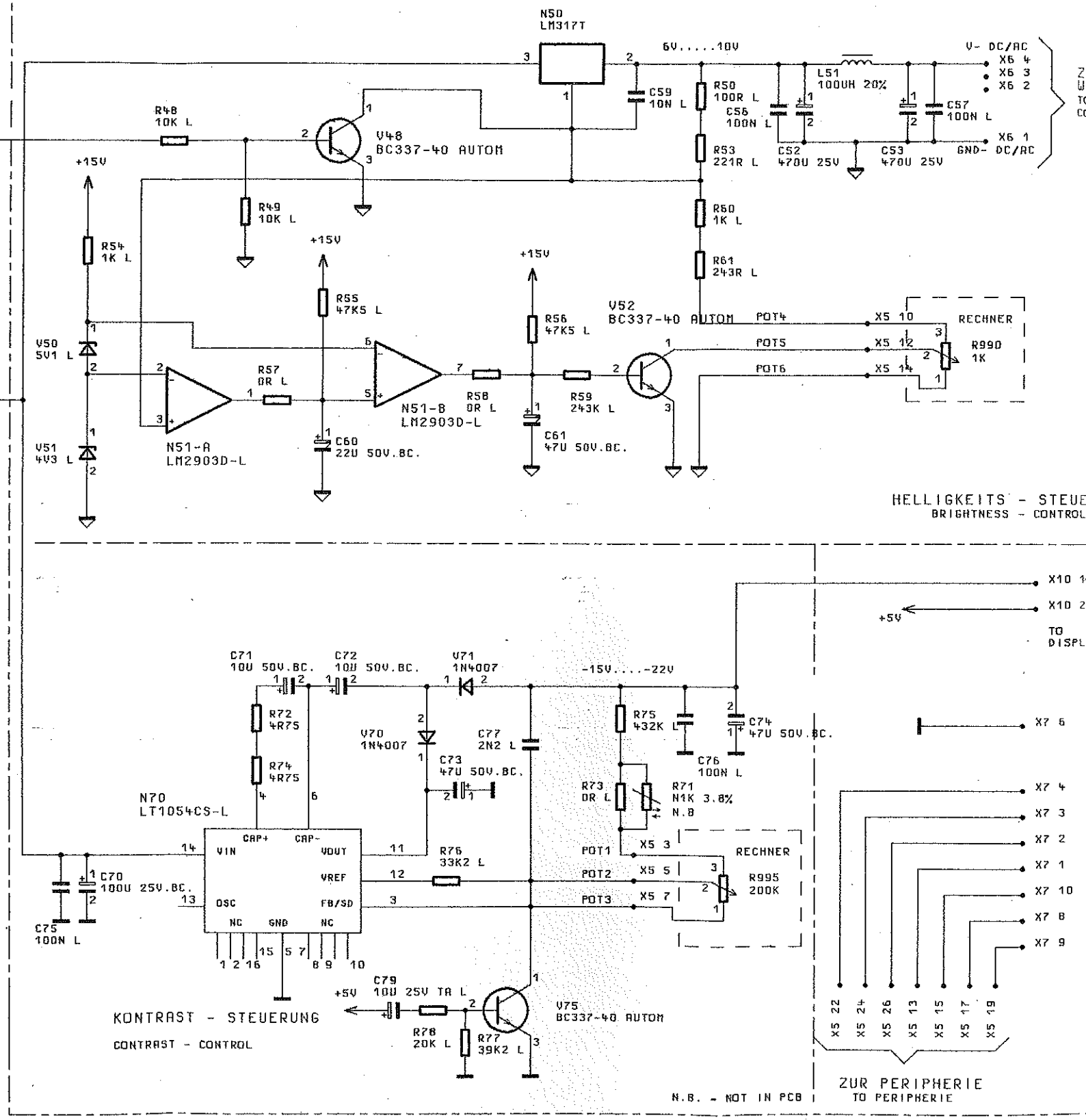


04/	48730 90	07.04.94	JN	1GPK	TAG	NAME	BENENNUNG
				BEARB.		JN	DREHGEBER
				GEPR.		DR	KNOB ASSEMBLY
				NORM			
				PLOTT	07.04.94		
/							ZEICHN.-NR. 1035.5592.01S
AEND. IND.	AENDERUNGS-MITTEILUNG.	DATUM	NAME	ROHDE&SCHWARZ			BLATT-NR. 1
				ZU GERAET SMP	REG.I.V. 1035.5005	ERSTE Z. 1035.5440	V. BL.

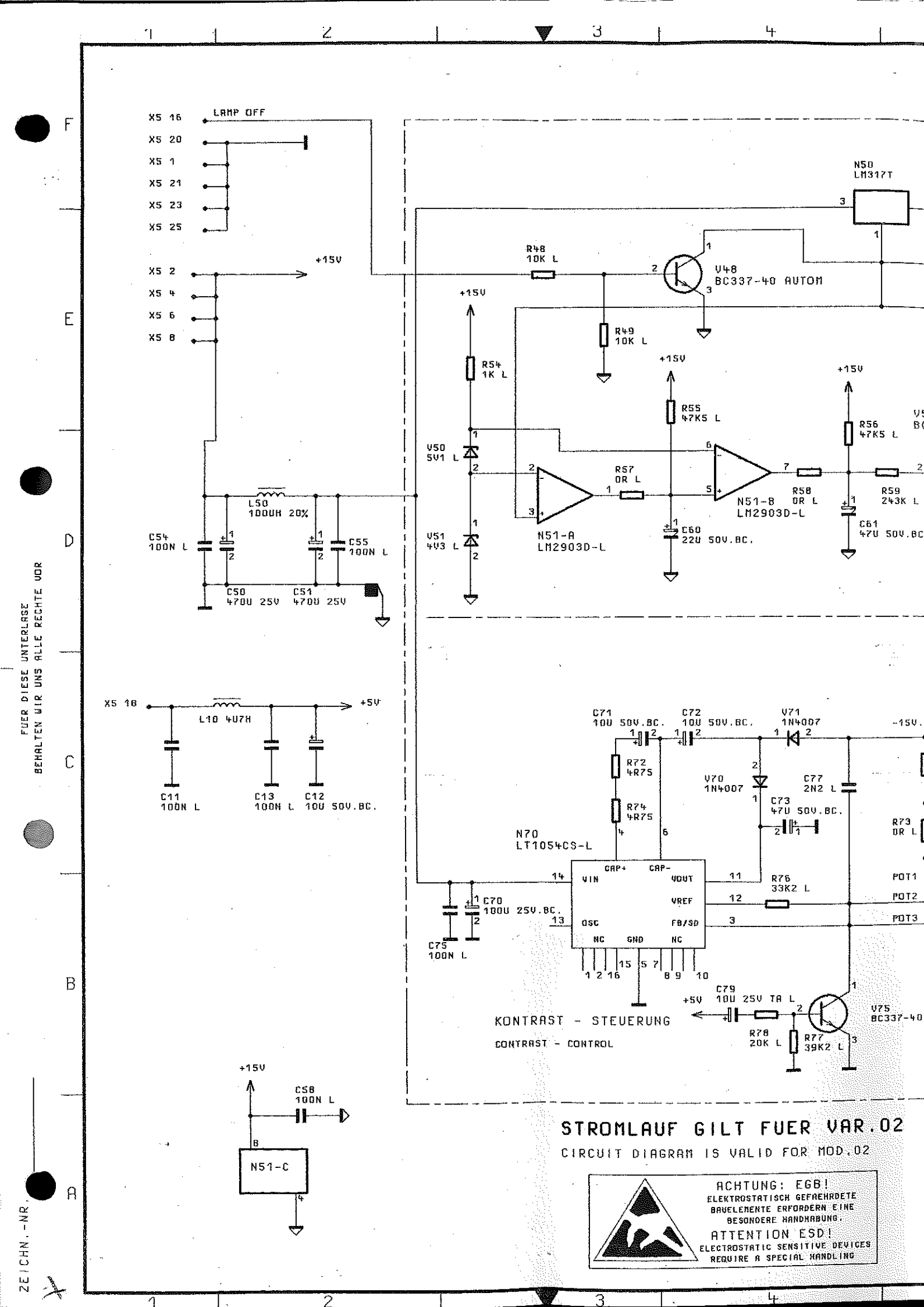


STROMLAUF GILT FUER VAR.02

CIRCUIT DIAGRAM IS VALID FOR MOD.02

ACHTUNG: EGB!
ELEKTROSTATISCH GEFAEHRDETE BAUELEMENTE ERFORDERN EINE BESONDERE HANDHABUNG.
ATTENTION ESD!
ELECTROSTATIC SENSITIVE DEVICES REQUIRE A SPECIAL HANDLING

04/	48730 90	07.04.94	JN	1GPK	TAG
				BEARB.	
				GEPR.	
				NORM	
				PLOTT	07.04.9
/					
AEND. IND.	AENDERUNGS-MITTEILUNG	DATUM	NAME	ROHDE&S	
				ZU GERAET	SM



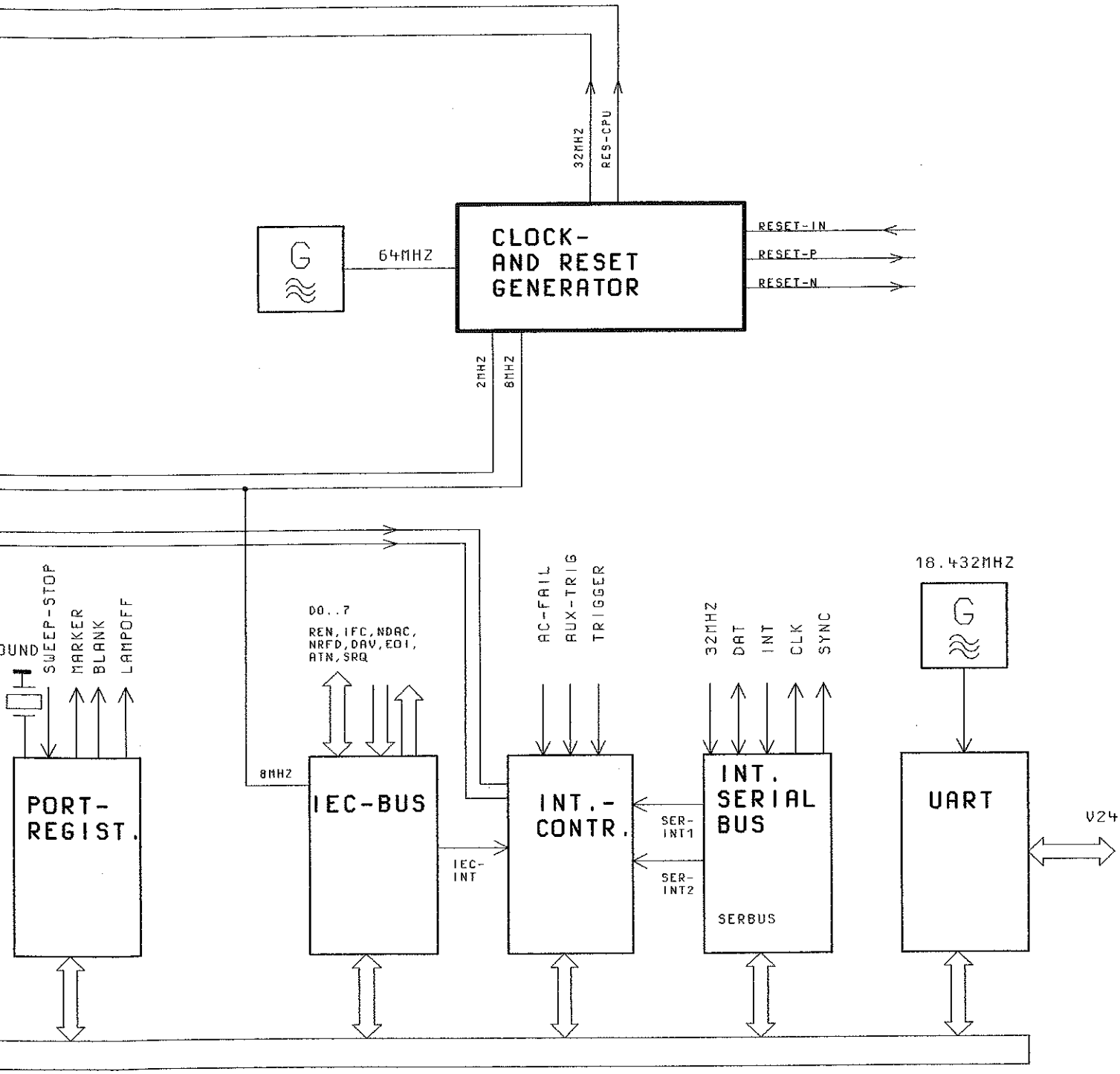
STROMLAUF GILT FUER VAR.02

CIRCUIT DIAGRAM IS VALID FOR MOD.02

ACHTUNG: EGB!
ELEKTROSTATISCH GEFAEHRDETE BAUELEMENTE ERFORDERN EINE BESONDERE HANDHABUNG.
ATTENTION ESD!
ELECTROSTATIC SENSITIVE DEVICES REQUIRE A SPECIAL HANDLING

FUER DIESE UNTERLAGE BEHALTEN WIR UNS ALLE RECHTE VOR

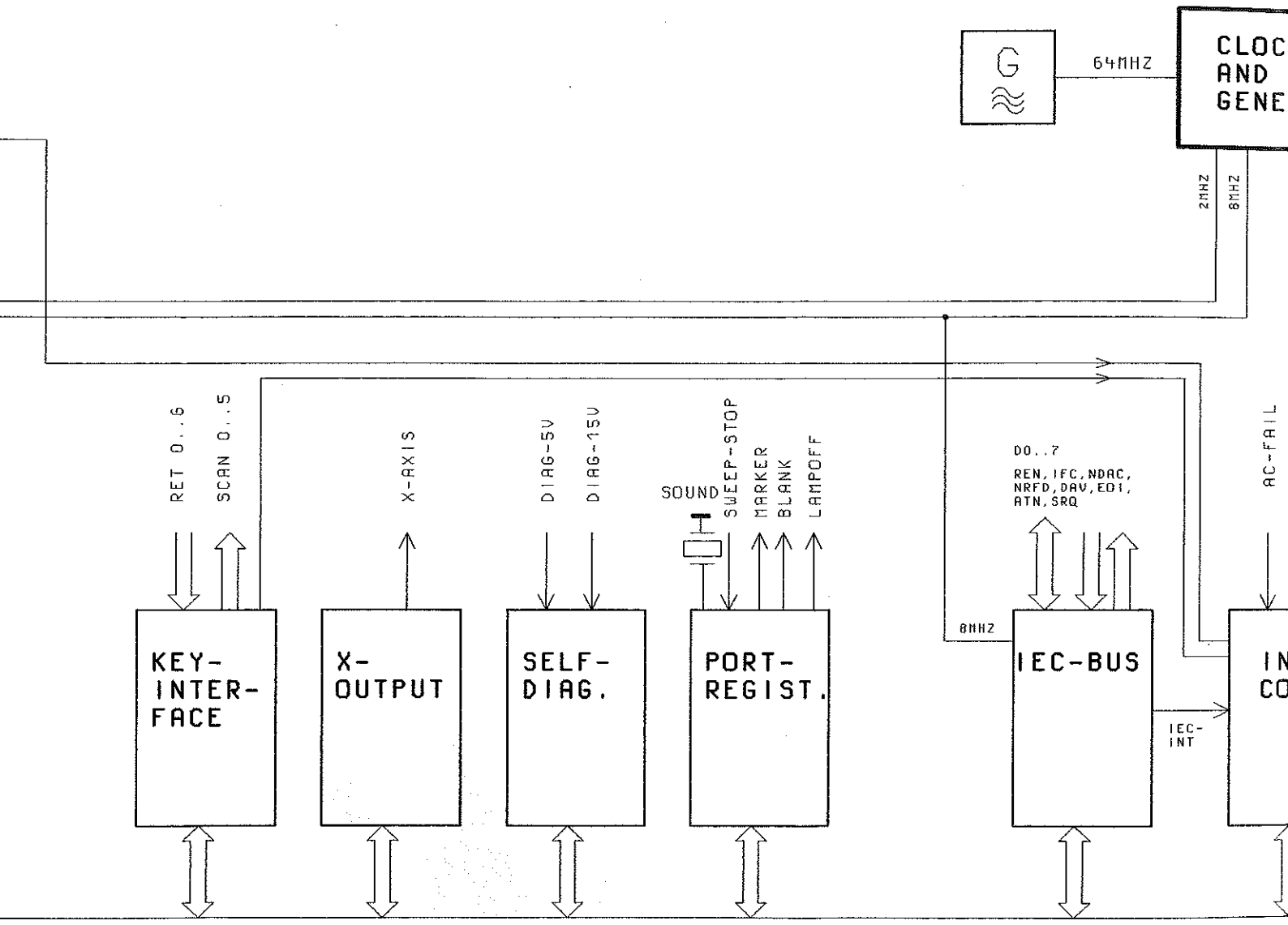
ZEICHN.-NR.



BINDENDE ANGABEN UEBER VARIANTEN,
TRIMMWERTE, BAUTEILWERTE UND
NICHT BESTUECKTE BAUTEILE SIEHE SA.

FOR BINDING INFORMATION ON MODELS,
TRIMMING AND COMPONENTS VALUES AND
NONFITTED COMPONENTS SEE PARTS LIST.

08/13		97-12-08	EI	MENP	TAG	NAME	BENENNUNG
				BEARB.		EI	RECHNER
				GEPR.			PROCESSOR
				NORM			
				PLOTT	09.12.97		
08/		13.03.95	DR	ROHDE&SCHWARZ			ZEICHN.-NR. 1035.7250.01S
AEND. IND.	AENDERUNGS-MITTEILUNG	DATUM	NAME	ZU GERAET SMP			BLATT-NR. 1+ v. 39 BL.
				REG.I.V. 1035.5005		ERSTE Z. 1035.5005	



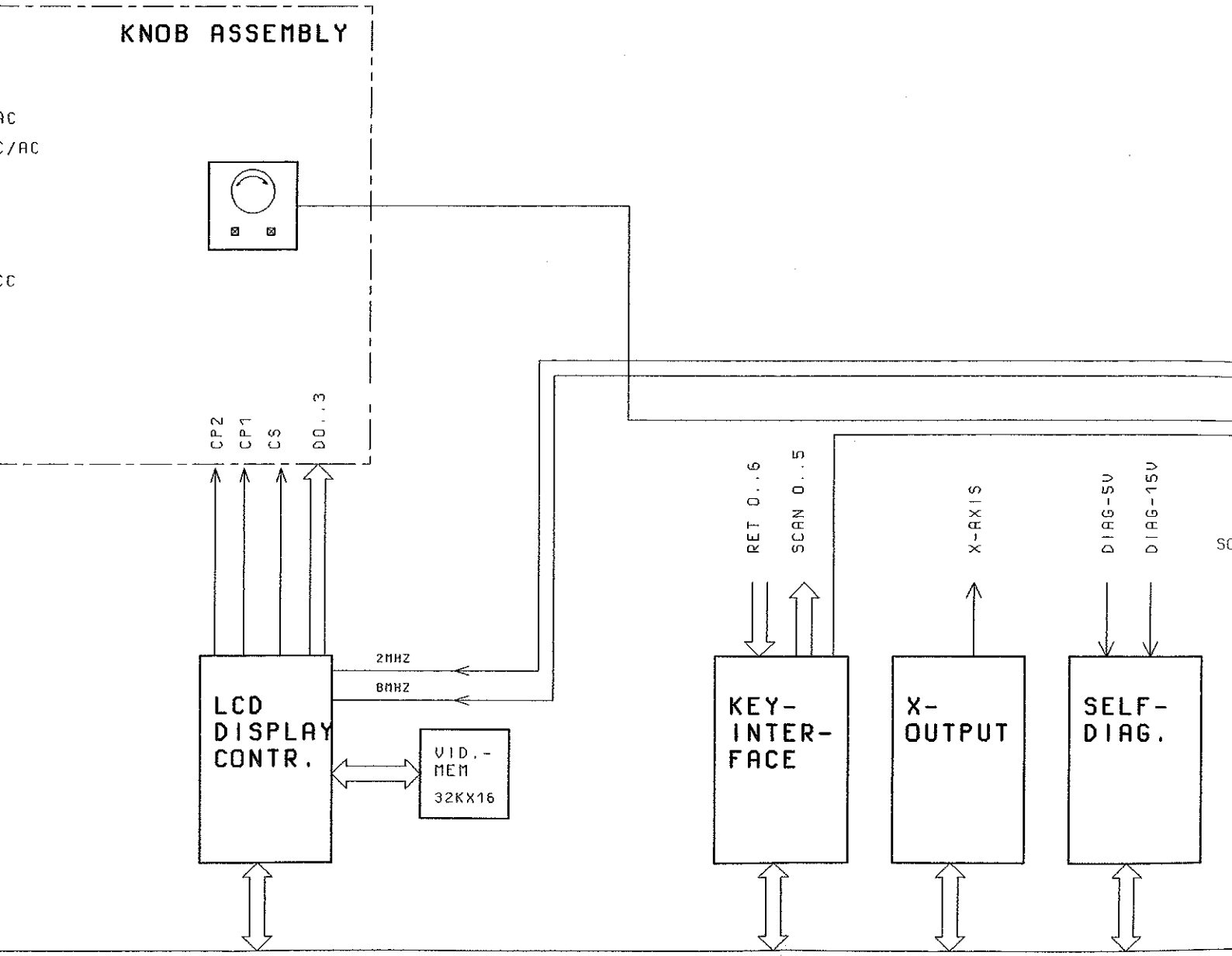
BINDENDE ANGABEN UEBER VARIANTEN,
TRIMMWERTE, BAUTEILWERTE UND
NICHT BESTUECKTE BAUTEILE SIEHE SA.

FOR BIN
TRIMMIN
NONFITT

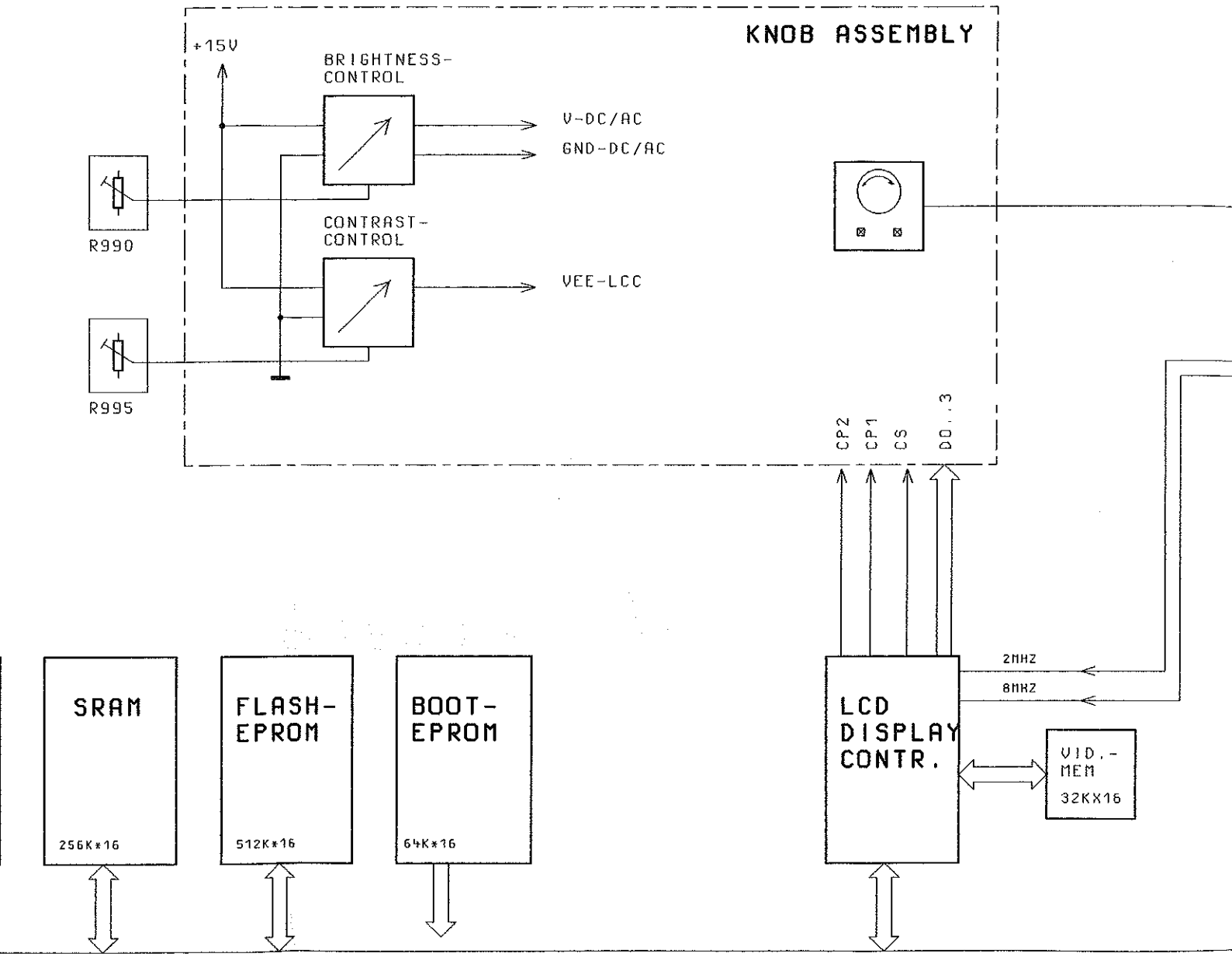
ACHTUNG: EGB!
ELEKTROSTATISCH GEFAEHRDETE
BAUELEMENTE ERFORDERN EINE
BESONDERE HANDHABUNG.
ATTENTION ESD!
ELECTROSTATIC SENSITIVE DEVICES
REQUIRE A SPECIAL HANDLING

08/13		97-12-08	EI	MENP	TAG
				BEARB.	
				GEPR.	
				NORM	
				PLOTT	09.12.97
08/		13.03.95	DR		
AEND. IND.	AENDERUNGS-MITTEILUNG	DATUM	NAME		

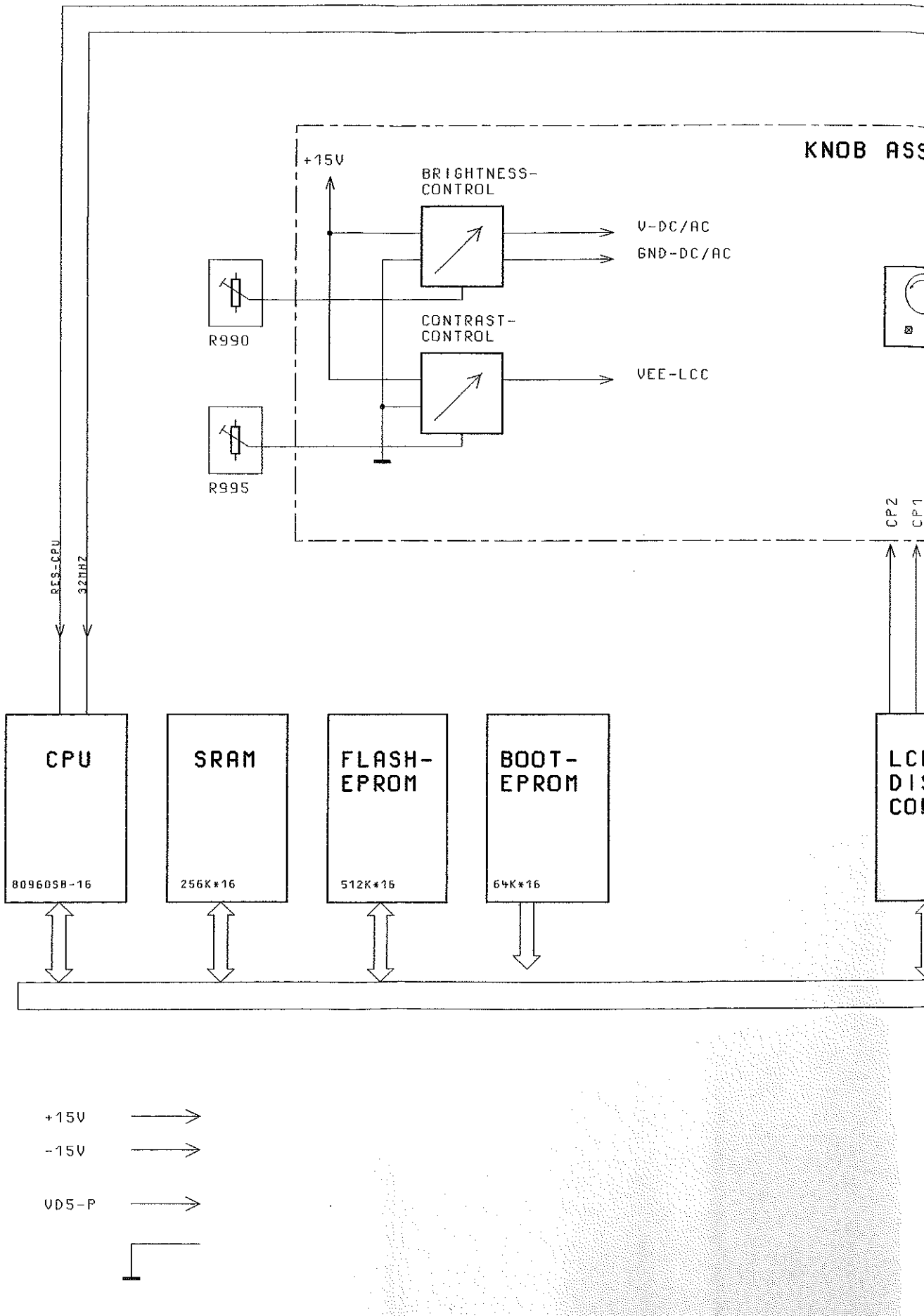
ROHDE&SCH
ZU GERAET SMP



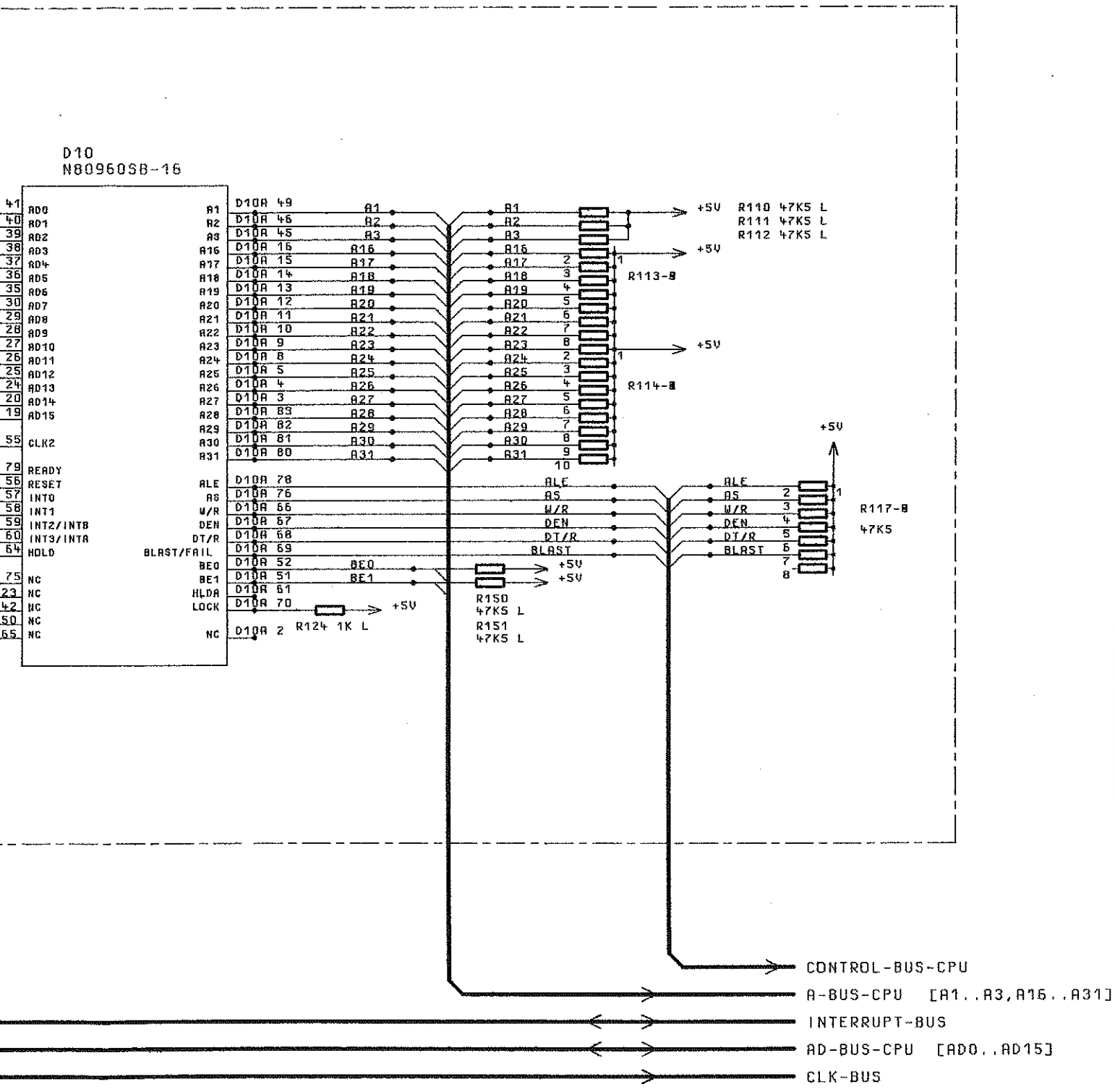
ACHTUNG: EGB!
ELEKTROSTATISCH GEFAEHRDETE BAUELEMENTE ERFORDERN EINE BESONDERE HANDHABUNG.
ATTENTION ESD!
ELECTROSTATIC SENSITIVE DEVICES REQUIRE A SPECIAL HANDLING


KNOB ASSEMBLY
+15V
BRIGHTNESS-
CONTROL
V-DC/AC
GND-DC/AC
CONTRAST-
CONTROL
VEE-LCC
R990
R995
CP2
CP1
CS
DO..3
2MHZ
8MHZ
LCD
DISPLAY
CONTR.
VID.-
MEM
32KX16
SRAM
256K*16
FLASH-
EPROM
512K*16
BOOT-
EPROM
64K*16

FUER DIESE UNTERLAGE
BEHALTEN WIR UNS ALLE RECHTE VOR



ZEICHN.-NR.

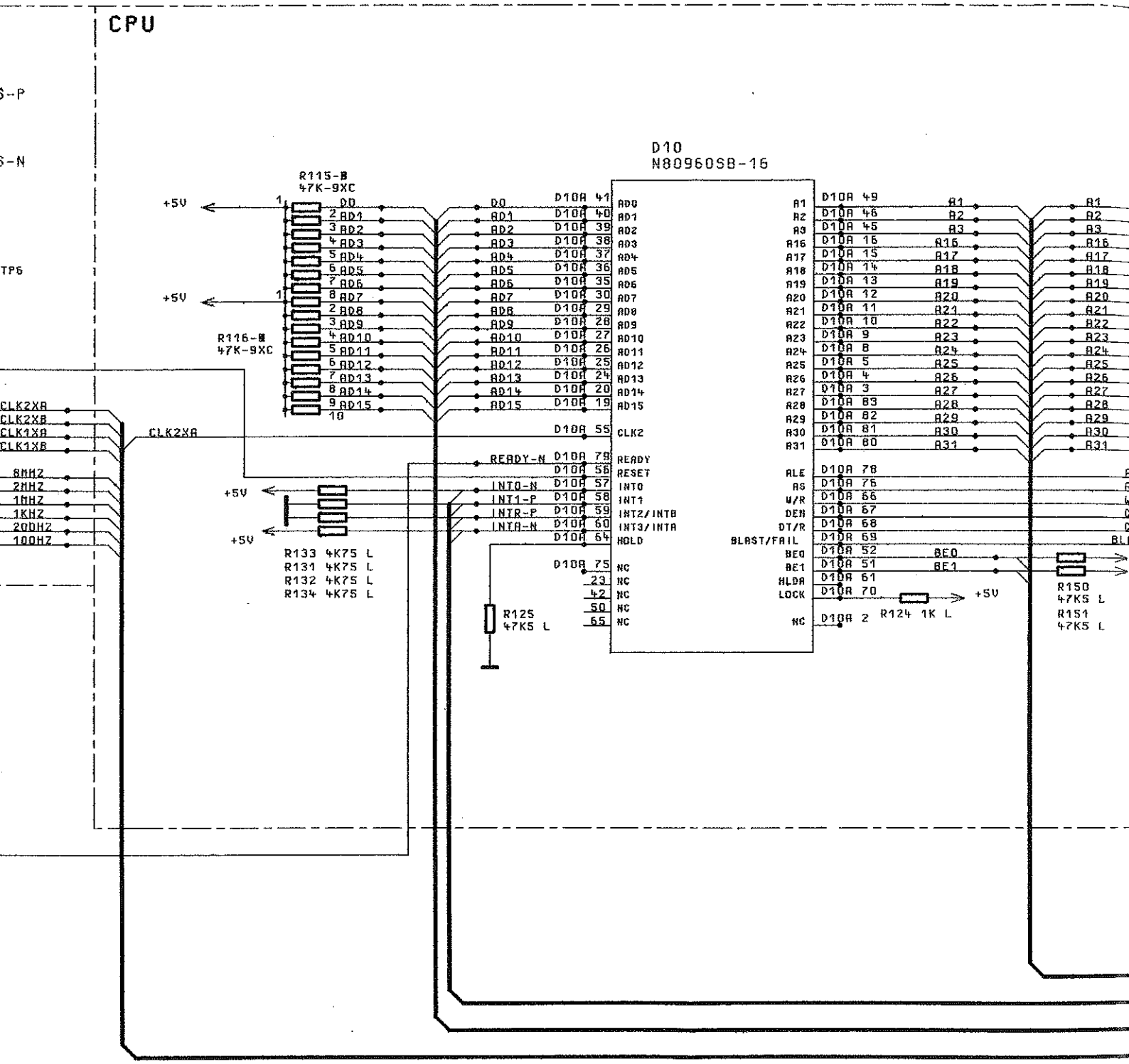


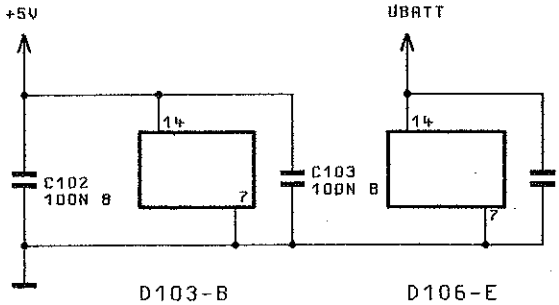
05/		19.04.94	DR	16PK	TAG	NAME	BENENNUNG
				BEARB.		JN	RECHNER
				GEPR.			PROCESSOR
				NORM			
				PLOTT	03.05.94		
/							ZEICHN.-NR. 1035.7250.015
AEND. IND.	AENDERUNGS-MITTEILUNG	DATUM	NAME	ZU GERAET SMP			REG.I.V. 1035.5005 ERSTE Z. 1035.5005

ROHDE&SCHWARZ

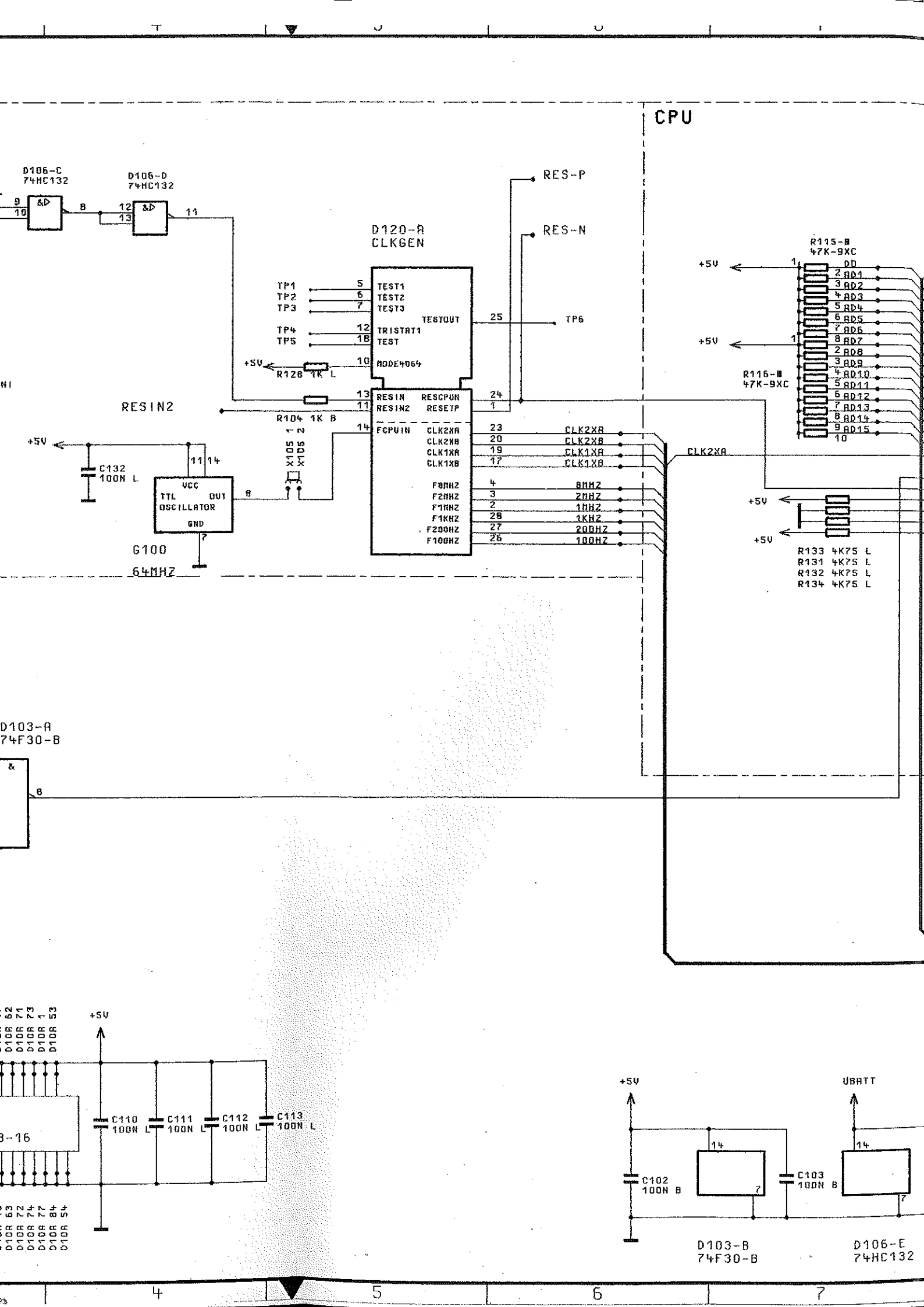
BLATT-NR. 2+ V. BL.

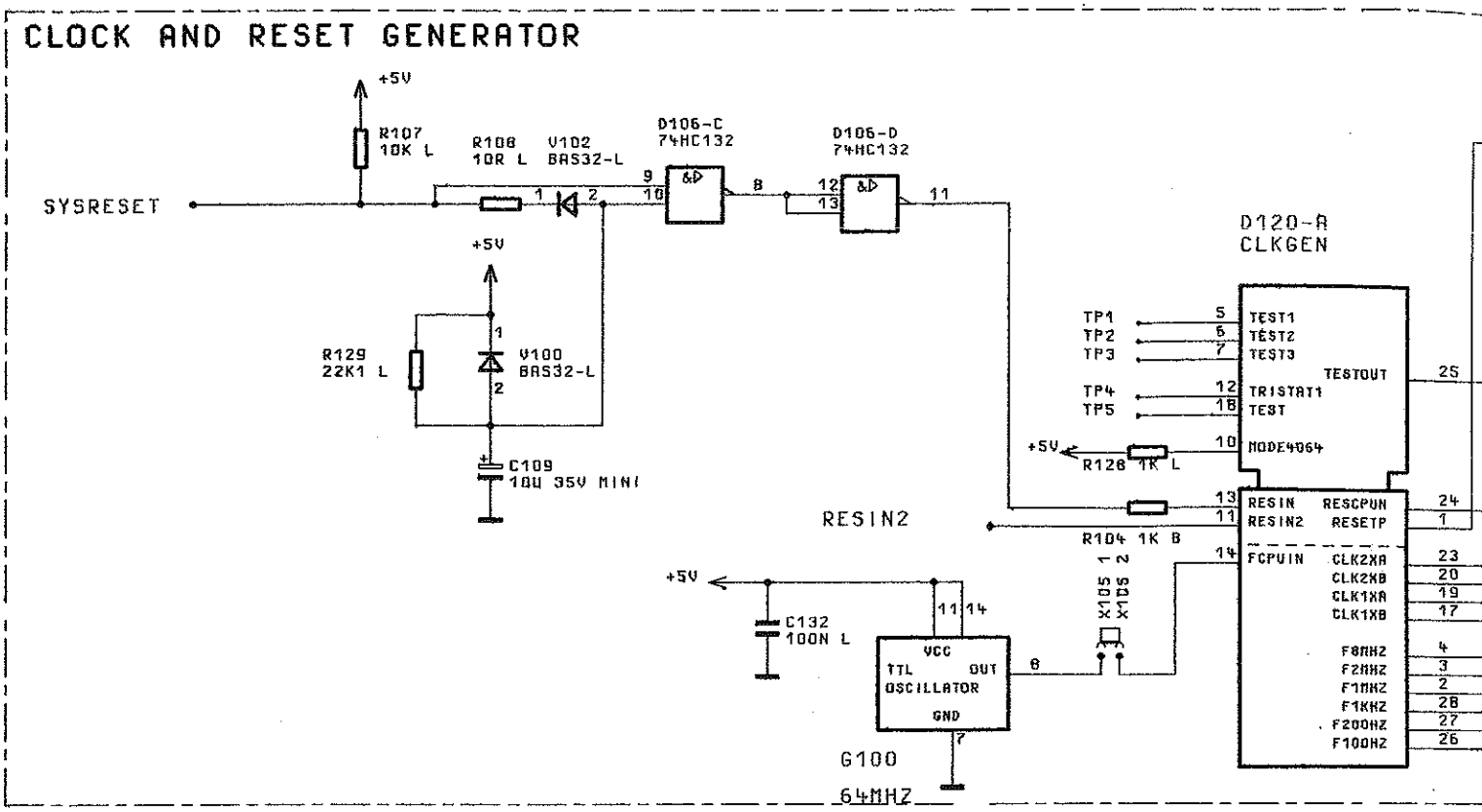
SHEET4

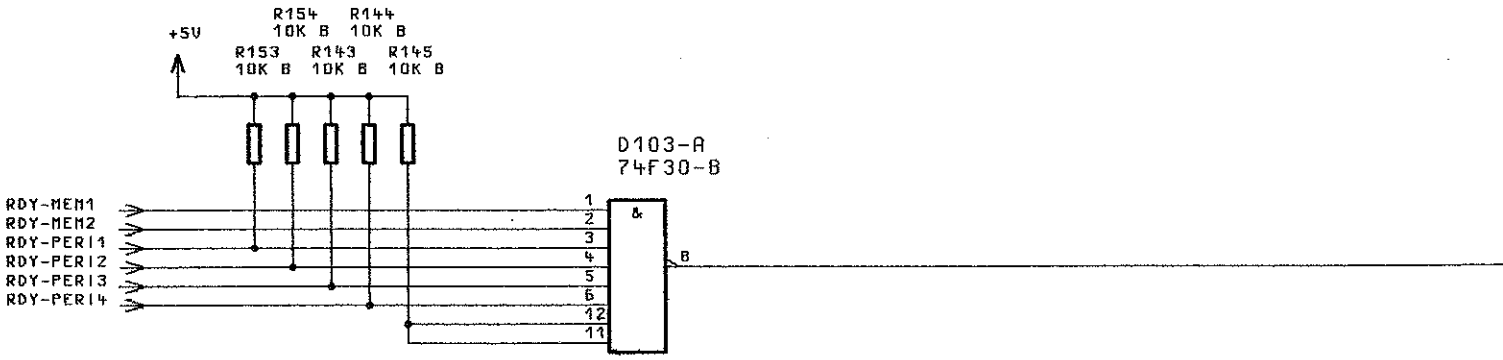


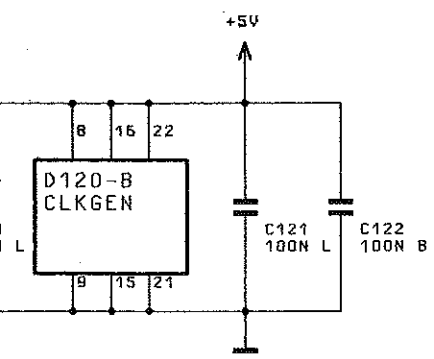


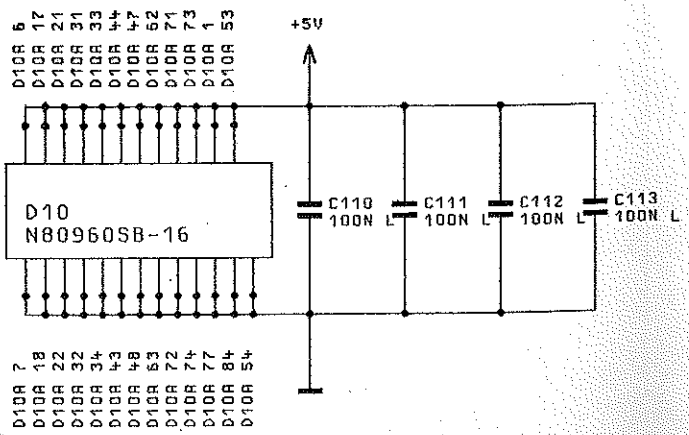
05/		19.04.94	DR	16PK	TAG
				BEARB.	
				GEPR.	
				NORM	
				PLOTT	03.05.94
/					
AEND. IND.	AENDERUNGS-MITTEILUNG	DATUM	NAME	ROHDE&SCH	
				ZU GERRET	SMP


CPU
D106-C
74HC132
D106-D
74HC132
D120-A
CLKGEN
RES-P
RES-N
TP1
TP2
TP3
TP4
TP5
TP6
TEST1
TEST2
TEST3
TESTOUT
TRISTAT1
TEST
MODE4064
R126 1K L
RESIN2
R104 1K B
RESIN
RESCPUN
RESETP
FCPUIN
CLK2XA
CLK2XB
CLK1XA
CLK1XB
F8MHZ
F2MHZ
F1MHZ
F1KHZ
F200HZ
F100HZ
8MHZ
2MHZ
1MHZ
1KHZ
200HZ
100HZ
X105
X106
C132
100N L
VCC
TTL
OSCILLATOR
OUT
GND
G100
64MHZ
+5V
R115-B
47K-9XC
R116-B
47K-9XC
AD0
AD1
AD2
AD3
AD4
AD5
AD6
AD7
AD8
AD9
AD10
AD11
AD12
AD13
AD14
AD15
R133 4K75 L
R131 4K75 L
R132 4K75 L
R134 4K75 L
D103-A
74F30-B
C110
100N L
C111
100N L
C112
100N L
C113
100N L
C102
100N B
C103
100N B
UBATT
D103-B
74F30-B
D106-E
74HC132

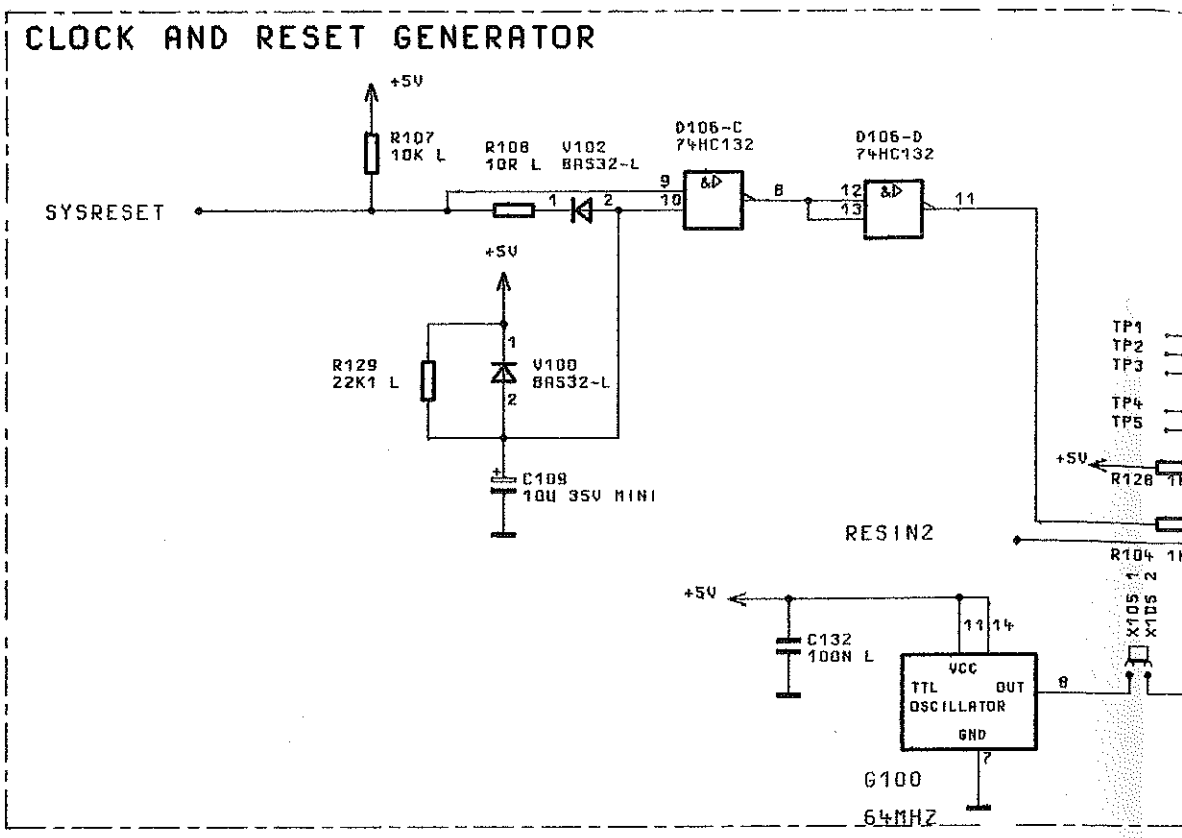

CLOCK AND RESET GENERATOR
+5V
R107
10K L
R108
10R L
V102
BAS32-L
D106-C
74HC132
D106-D
74HC132
SYSRESET
R129
22K1 L
V100
BAS32-L
C109
10U 35V MINI
D120-A
CLKGEN
TP1
TP2
TP3
TP4
TP5
TEST1
TEST2
TEST3
TESTOUT
TRISTAT1
TEST
MODE4064
R126 1K L
RESIN2
R104 1K B
RESIN
RESCPUN
RESETP
FCPUIN
CLK2XA
CLK2XB
CLK1XA
CLK1XB
F8MHZ
F2MHZ
F1MHZ
F1KHZ
F200HZ
F100HZ
C132
100N L
VCC
TTL
OSCILLATOR
OUT
GND
G100
64MHZ
X105
X106

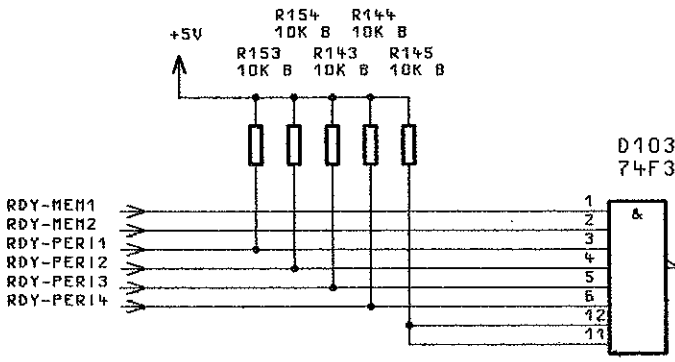

+5V
R154
10K B
R144
10K B
R153
10K B
R143
10K B
R145
10K B
D103-A
74F30-B
RDY-MEM1
RDY-MEM2
RDY-PERI1
RDY-PERI2
RDY-PERI3
RDY-PERI4

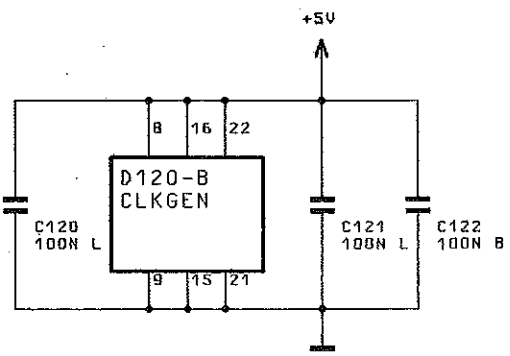

+5V
D120-B
CLKGEN
C121
100N L
C122
100N B

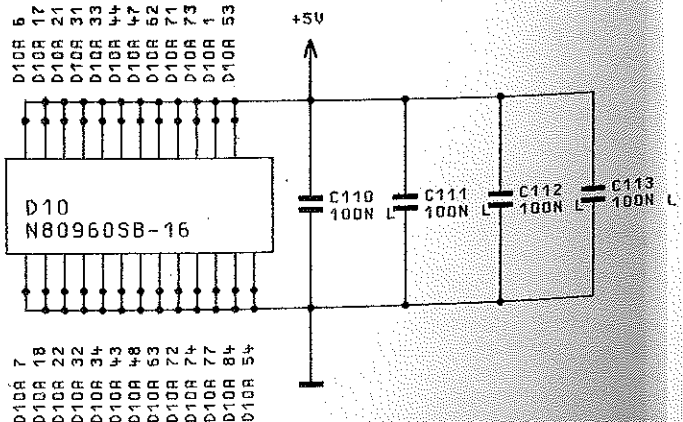

+5V
D10
N80960SB-16
C110
100N L
C111
100N L
C112
100N L
C113
100N L

FUER DIESE UNTERLAGE
BEHALTEN WIR UNS ALLE RECHTE VOR

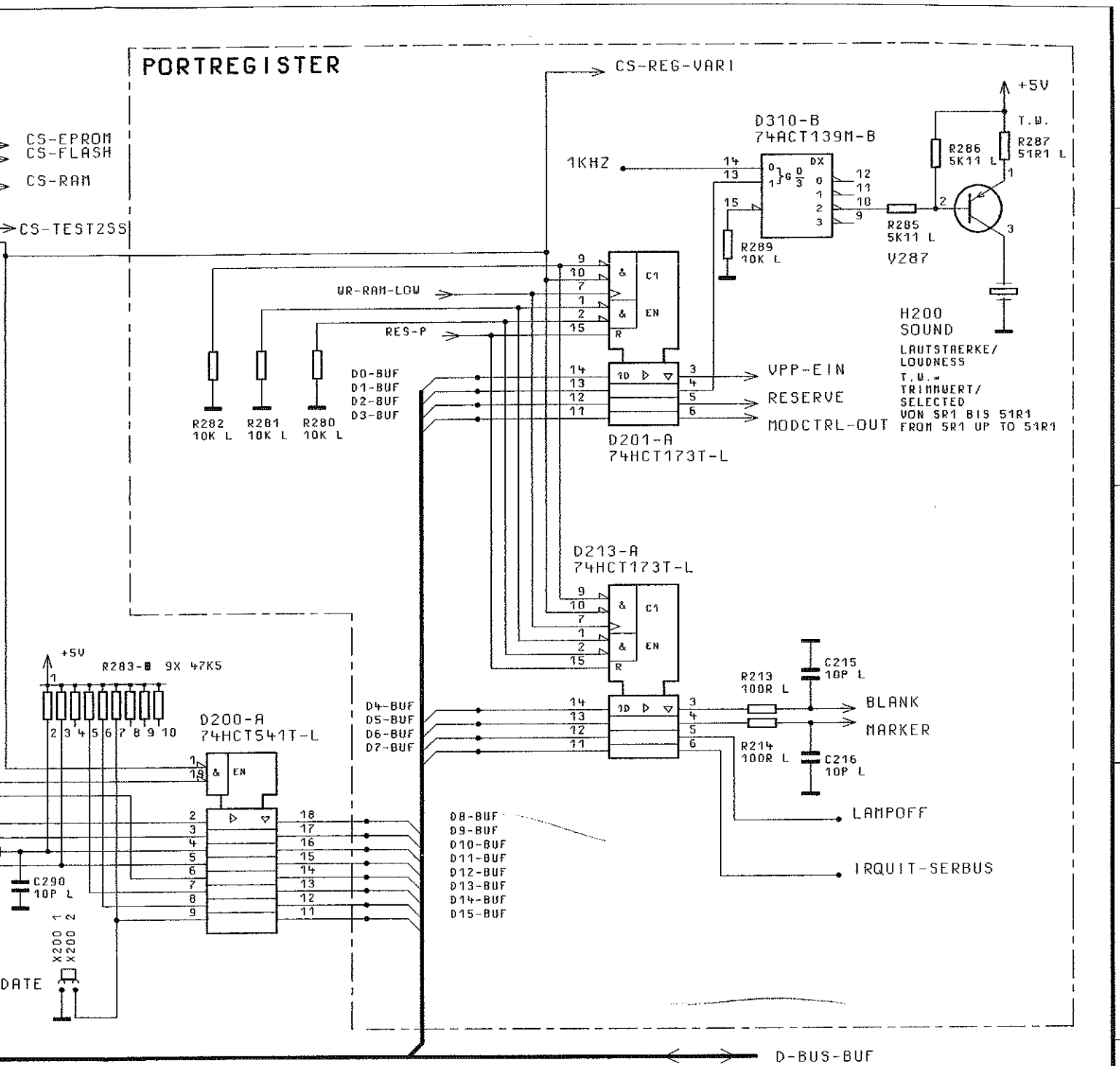








ZEICHN NR.



BINDENDE ANGABEN UEBER VARIANTEN, TRIMMWERTE, BAUTEILWERTE UND NICHT BESTUECKTE BAUTEILE SIEHE SA.

FOR BINDING INFORMATION ON MODELS, TRIMMING AND COMPONENTS VALUES AND NONFITTED COMPONENTS SEE PARTS LIST.

08/13		97-12-08	EI	MENP	TAG	NAME	BENENNUNG
				BEARB.		EI	RECHNER
				GEPR.			PROCESSOR
				NORM			
				PLOTT	09.12.97		
08/		13.03.95	DR	ROHDE&SCHWARZ			ZEICHN.-NR. 1035.7250.01S
AEND. IND.	AENDERUNGS-MITTEILUNG	DATUM	NAME	ZU GERAET SMP			BLATT-NR. 3+
				REG.I.V. 1035.5005		ERSTE Z. 1035.5005	V.B. OBL.

SB!
FAEHRDETE
ERN EINE
ABUNG.
SD!
IVE DEVICES
HANDLING

PORTREGISTER

+5V

D214-A
74ACT138M-B
R209
1K B

A27
A28
A29
A30
A31

CS-EPROM
CS-FLASH
CS-RAM
CS-TEST2SS

TP7

1KHZ

WR-RAM-LOW
RES-P

R282 10K L
R281 10K L
R280 10K L

D0-BUF
D1-BUF
D2-BUF
D3-BUF

D21
74H

R220-B
47K-9XC

D15-BUF
D14-BUF
D13-BUF
D12-BUF
D11-BUF
D10-BUF
D9-BUF
D8-BUF

R221-B
47K-9XC

D7-BUF
D6-BUF
D5-BUF
D4-BUF
D3-BUF
D2-BUF
D1-BUF
D0-BUF

+5V
R283-B 9X 47K5

D200-A
74HCT541T-L

D4-BUF
D5-BUF
D6-BUF
D7-BUF

RD-MEM1
DIR-FF
KEY-INT-P
KNOB-INT-P
SWEEP-STOP
MODCTRL-IN

R290
1K L

C290
10P L

X200 1
X200 2

UPDATE

D8-BUF
D9-BUF
D10-BUF
D11-BUF
D12-BUF
D13-BUF
D14-BUF
D15-BUF

BINDENDE ANGABEN UEBER VARIANTEN,
TRIMMWERTE, BAUTEILWERTE UND
NICHT BESTUECKTE BAUTEILE SIEHE SA.

FOR BINDING I
TRIMMING AND
NONFITTED COM

C212
100N B

C214
100N L

C213
100N L

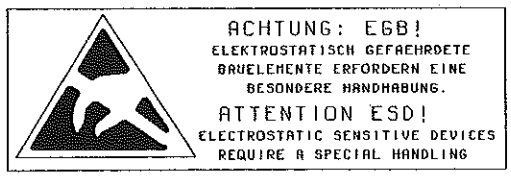
D214-B
74ACT138M-B

D216-B
74ACT573M-L

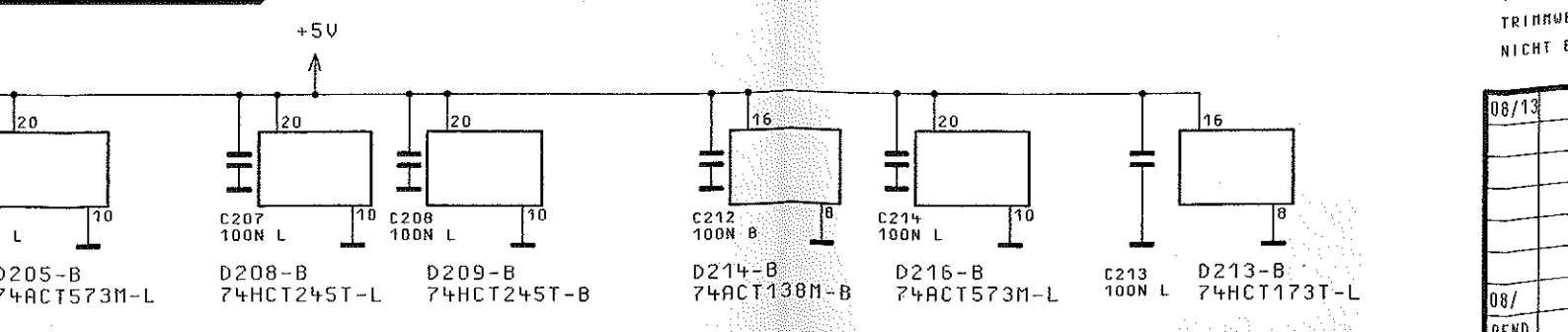
D213-B
74HCT173T-L

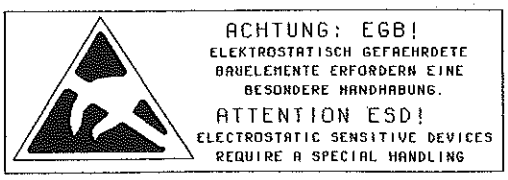
08/13		97-12-08	EI	MENP	TAG	NAME
				BEARB.		EI
				GEPR.		
				NORM		
				PLOTT	09.12.97	
08/		13.03.95	DR			
AEND. IND.	AENDERUNGS-MITTEILUNG	DATUM	NAME			

ROHDE&SCHWARZ
ZU GERAET SMP


ACHTUNG: EGB!
ELEKTROSTATISCH GEFAEHRDETE
BAUELEMENTE ERFORDERN EINE
BESONDERE HANDHABUNG.
ATTENTION ESD!
ELECTROSTATIC SENSITIVE DEVICES
REQUIRE A SPECIAL HANDLING

+5V
D214-A
74ACT138M-B
R209
1K B
A27
A28
A29
A30
A31
CS-EPROM
CS-FLASH
CS-RAM
CS-TEST2SS
TP7
D208-A
74HCT245T-L
R220-B
47K-9XC
DEN
DT/R
AD15
AD14
AD13
AD12
AD11
AD10
AD9
AD8
D15-BUF
D14-BUF
D13-BUF
D12-BUF
D11-BUF
D10-BUF
D9-BUF
D8-BUF
D209-A
74HCT245T-B
R221-B
47K-9XC
AD7
AD6
AD5
AD4
AD3
AD2
AD1
D0
D7-BUF
D6-BUF
D5-BUF
D4-BUF
D3-BUF
D2-BUF
D1-BUF
D0-BUF
R283-B
RD-MEM1
DIR-FF
KEY-INT-P
KNOB-INT-P
SWEEP-STOP
MODCTRL-IN
R290
1K L
C290
10P L
X200 1
X200 2
UPDATE


+5V
C205-B
74ACT573M-L
C207
100N L
C208
100N L
D208-B
74HCT245T-L
D209-B
74HCT245T-B
C212
100N B
C214
100N L
D214-B
74ACT138M-B
D216-B
74ACT573M-L
C213
100N L
D213-B
74HCT173T-L



D204-A
74ACT573M-L

A15-PERI
A14-PERI
A13-PERI
A12-PERI
A11-PERI
A10-PERI
A9-PERI
A8-PERI

D216-A
74ACT573M-L

A15-MEM
A14-MEM
A13-MEM
A12-MEM
A11-MEM
A10-MEM
A9-MEM
A8-MEM

D205-A
74ACT573M-L

A7-PERI TP8
A6-PERI TP9
A5-PERI TP10
A4-PERI TP11
A7-MEM
A6-MEM
A5-MEM
A4-MEM

D202-A
74FCT244-L

A3-PERI
A2-PERI
A1-PERI
BE0-PERI

D202-B
74FCT244-L

BE1-PERI
AS-PERI
W/R-PERI
BLAST-PERI

D208-A
74HCT245T-L

+5V
R220-B
47K-9XC
DEN
DT/R
AD15
AD14
AD13
AD12
AD11
AD10
AD9
AD8

D209-A
74HCT245T-B

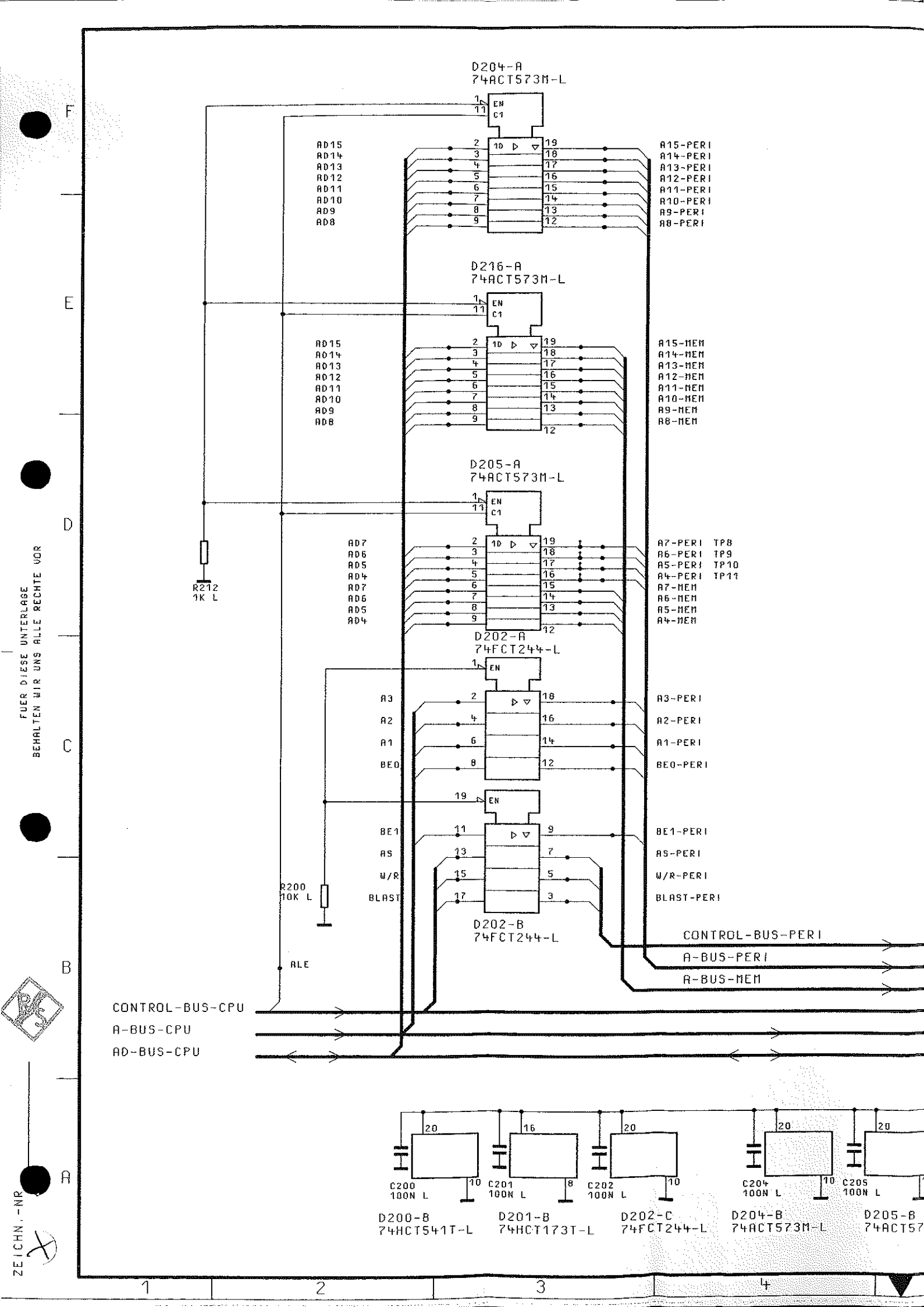
+5V
R221-B
47K-9XC
DEN
DT/R
AD7
AD6
AD5
AD4
AD3
AD2
AD1
D0

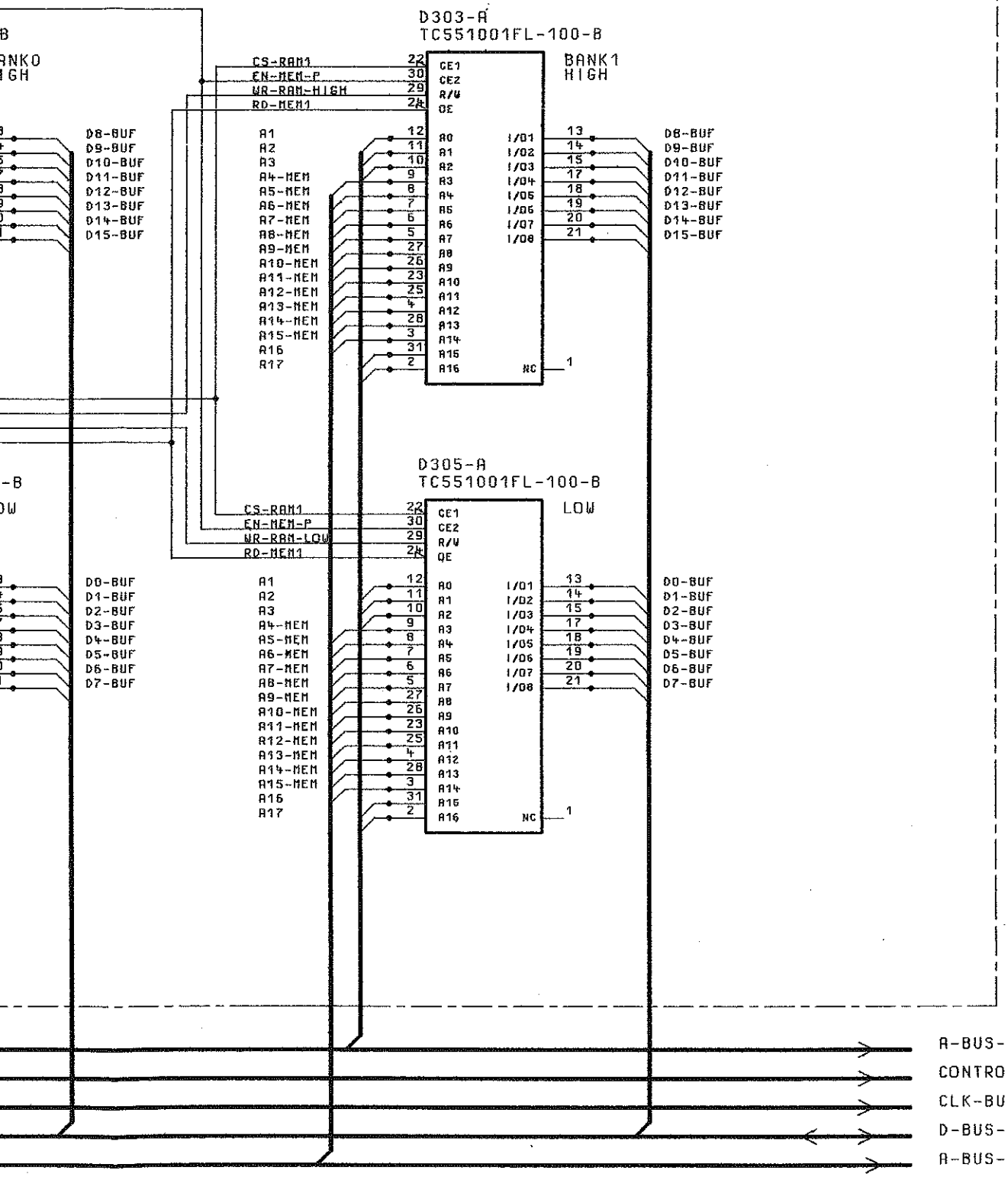
CONTROL-BUS-PERI
A-BUS-PERI
A-BUS-MEM

+5V

C201 100N L
C202 100N L
C204 100N L
C205 100N L
C207 100N L
C208 100N L
C212 100N B

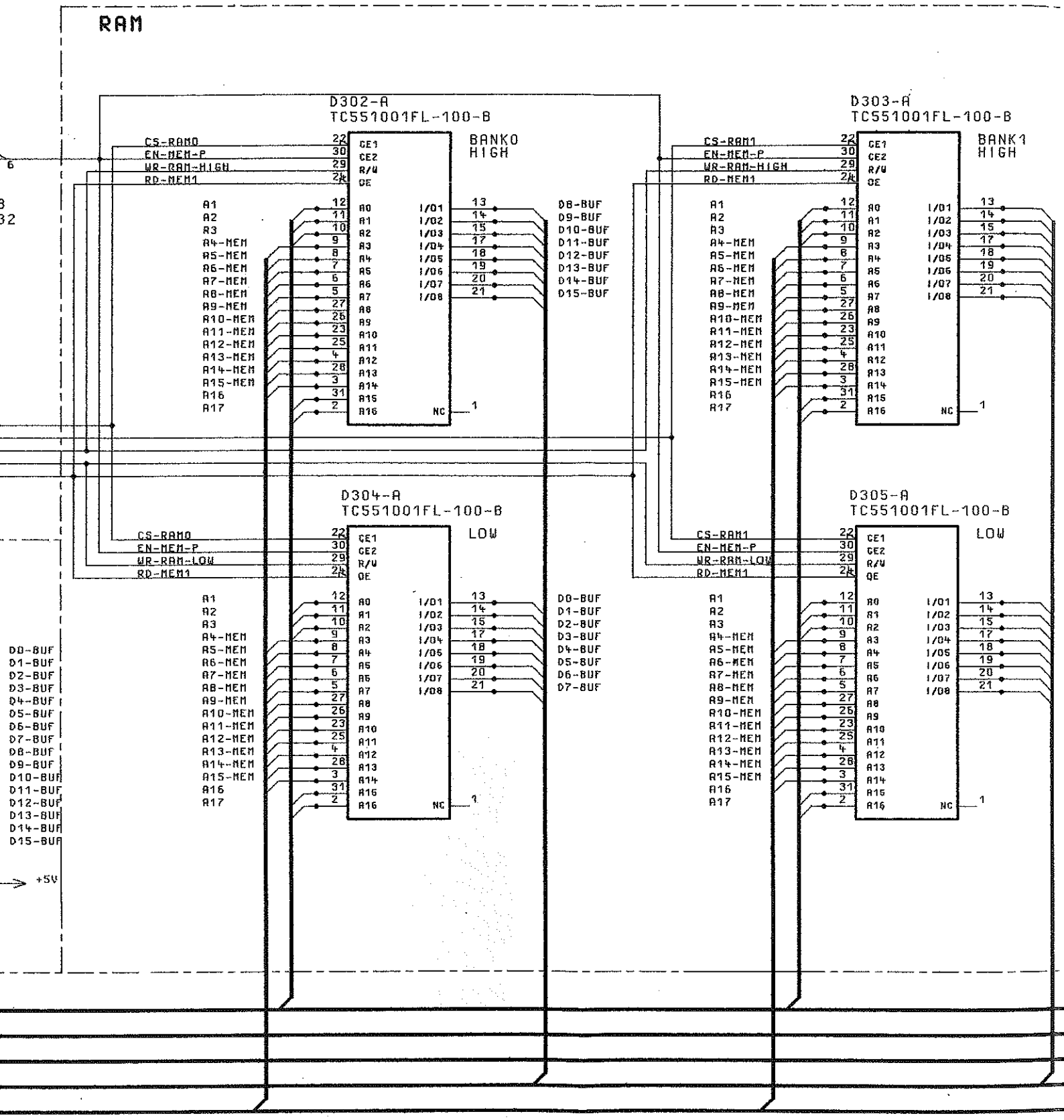
D201-B 74HCT173T-L
D202-C 74FCT244-L
D204-B 74ACT573M-L
D205-B 74ACT573M-L
D208-B 74HCT245T-L
D209-B 74HCT245T-B
D214-B 74ACT138M-

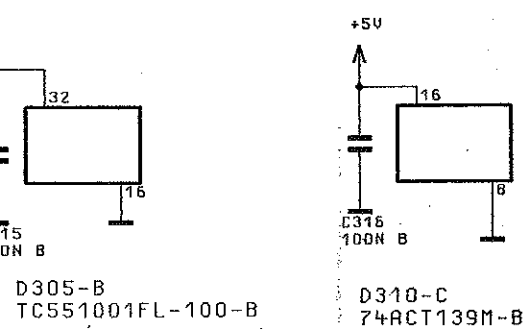

D204-A
74ACT573M-L
EN
C1
1D
AD15
AD14
AD13
AD12
AD11
AD10
AD9
AD8
A15-PERI
A14-PERI
A13-PERI
A12-PERI
A11-PERI
A10-PERI
A9-PERI
A8-PERI
D216-A
74ACT573M-L
A15-MEM
A14-MEM
A13-MEM
A12-MEM
A11-MEM
A10-MEM
A9-MEM
A8-MEM
D205-A
74ACT573M-L
AD7
AD6
AD5
AD4
A7-PERI
A6-PERI
A5-PERI
A4-PERI
TP8
TP9
TP10
TP11
A7-MEM
A6-MEM
A5-MEM
A4-MEM
R212
1K L
D202-A
74FCT244-L
A3
A2
A1
BE0
A3-PERI
A2-PERI
A1-PERI
BE0-PERI
BE1
AS
W/R
BLAST
BE1-PERI
AS-PERI
W/R-PERI
BLAST-PERI
R200
10K L
D202-B
74FCT244-L
ALE
CONTROL-BUS-PERI
A-BUS-PERI
A-BUS-MEM
CONTROL-BUS-CPU
A-BUS-CPU
AD-BUS-CPU
C200
100N L
C201
100N L
C202
100N L
C204
100N L
C205
100N L
D200-B
74HCT541T-L
D201-B
74HCT173T-L
D202-C
74FCT244-L
D204-B
74ACT573M-L
D205-B
74ACT57
FUER DIESE UNTERLAGE
BEHALTEN WIR UNS ALLE RECHTE VOR
ZEICHN.-NR
F
E
D
C
B
A
1
2
3
4



05/		19.04.94	DR	1GPK	TAG	NAME	BENENNUNG
				BEARB.		JN	RECHNER
				GEPR.			PROCESSOR
				NORM			
				PLOTT	03.05.94		
/				ROHDE&SCHWARZ			ZEICHN.-NR. 1035.7250.01S
AEND. IND.	AENDERUNGS-MITTEILUNG	DATUM	NAME	ZU GERAET	SMP		BLATT-NR. 4+
				REG.I.V.	1035.5005	ERSTE Z.	1035.5005

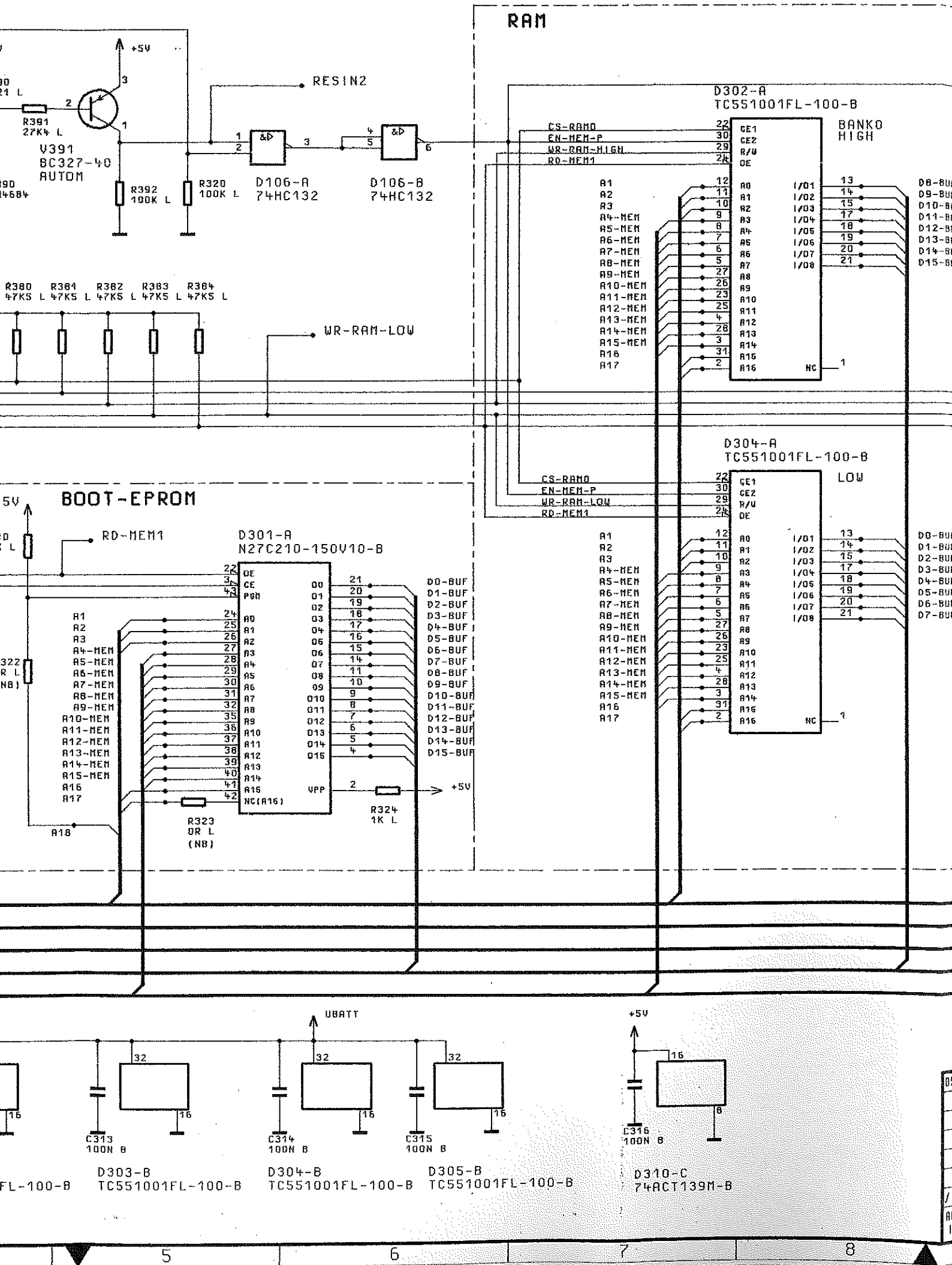
RAM

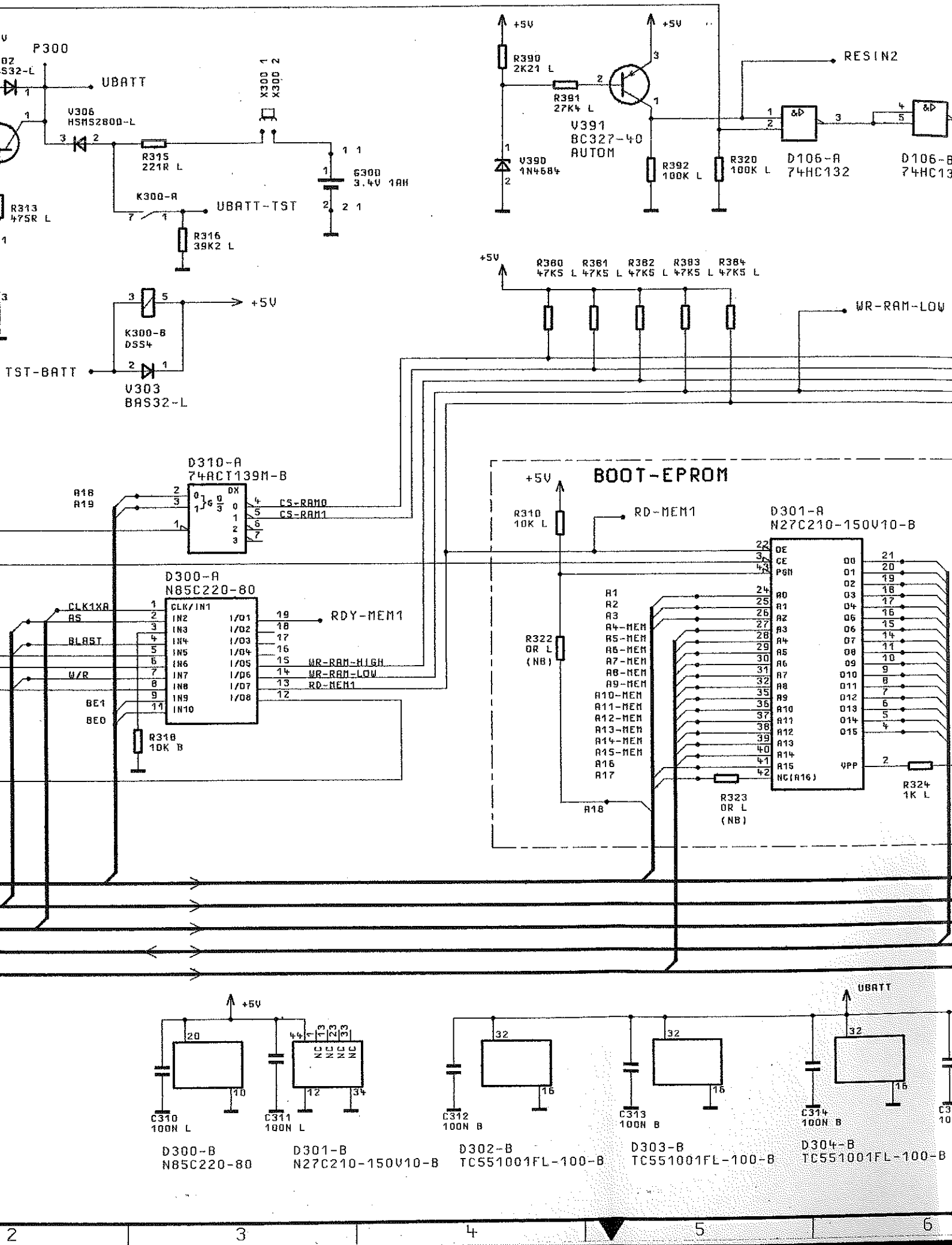


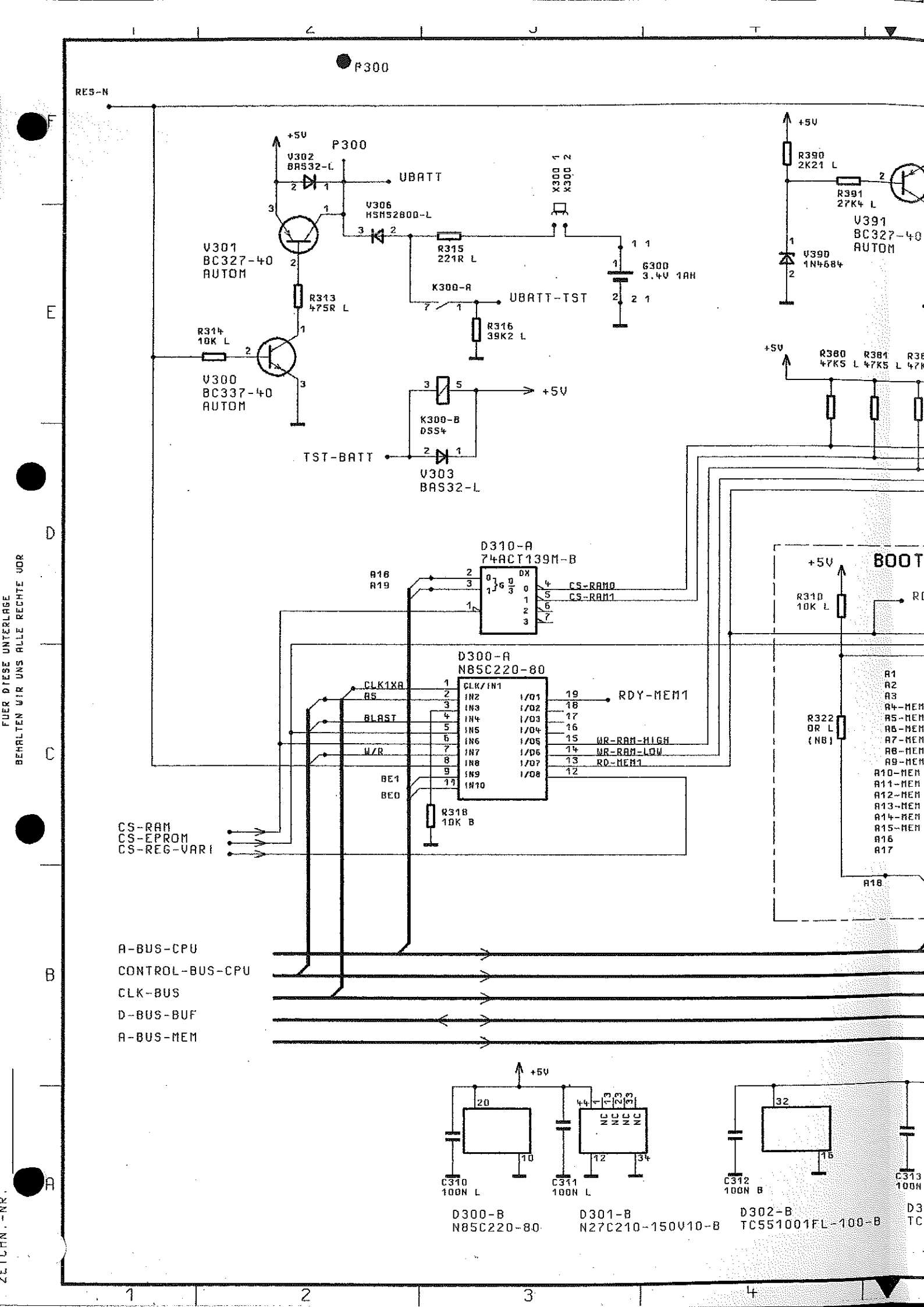


05/		19.04.94	DR	1GPK	TAG	NAME
				BEARB.		JN
				GEPR.		
				NORM		
				PLOTT	03.05.94	
/						
ÄEND. IND.	ÄENDERUNGS-MITTEILUNG	DATUM	NAME	ZU GERÄET	SMP	

ROHDE&SCHWARZ

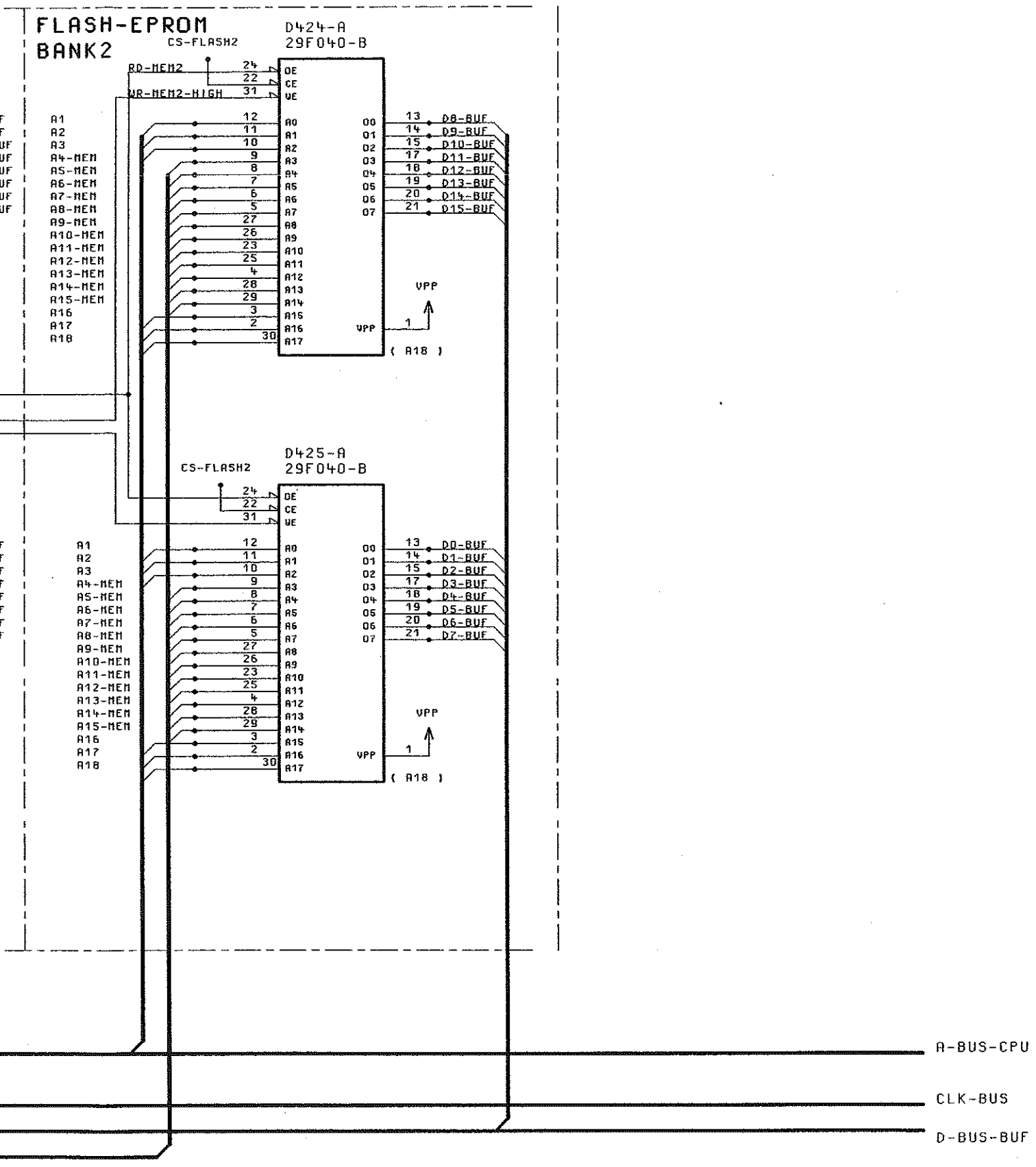

RAM
+5V
RESIN2
R391
27K4 L
V391
BC327-40
AUTOM
R392
100K L
R320
100K L
D106-A
74HC132
D106-B
74HC132
R380 R381 R382 R383 R384
47K5 L 47K5 L 47K5 L 47K5 L 47K5 L
WR-RAM-LOW
D302-A
TC551001FL-100-B
BANKO
HIGH
CS-RAM0
EN-MEM-P
WR-RAM-HIGH
RD-MEM1
D304-A
TC551001FL-100-B
LOW
WR-RAM-LOW
BOOT-EPROM
RD-MEM1
D301-A
N27C210-150V10-B
R323
0R L
(N8)
R324
1K L
A18
UBATT
C313
100N B
C314
100N B
C315
100N B
C316
100N B
D303-B
TC551001FL-100-B
D304-B
TC551001FL-100-B
D305-B
TC551001FL-100-B
D310-C
74ACT139M-B


P300
UBATT
V306
HSMS2800-L
X300 1
X300 2
R315
221R L
G300
3.4V 1AH
K300-A
UBATT-TST
R316
39K2 L
R313
475R L
K300-B
D554
+5V
TST-BATT
V303
BAS32-L
+5V
R390
2K21 L
R391
27K4 L
V391
BC327-40
AUTOM
V390
1N4684
R392
100K L
R320
100K L
RESIN2
D106-A
74HC132
D106-B
74HC13
+5V
R380 47K5 L
R381 47K5 L
R382 47K5 L
R383 47K5 L
R384 47K5 L
WR-RAM-LOW
D310-A
74ACT139M-B
A18
A19
CS-RAM0
CS-RAM1
BOOT-EPROM
+5V
R310
10K L
RD-MEM1
D301-A
N27C210-150V10-B
D300-A
N85C220-80
CLK1XA
AS
BLAST
W/R
BE1
BE0
RDY-MEM1
WR-RAM-HIGH
WR-RAM-LOW
RD-MEM1
R318
10K B
R322
OR L
(NB)
A1
A2
A3
A4-MEM
A5-MEM
A6-MEM
A7-MEM
A8-MEM
A9-MEM
A10-MEM
A11-MEM
A12-MEM
A13-MEM
A14-MEM
A15-MEM
A16
A17
A18
R323
OR L
(NB)
R324
1K L
C310
100N L
C311
100N L
C312
100N B
C313
100N B
C314
100N B
UBATT
D300-B
N85C220-80
D301-B
N27C210-150V10-B
D302-B
TC551001FL-100-B
D303-B
TC551001FL-100-B
D304-B
TC551001FL-100-B



FUER DIESE UNTERLAGE
BEHALTEN WIR UNS ALLE RECHTE VOR

ZEICHN.-NR.



06/	49155 03	07.10.94	DR	1GPK	TAG	NAME	BENENNUNG
				BEARB.		DR	RECHNER
				GEPR.			PROCESSOR
				NORM			
				PLOTT	07.10.94		
/							
AEND. IND.	AENDERUNGS-MITTEILUNG	DATUM	NAME				

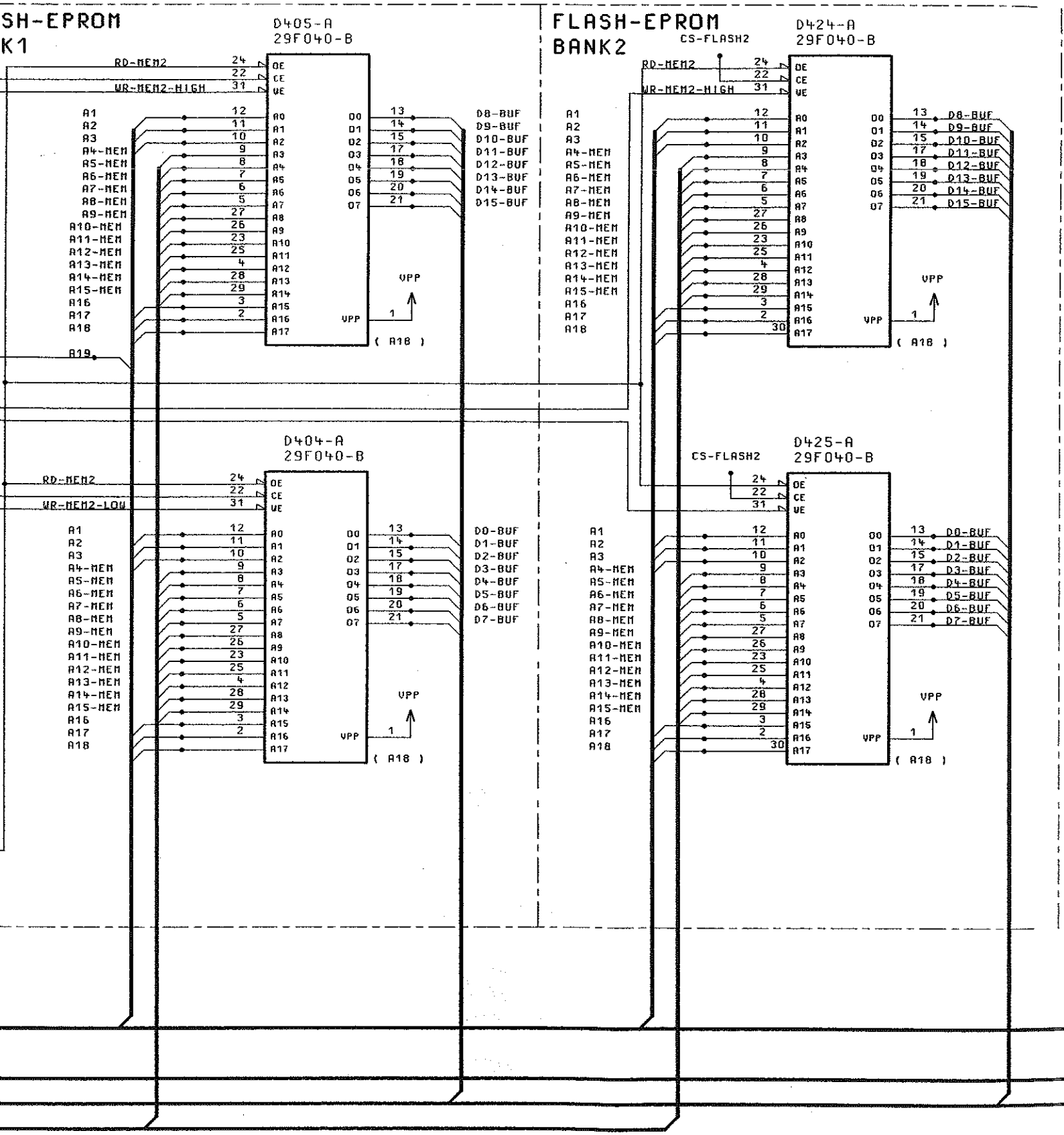
ROHDE&SCHWARZ

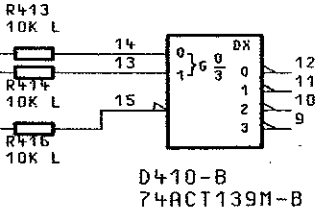
ZU GERAET SMP

ZEICHN.-NR. 1035.7250.01S

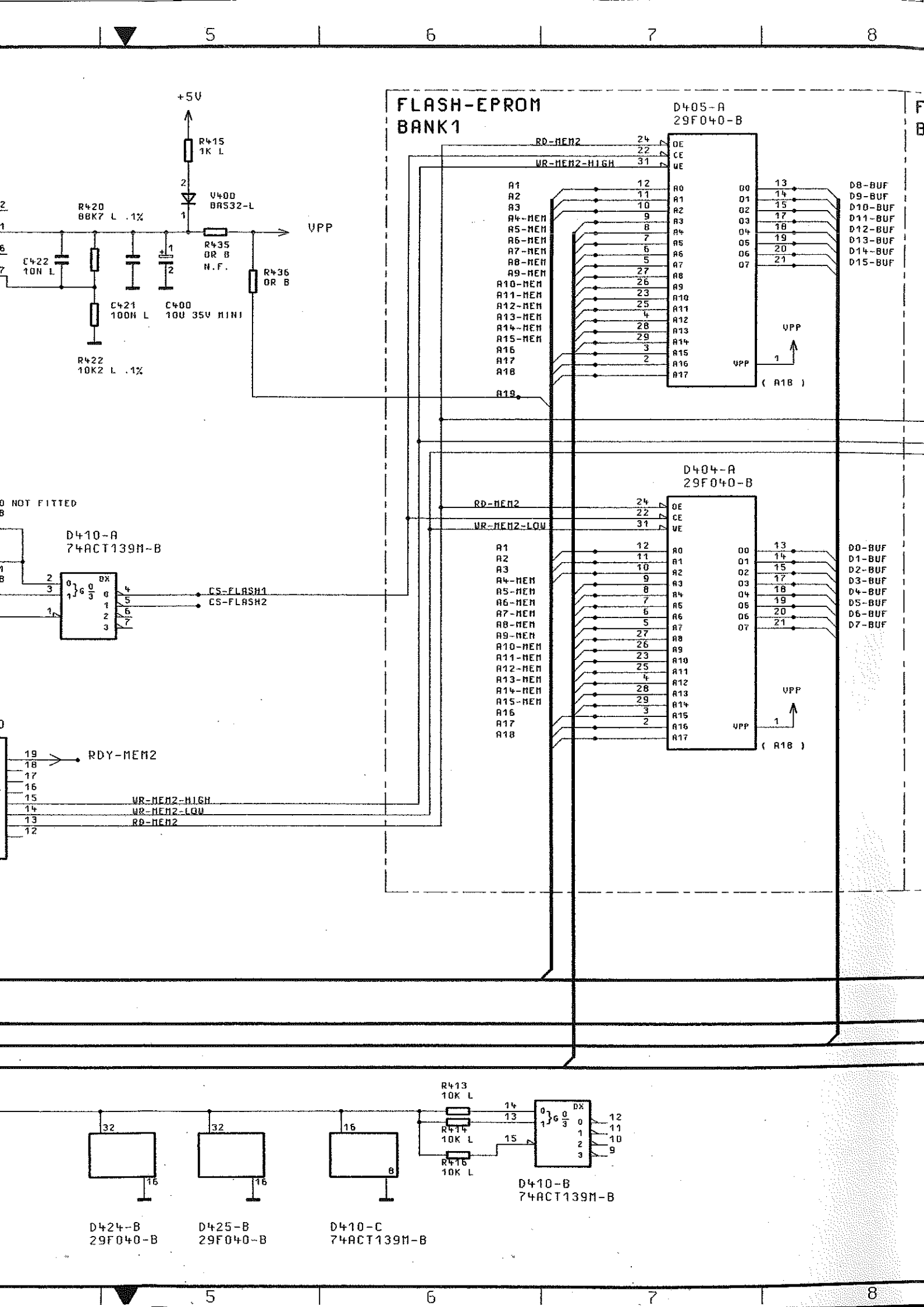
BLATT-NR. 5+ V. BL.

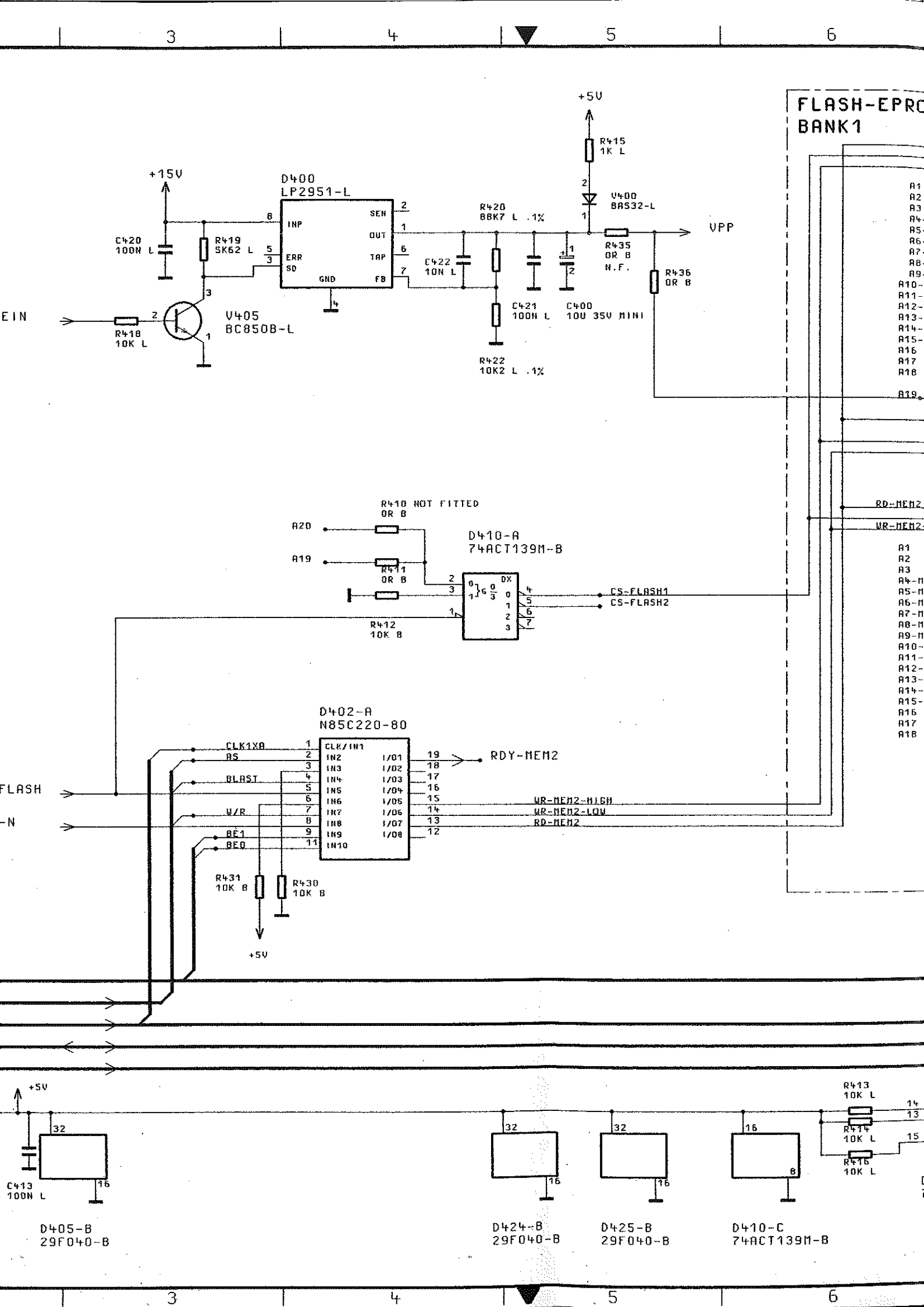
REG.I.V. 1035.5005 ERSTE Z. 1035.5005

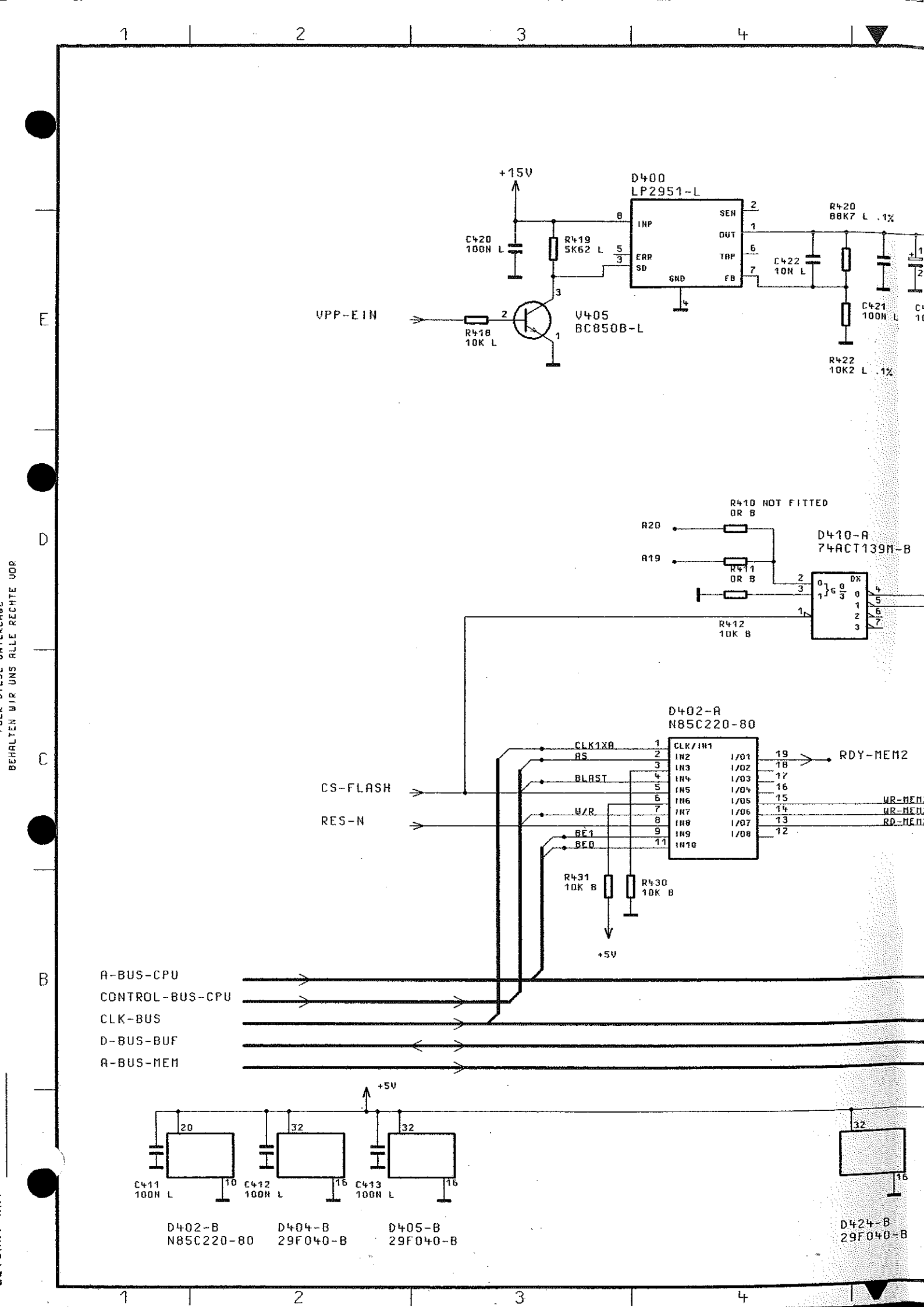




06/	49155 03	07.10.94	DR	1GPK	TAG	NAME
				BEARB.		DR
				GEPR.		
				NORM		
				PLOTT	07.10.94	
/						
REND. IND.	RENDERUNGS-MITTEILUNG	DATUM	NAME	ROHDE&SCHWARZ		
				ZU GERAET	SMP	


FLASH-EPROM
BANK1
D405-A
29F040-B
D404-A
29F040-B
+5V
R415
1K L
V400
BAS32-L
R420
88K7 L .1%
R435
0R B
N.F.
VPP
R436
0R B
C422
10N L
C421
100N L
C400
10U 35V MINI
R422
10K2 L .1%
NOT FITTED
D410-A
74ACT139M-B
CS-FLASH1
CS-FLASH2
RDY-MEM2
WR-MEM2-HIGH
WR-MEM2-LOW
RD-MEM2
A1
A2
A3
A4-MEM
A5-MEM
A6-MEM
A7-MEM
A8-MEM
A9-MEM
A10-MEM
A11-MEM
A12-MEM
A13-MEM
A14-MEM
A15-MEM
A16
A17
A18
A19
OE
CE
WE
A0
A1
A2
A3
A4
A5
A6
A7
A8
A9
A10
A11
A12
A13
A14
A15
A16
A17
VPP
(A18)
O0
O1
O2
O3
O4
O5
O6
O7
D8-BUF
D9-BUF
D10-BUF
D11-BUF
D12-BUF
D13-BUF
D14-BUF
D15-BUF
D0-BUF
D1-BUF
D2-BUF
D3-BUF
D4-BUF
D5-BUF
D6-BUF
D7-BUF
R413
10K L
R414
10K L
R416
10K L
D410-B
74ACT139M-B
D424-B
29F040-B
D425-B
29F040-B
D410-C
74ACT139M-B


FLASH-EPRO
BANK1
+5V
R415
1K L
V400
BAS32-L
+15V
D400
LP2951-L
R420
88K7 L .1%
VPP
C420
100N L
R419
5K62 L
INP
SEN
OUT
TAP
FB
ERR
SD
GND
C422
10N L
R435
0R B
N.F.
R436
0R B
C421
100N L
C400
10U 35V MINI
R422
10K2 L .1%
EIN
V405
BC850B-L
R418
10K L
R410 NOT FITTED
0R B
A20
A19
R411
0R B
D410-A
74ACT139M-B
CS-FLASH1
CS-FLASH2
R412
10K B
RD-MEM2
WR-MEM2-
D402-A
N85C220-80
CLK1XA
AS
BLAST
W/R
BE1
BE0
RDY-MEM2
WR-MEM2-HIGH
WR-MEM2-LOW
RD-MEM2
FLASH
R431
10K B
R430
10K B
+5V
C413
100N L
D405-B
29F040-B
D424-B
29F040-B
D425-B
29F040-B
D410-C
74ACT139M-B
R413
10K L
R414
10K L
R416
10K L



DOOR DIESE UNTERLAGE
BEHALTEN WIR UNS ALLE RECHTE VOR

CS-REG3

WR-REG1-HIGH

CS-REG1

CS-REG2

WR-REG2-HIGH

WR-REG4-LOW

RD-PERI1

VAR	R592	R593	R594
04		N.F.	

D590-A
74HCT541T-L

R590-H
R590-I

1 1G
19 & EN

2 18 D7-BUF
3 17 D6-BUF
4 16 D5-BUF
5 15 D4-BUF
6 14 D3-BUF
7 13 D2-BUF
8 12 D1-BUF
9 11 D0-BUF

8 9 10

R598
1K L

AEZ	R595	R596	R597	R598
01				N.F.
02			N.F.	
03			N.F.	N.F.
04		N.F.		
05		N.F.		N.F.
06		N.F.	N.F.	
07		N.F.	N.F.	N.F.
08	N.F.			
⋮				
15	N.F.	N.F.	N.F.	N.F.

A-BUS-CPU

CONTROL-BUS-PERI

A-BUS-PERI

D-BUS-BUF

CLK-BUS

IND.	ÄNDERUNGS-MITTEILUNG	DATUM	NAME
08/		13.03.95	DR
/			

	TAG	NAME
ZGPK		
BEARB.		DR
GEPR.		
NORM		
PLOTT	13.03.95	

ROHDE&SCHWARZ

ZU GERÄET SMP

BENENNUNG

RECHNER
PROCESSOR

ZEICHN.-NR. 1035.7250.01S

BLATT-NR. 6+

REG.I.V. 1035.5005 ERSTE Z. 1035.5005

CS-

D520-A
74ACT32M-L
≥1
3
WR-

CS-

CS-

D520-B
74ACT32M-L
≥1
6
WR-

D520-C
74ACT32M-L
≥1
8
WR-

RD-

+5V
R590-A
47K-9XC
R590-B
R590-C
R590-D
R590-E
R590-F
R590-G
R590-H
R590-I
1
2
3
4
5
6
7
8
9
10

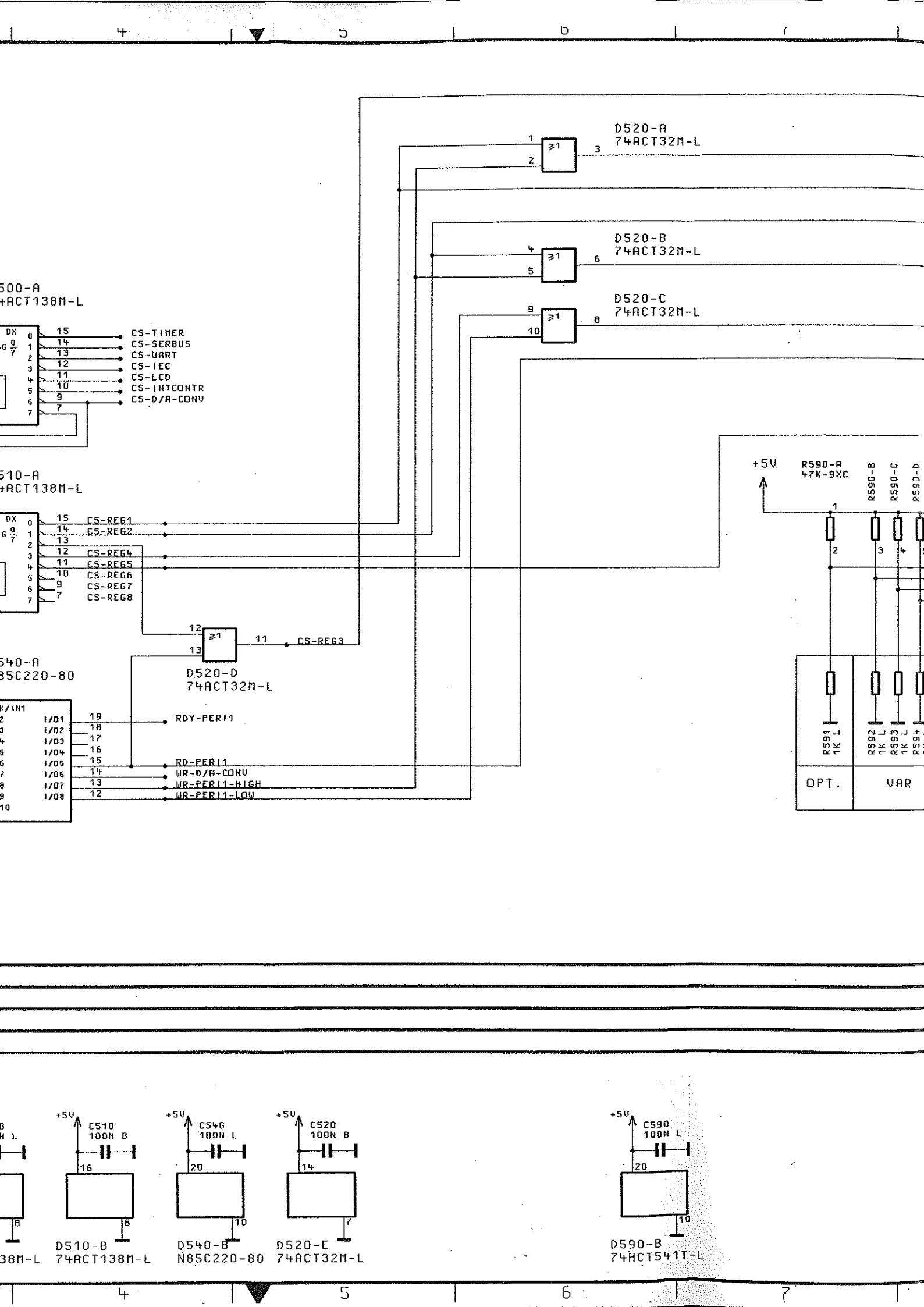
D590-A
74HCT541T-L
1
19
&
EN
2
3
4
5
6
7
8
9
18 D7-BUF
17 D6-BUF
16 D5-BUF
15 D4-BUF
14 D3-BUF
13 D2-BUF
12 D1-BUF
11 D0-BUF

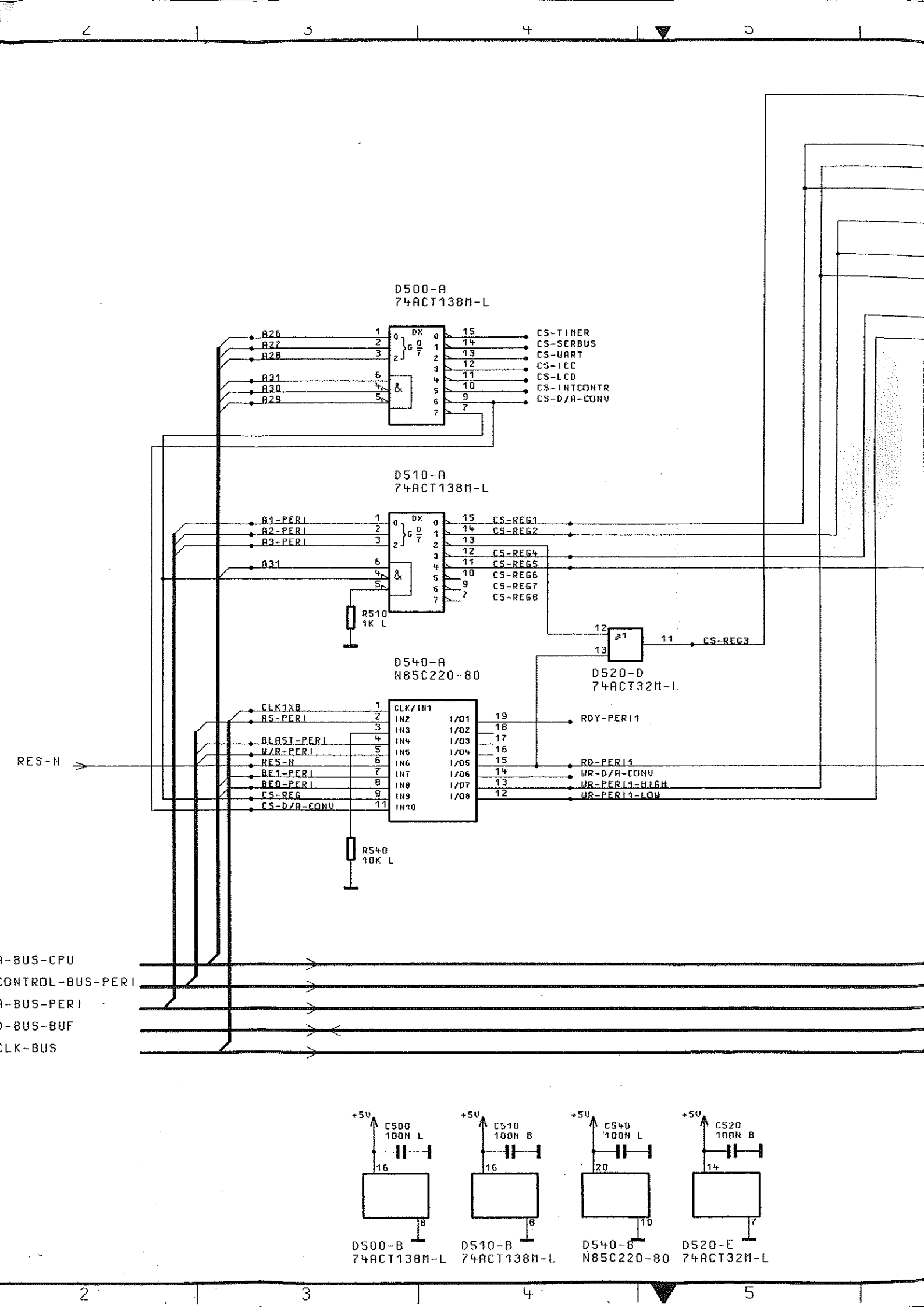
R591 1K L
R592 1K L
R593 1K L
R594 1K L
R595 1K L
R596 1K L
R597 1K L
R598 1K L

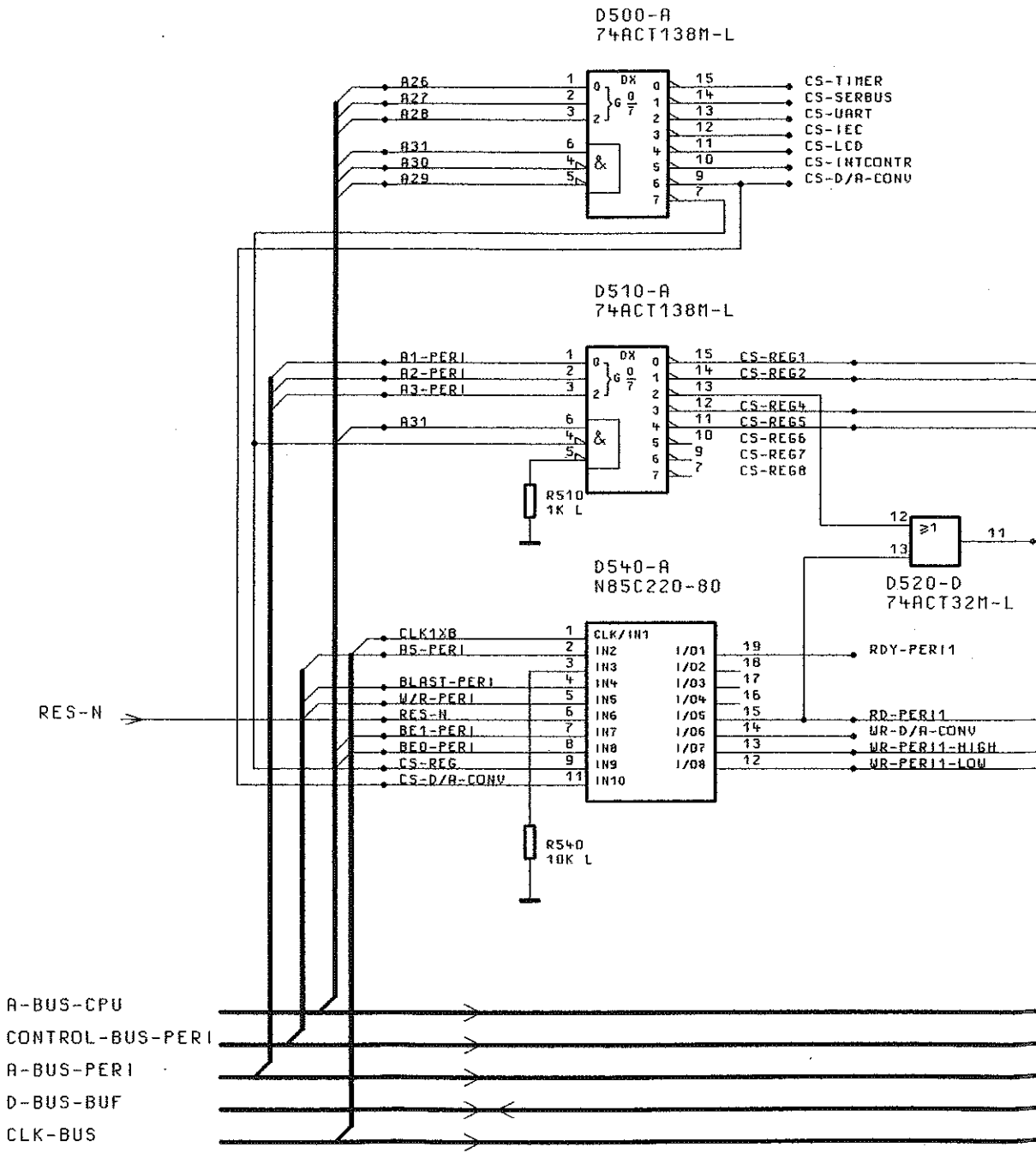
OPT.
VAR
AEZ

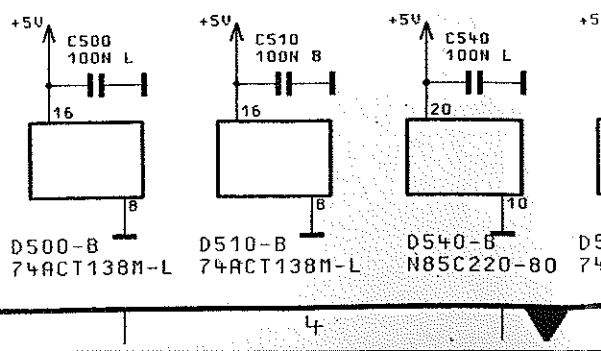
+5V
C590
100N L
20
10
D590-B
74HCT541T-L

08/		13.03.95	DR	1GPK	TAG
				BEARB.	
				GEPR.	
				NORM	
				PLOTT	13.03.95
/					
AEND. IND.	AENDERUNGS-MITTEILUNG	DATUM	NAME	ROHDE&SCH	
				ZU GERAET	SMP


D520-A
74ACT32M-L
D520-B
74ACT32M-L
D520-C
74ACT32M-L
D520-D
74ACT32M-L
D520-E
74ACT32M-L
CS-TIMER
CS-SERBUS
CS-UART
CS-IEC
CS-LCD
CS-INTCONTR
CS-D/A-CONV
CS-REG1
CS-REG2
CS-REG4
CS-REG5
CS-REG6
CS-REG7
CS-REG8
CS-REG3
RDY-PERI1
RD-PERI1
WR-D/A-CONV
WR-PERI1-HIGH
WR-PERI1-LOW
+5V
R590-A
47K-9XC
R590-B
R590-C
R590-D
R591
1K L
R592
1K L
R593
1K L
OPT.
VAR
C510
100N B
C540
100N L
C520
100N B
C590
100N L
D510-B
74ACT138M-L
D540-B
N85C220-80
D520-E
74ACT32M-L
D590-B
74HCT541T-L

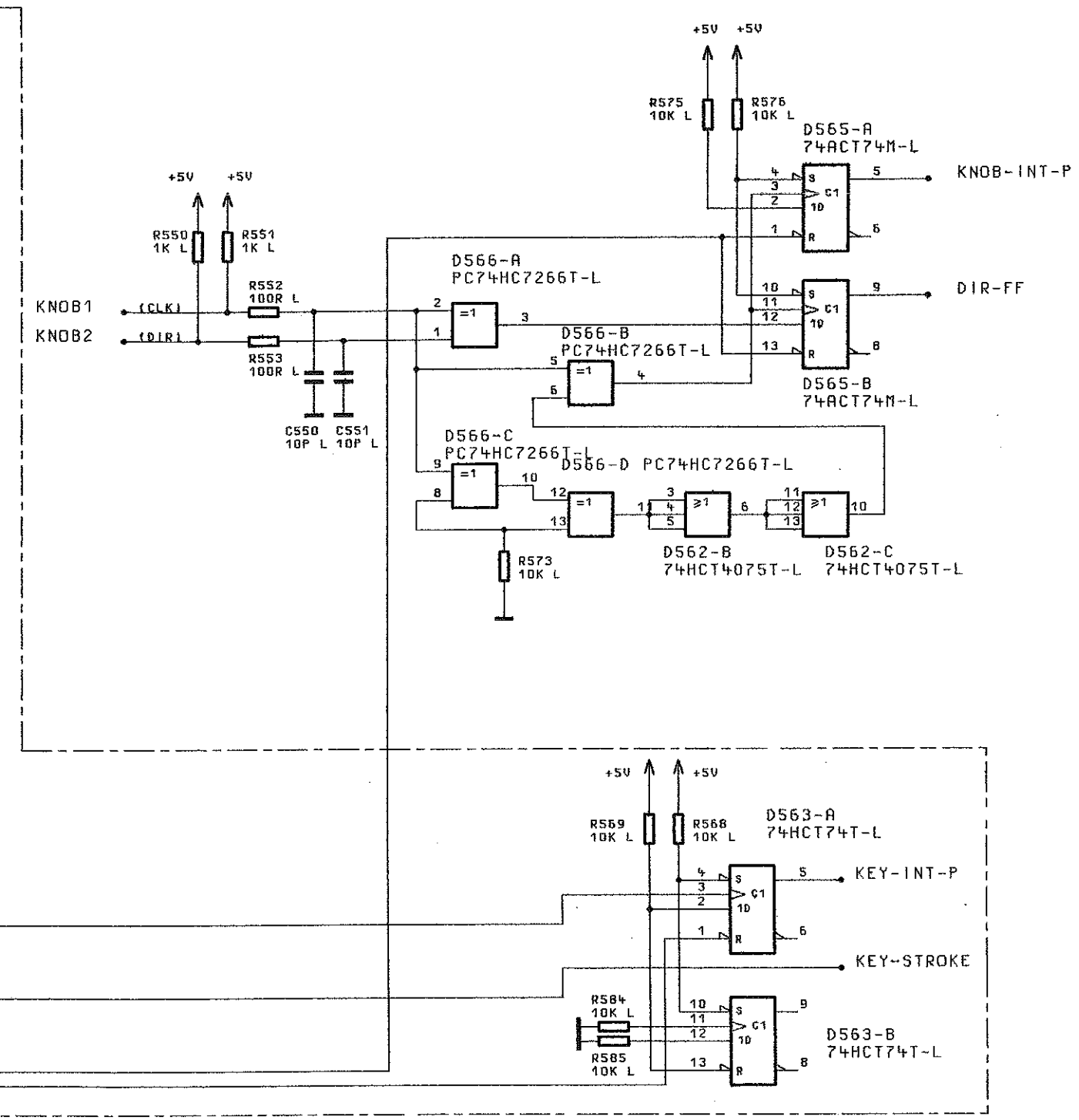

D500-A
74ACT138M-L
A26
A27
A28
A31
A30
A29
CS-TIMER
CS-SERBUS
CS-UART
CS-IEC
CS-LCD
CS-INTCONTR
CS-D/A-CONV
D510-A
74ACT138M-L
A1-PERI
A2-PERI
A3-PERI
A31
R510
1K L
CS-REG1
CS-REG2
CS-REG4
CS-REG5
CS-REG6
CS-REG7
CS-REG8
D520-D
74ACT32M-L
CS-REG3
D540-A
N85C220-80
CLK1XB
AS-PERI
BLAST-PERI
W/R-PERI
RES-N
BE1-PERI
BE0-PERI
CS-REG
CS-D/A-CONV
R540
10K L
RDY-PERI1
RD-PERI1
WR-D/A-CONV
WR-PERI1-HIGH
WR-PERI1-LOW
RES-N
A-BUS-CPU
CONTROL-BUS-PERI
A-BUS-PERI
D-BUS-BUF
CLK-BUS
+5V
C500
100N L
C510
100N B
C540
100N L
C520
100N B
D500-B
74ACT138M-L
D510-B
74ACT138M-L
D540-B
N85C220-80
D520-E
74ACT32M-L



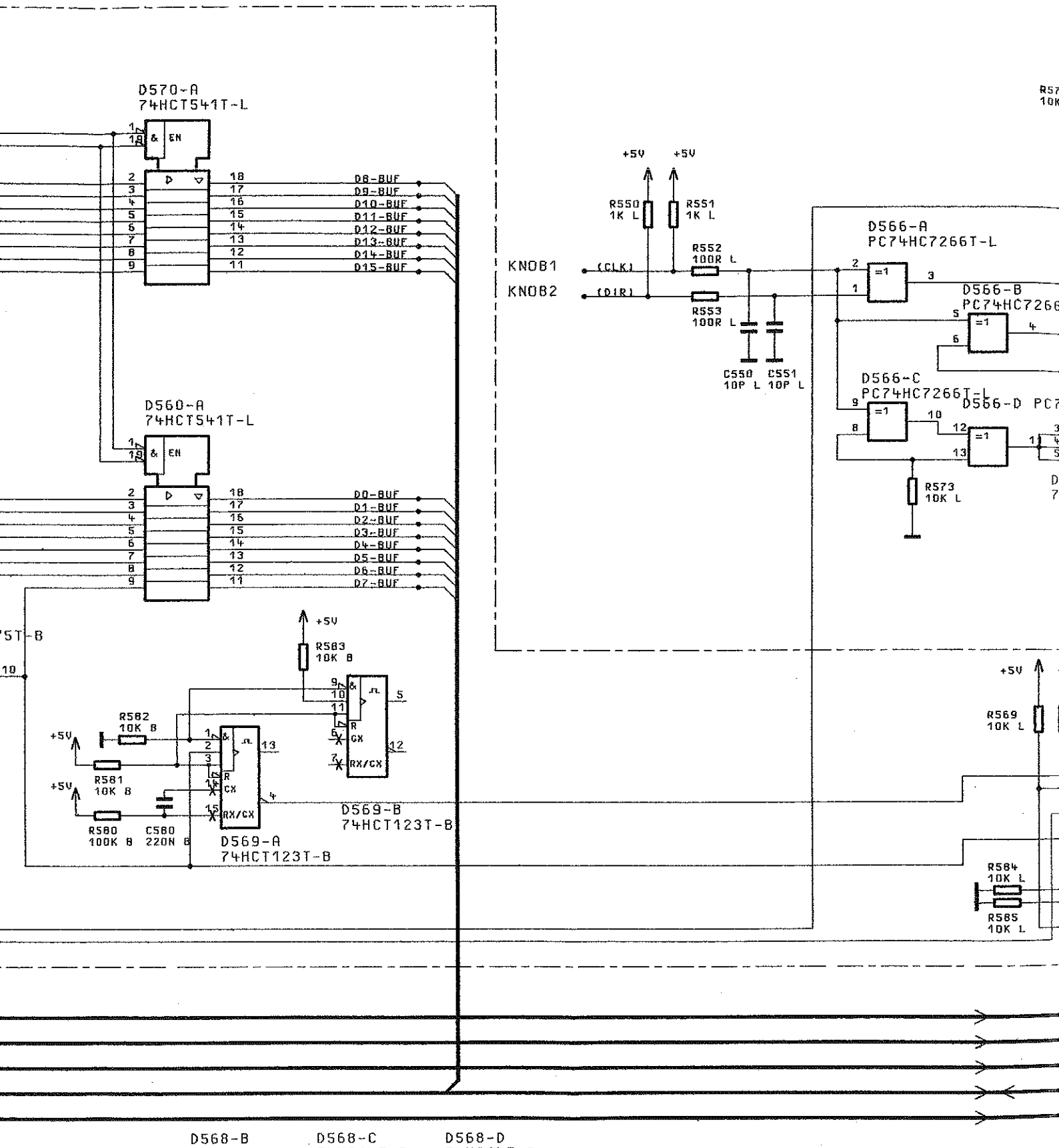


FUER DIESE UNTERLAGE
BEHALTEN WIR UNS ALLE RECHTE VOR

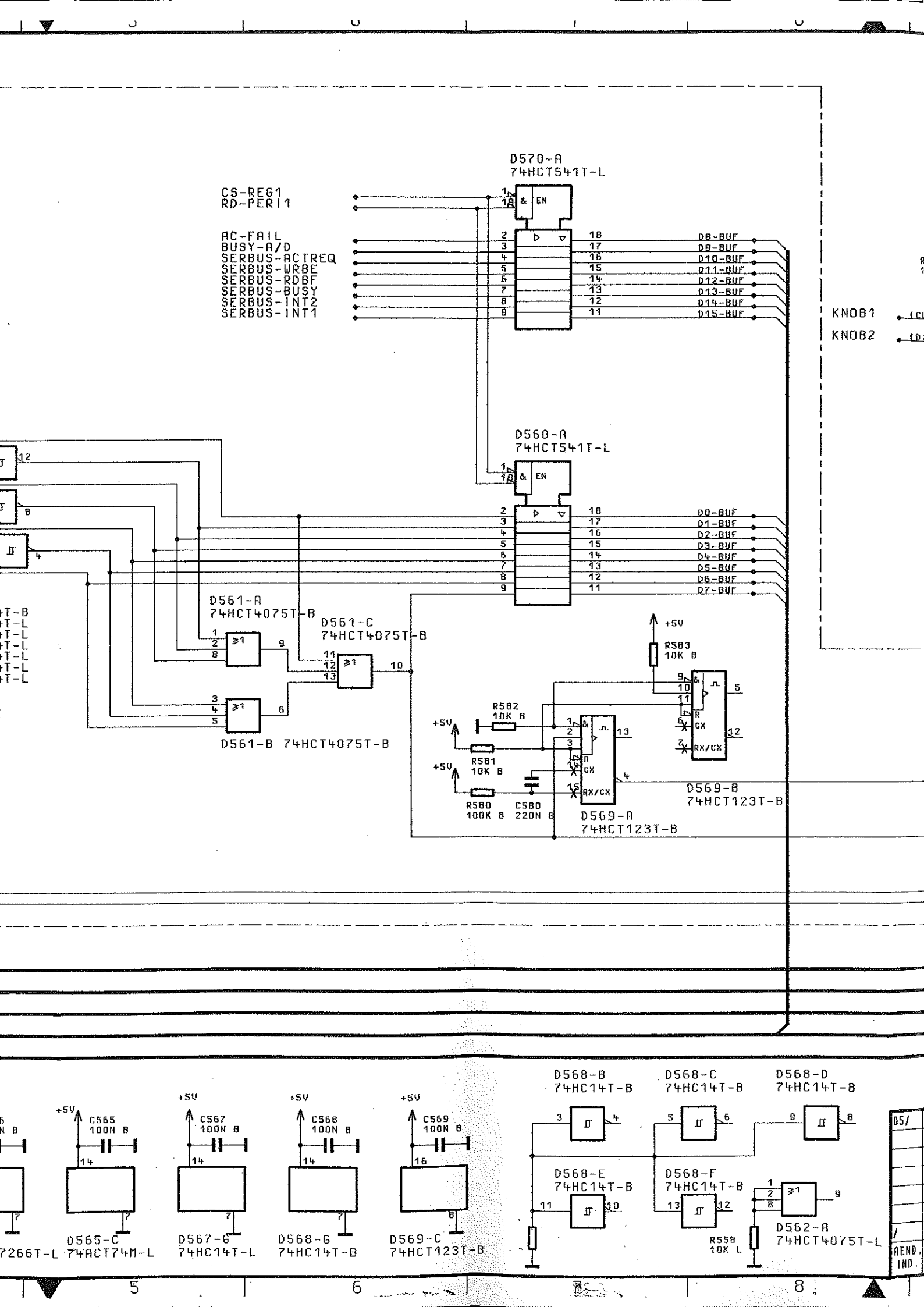
ZEICHN.-NR.



05/		19.04.94	DR	1GPK	TAG	NAME	BENENNUNG
				BEARB.		JN	RECHNER
				GEPR.			PROCESSOR
				NORM			
				PLOTT	03.05.94		
/							ZEICHN.-NR. 1035.7250.01S
AEND. IND.	AENDERUNGS-MITTEILUNG	DATUM	NAME	ROHDE&SCHWARZ			BLATT-NR. 7+
				ZU GERAET SMP			REG.I.V. 1035.5005 ERSTE Z. 1035.5005



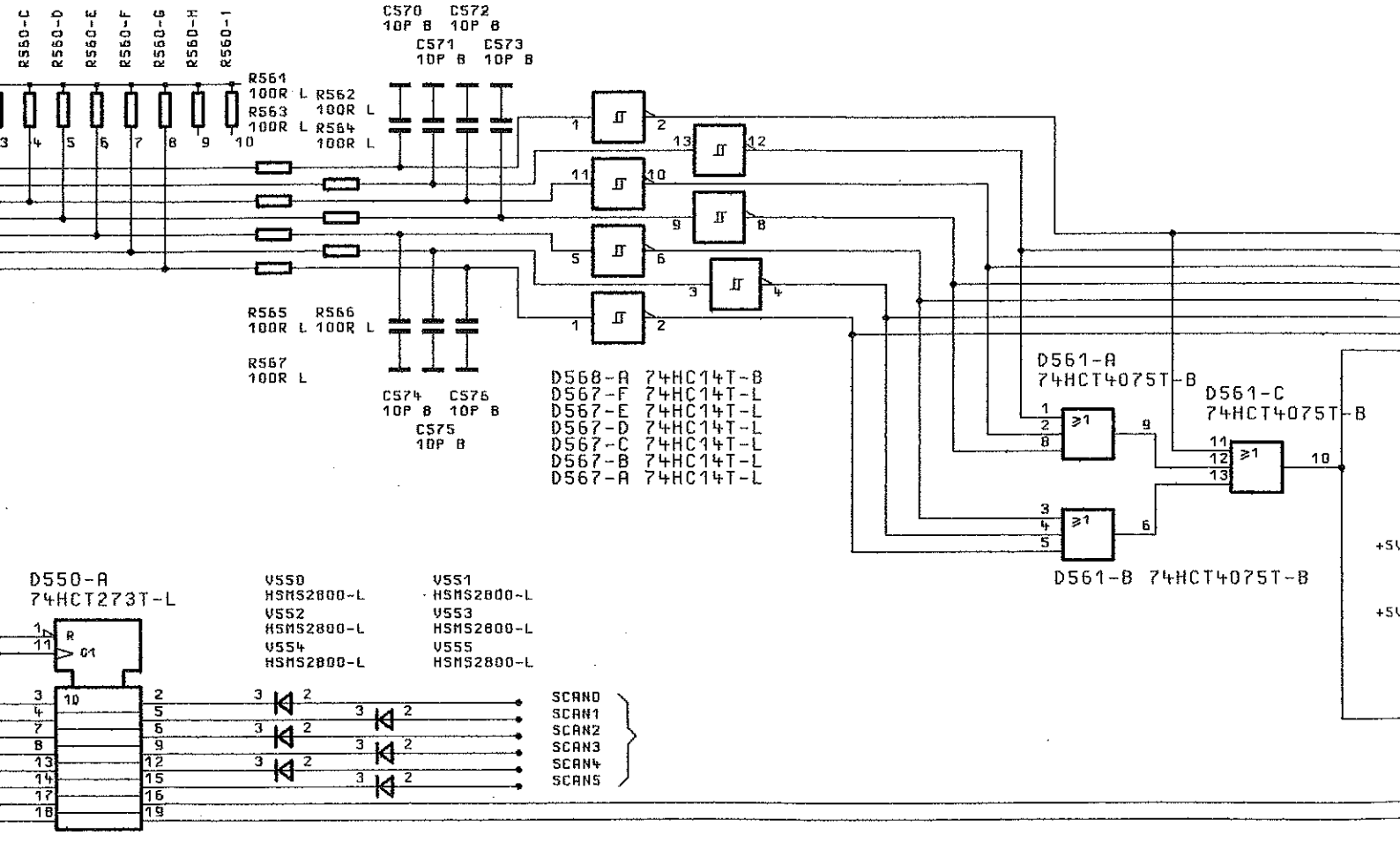
05/		19.04.94	DR	1GPK	TAG	NAME
				BEARB.		JN
				GEPR.		
				NORM		
				PLOTT	03.05.94	
/						
AEND. IND.	AENDERUNGS-MITTEILUNG	DATUM	NAME	ROHDE&SCHWARZ		
				ZU GERAET	SMP	

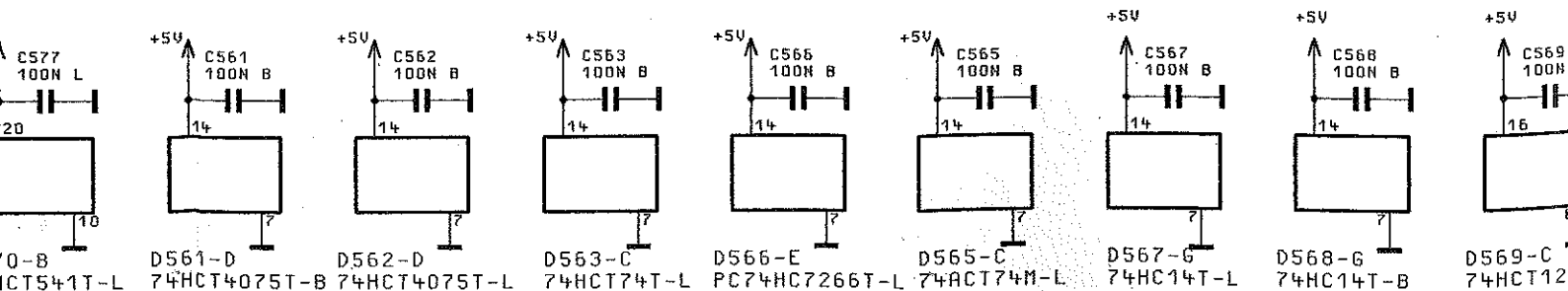

D570-A
74HCT541T-L
CS-REG1
RD-PERI1
AC-FAIL
BUSY-A/D
SERBUS-ACTREQ
SERBUS-WRBE
SERBUS-RDBF
SERBUS-BUSY
SERBUS-INT2
SERBUS-INT1
D8-BUF
D9-BUF
D10-BUF
D11-BUF
D12-BUF
D13-BUF
D14-BUF
D15-BUF
KNOB1
KNOB2
D560-A
74HCT541T-L
D0-BUF
D1-BUF
D2-BUF
D3-BUF
D4-BUF
D5-BUF
D6-BUF
D7-BUF
D561-A
74HCT4075T-B
D561-C
74HCT4075T-B
D561-B 74HCT4075T-B
+5V
R583
10K B
R582
10K B
R581
10K B
R580
100K B
C580
220N B
D569-A
74HCT123T-B
D569-B
74HCT123T-B
C565
100N B
C567
100N B
C568
100N B
C569
100N B
D565-C
74ACT74M-L
D567-G
74HC14T-L
D568-G
74HC14T-B
D569-C
74HCT123T-B
D568-B
74HC14T-B
D568-C
74HC14T-B
D568-D
74HC14T-B
D568-E
74HC14T-B
D568-F
74HC14T-B
R558
10K L
D562-A
74HCT4075T-L
AEND.
IND.

RFACE

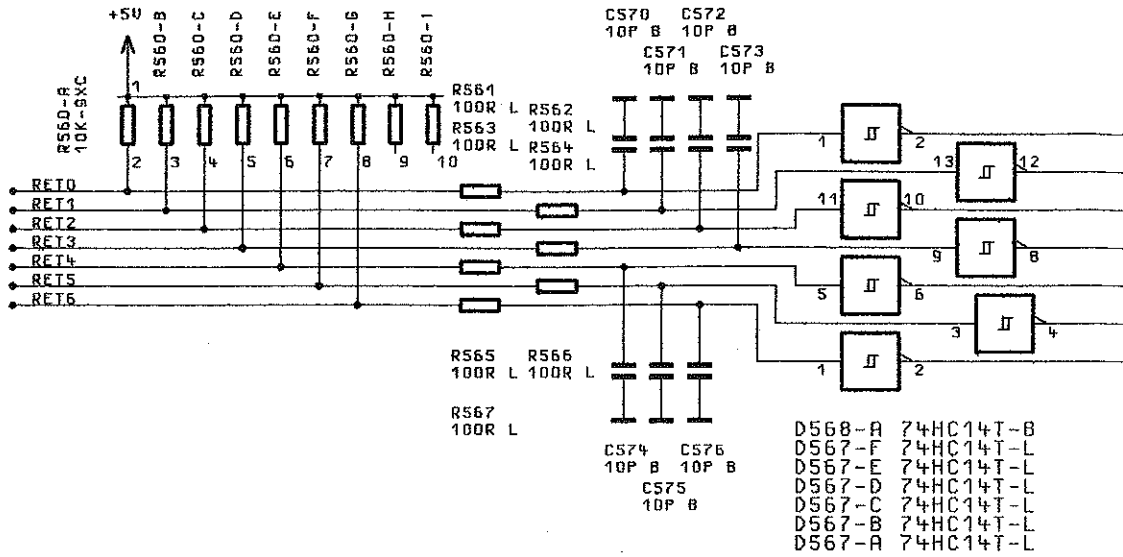
CS-REG1
RD-PERI1

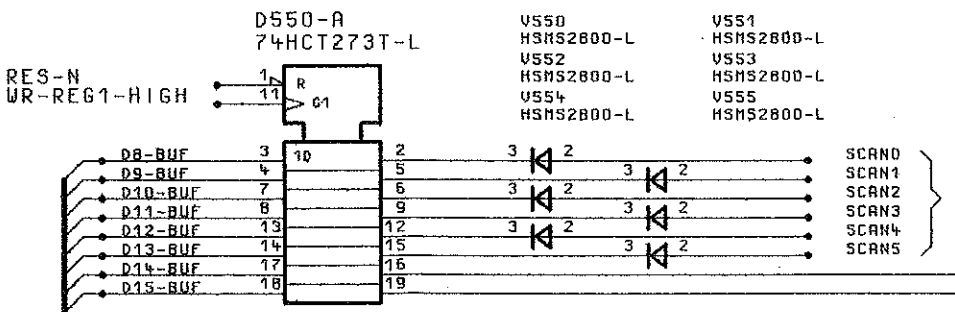
AC-FAIL
BUSY-A/D
SERBUS-ACTREQ
SERBUS-WRBE
SERBUS-RDBF
SERBUS-BUSY
SERBUS-INT2
SERBUS-INT1



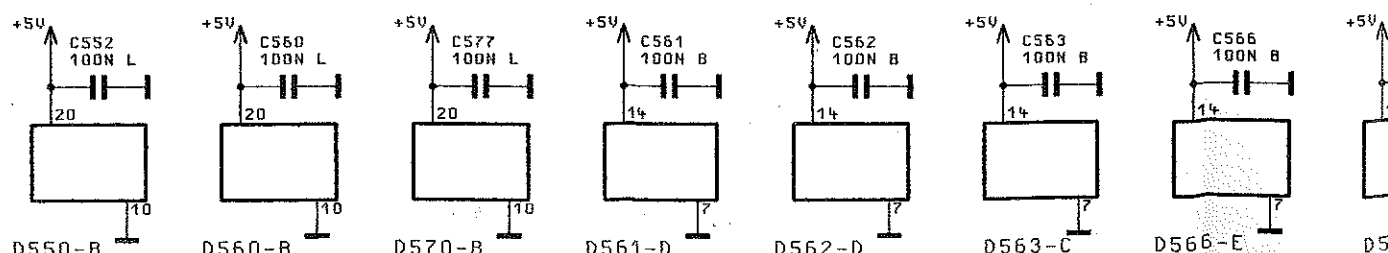


KEY-INTERFACE





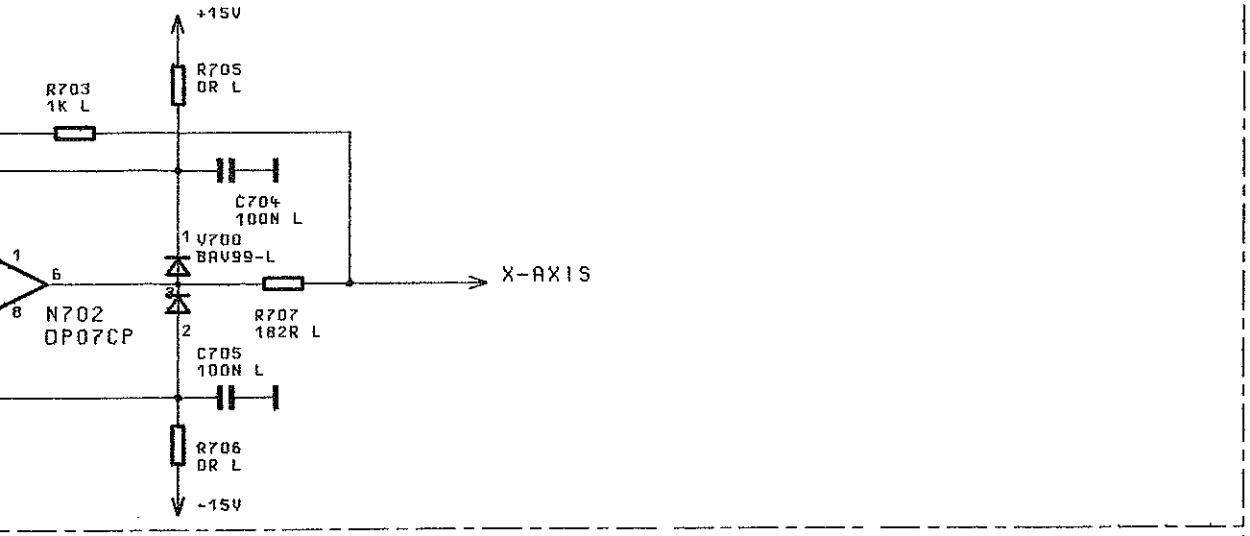
A-BUS-CPU
CONTROL-BUS-PERI
A-BUS-PERI
D-BUS-BUF
CLK-BUS

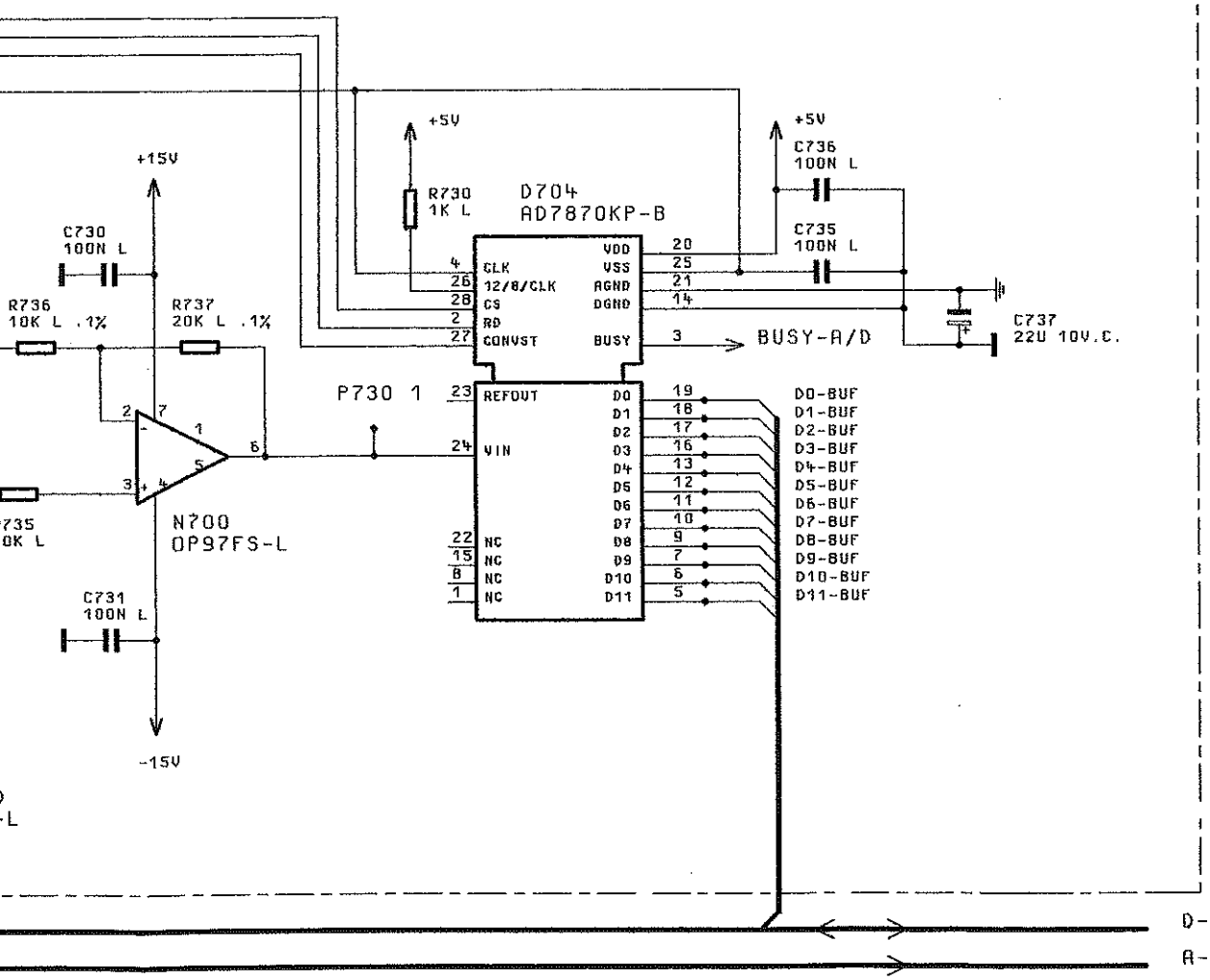


FUER DIESE UNTERLAGE
BEHALTEN WIR UNS ALLE RECHTE VOR

ZEICHN.-NR.

P730



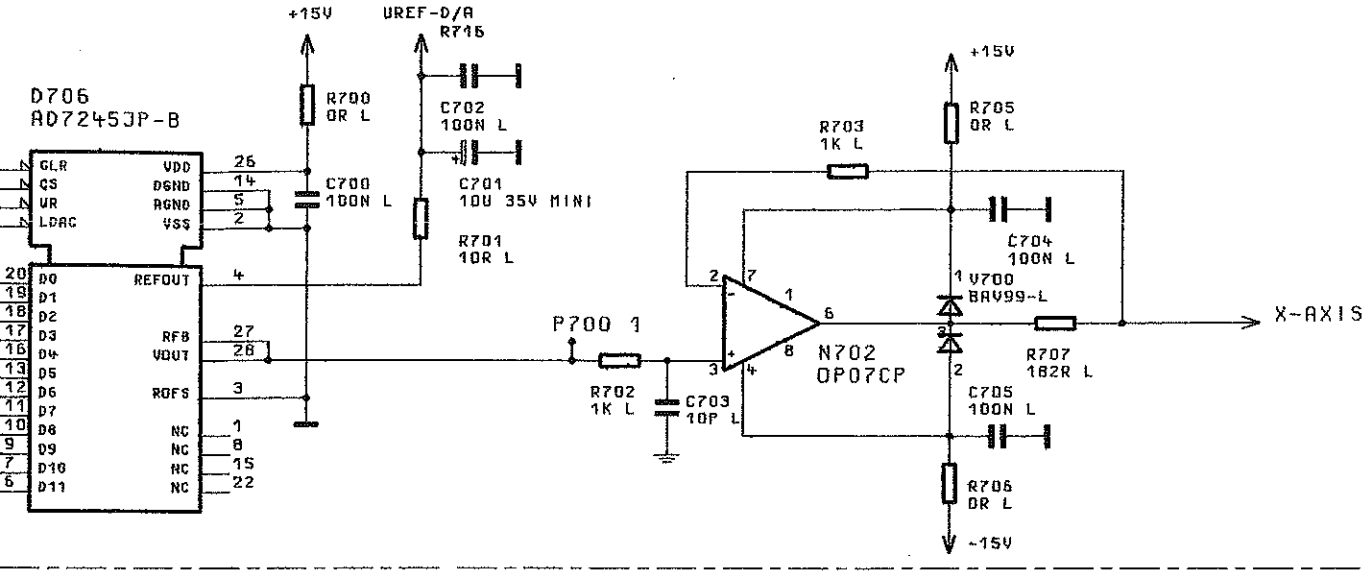


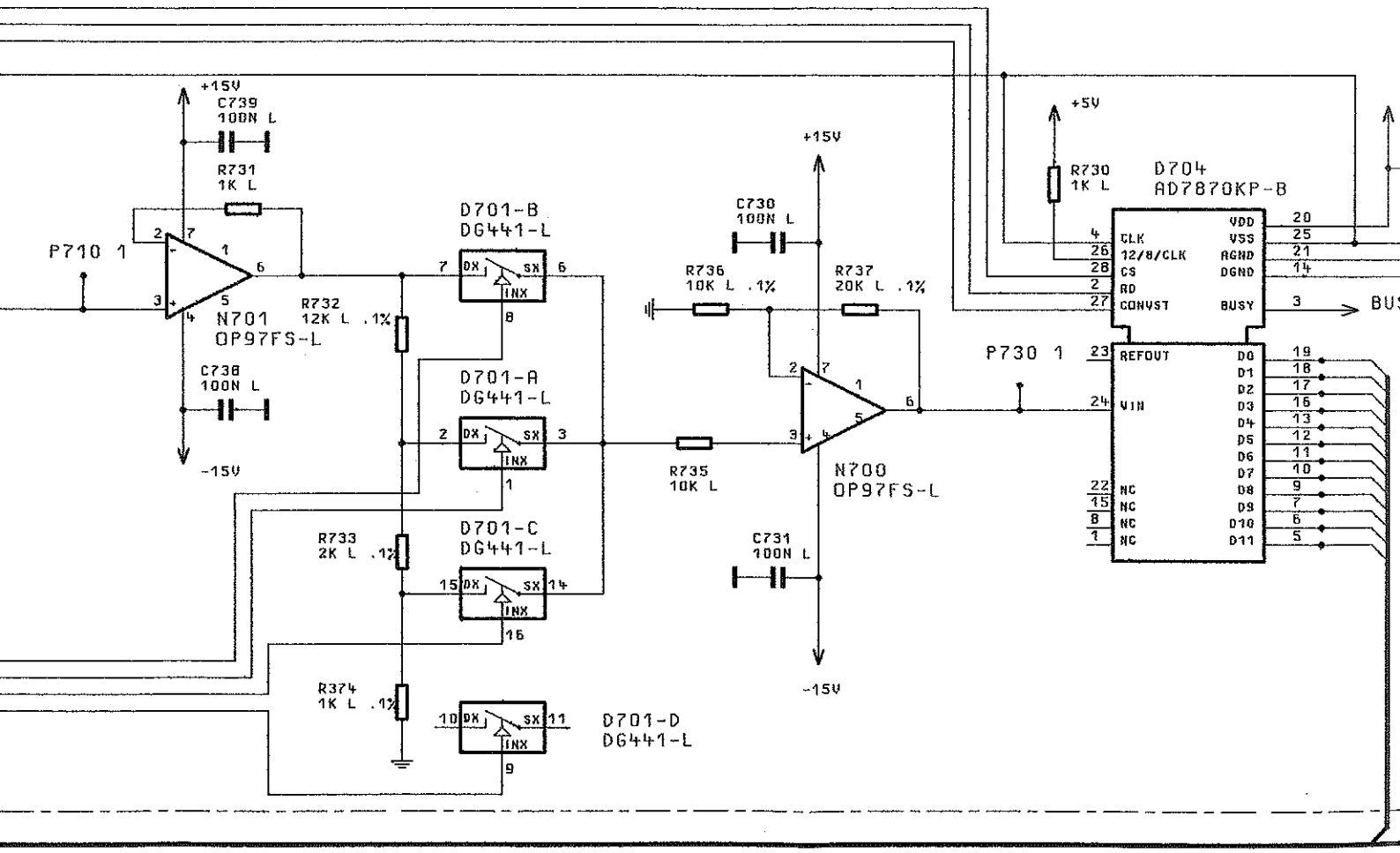
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				BEARB.		JN	RECHNER
				GEPR.			PROCESSOR
				NORM			
				PLOTT	03.05.94		
/							ZEICHN.-NR. 1035.7250.01S
AEND. IND.	AENDERUNGS-MITTEILUNG	DATUM	NAME				BLATT-NR. 8+

ROHDE&SCHWARZ

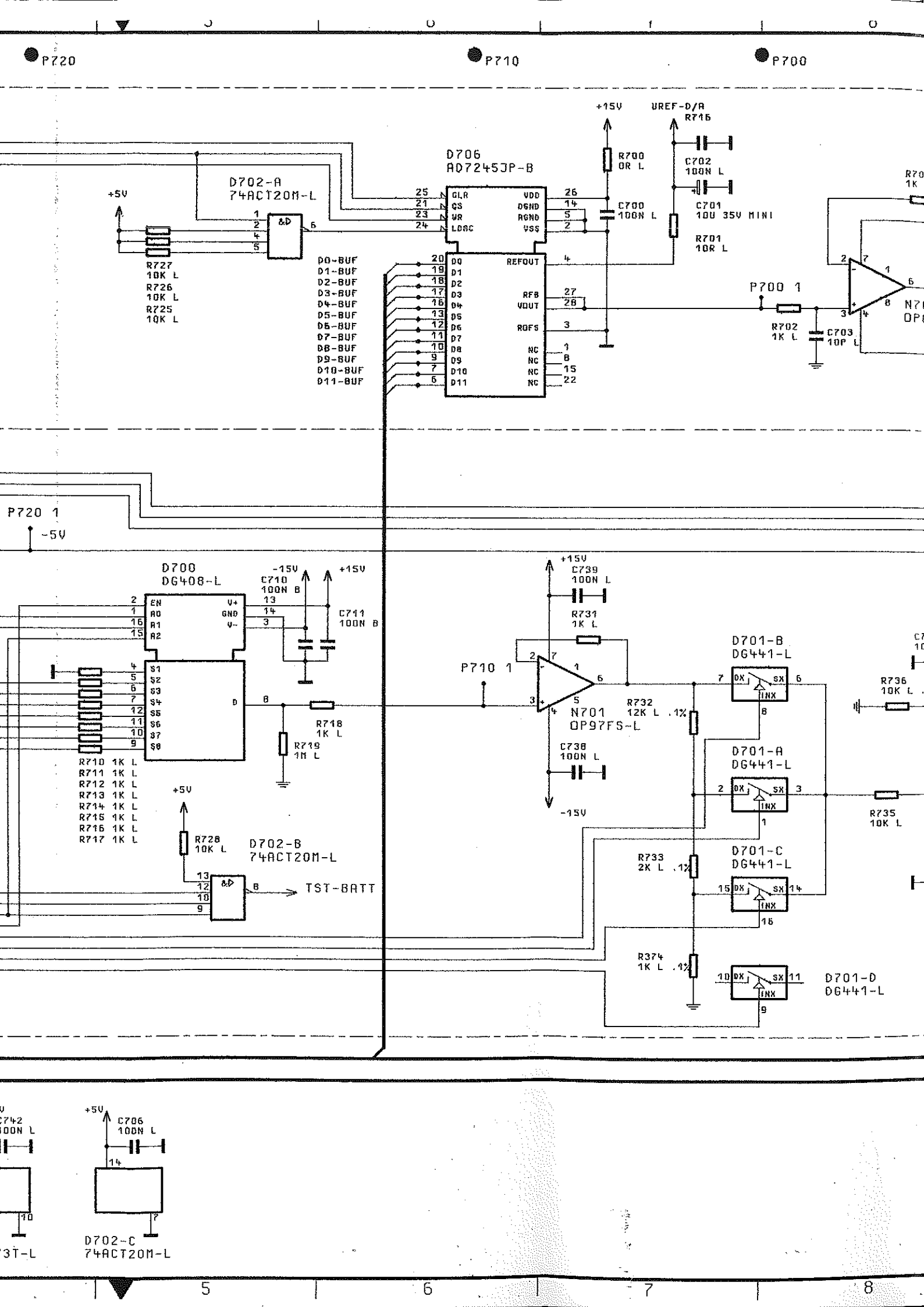
ZU GERAET SMP | REG.I.V. 1035.5005 | ERSTE Z. 1035.5005

P710 P700 P730

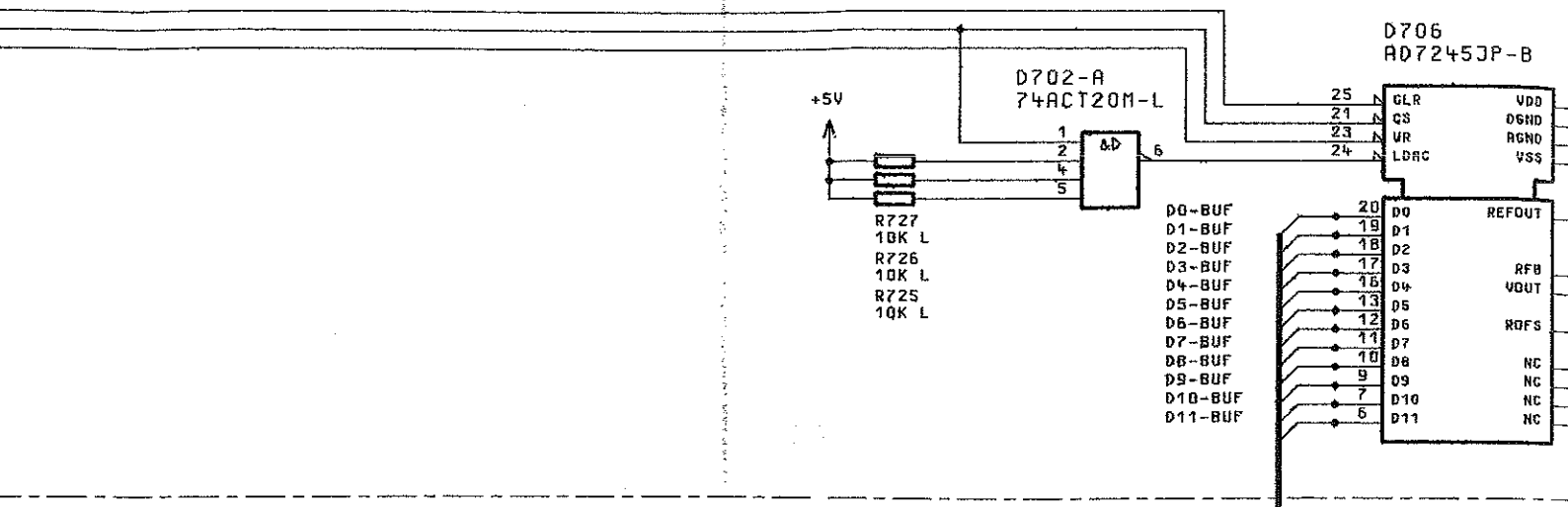




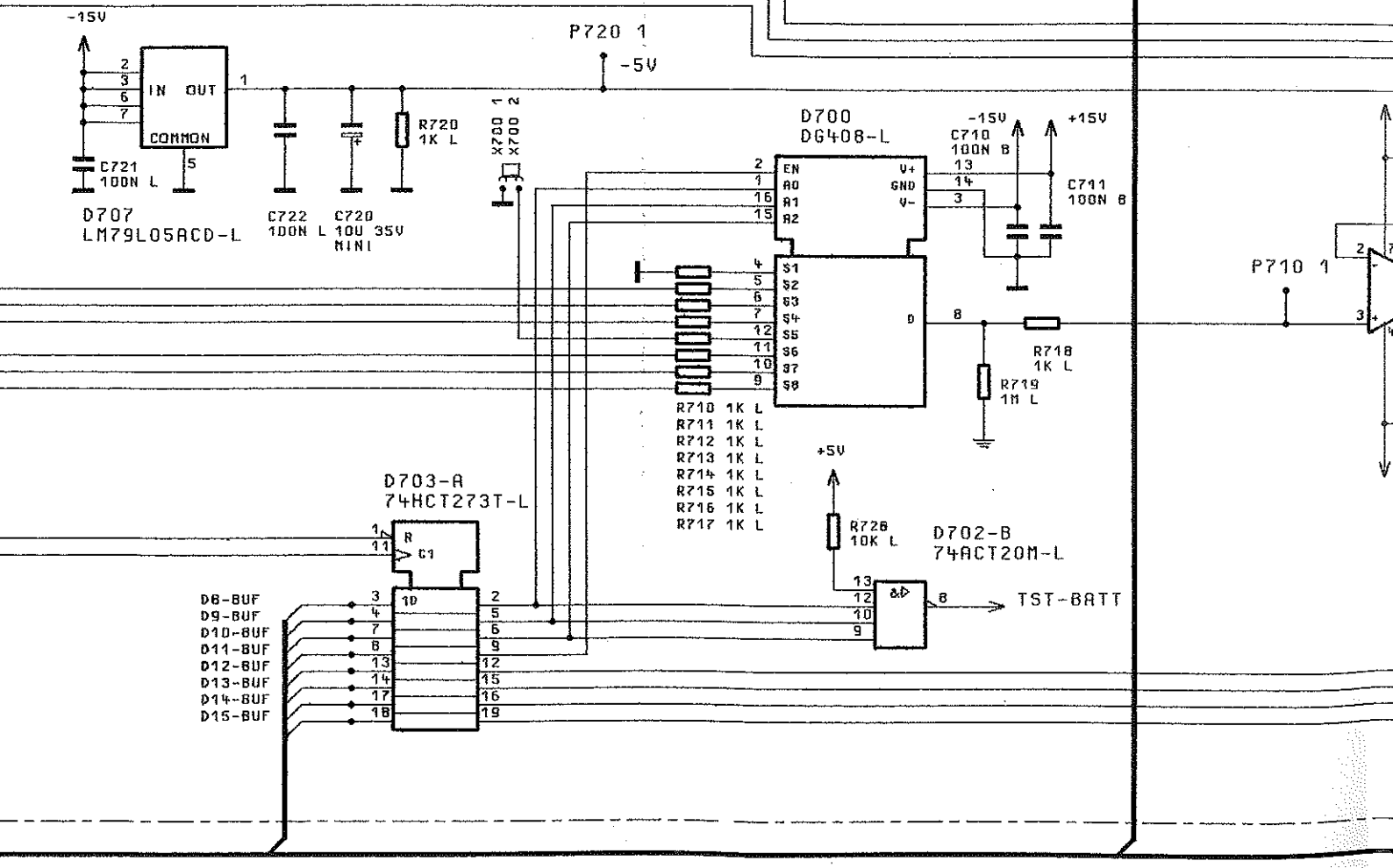
05/		19.04.94	DR	1GPK	TAG	NAM
				BEARB.		JN
				GEPR.		
				NORM		
				PLOTT	03.05.94	
/						
AEND. IND.	AENDERUNGS-MITTEILUNG	DATUM	NAME	ROHDE&SCHWA		
				ZU GERAET	SMP	

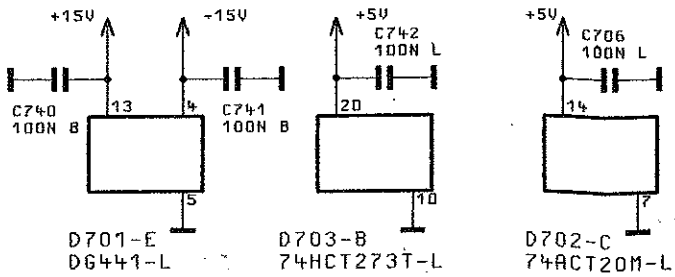

P720
P710
P700
D706
AD7245JP-B
D702-A
74ACT20M-L
+5V
+15V
UREF-D/A
R716
R700
0R L
C702
100N L
C701
10U 35V MINI
R701
10R L
C700
100N L
R727
10K L
R726
10K L
R725
10K L
D0-BUF
D1-BUF
D2-BUF
D3-BUF
D4-BUF
D5-BUF
D6-BUF
D7-BUF
D8-BUF
D9-BUF
D10-BUF
D11-BUF
GLR
QS
WR
LDAC
VDD
DGND
AGND
VSS
REFOUT
RFB
VOUT
ROFS
NC
D0
D1
D2
D3
D4
D5
D6
D7
D8
D9
D10
D11
P700 1
R702
1K L
C703
10P L
P720 1
-5V
D700
DG408-L
-15V
C710
100N B
C711
100N B
EN
A0
A1
A2
V+
GND
V-
S1
S2
S3
S4
S5
S6
S7
S8
D
R710 1K L
R711 1K L
R712 1K L
R713 1K L
R714 1K L
R715 1K L
R716 1K L
R717 1K L
R718
1K L
R719
1M L
R728
10K L
D702-B
74ACT20M-L
TST-BATT
P710 1
C739
100N L
R731
1K L
N701
OP97FS-L
R732
12K L .1%
C738
100N L
D701-B
DG441-L
D701-A
DG441-L
D701-C
DG441-L
D701-D
DG441-L
DX
SX
INX
R736
10K L
R735
10K L
R733
2K L .1%
R374
1K L .1%
C706
100N L
D702-C
74ACT20M-L

PUT



DIAGNOSE





FUER DIESE UNTERLAGE
BEHALTEN WIR UNS ALLE RECHTE VOR

P720

X-OUTPUT

RES-N
CS-D/A-CONV
WR-D/A-CONV

+5V

SELF DIAGNOSE

CS-REG2
RD-PERI1
CS-REG3

-15V

D707
LM79L05ACD-L

C721
100N L

C722
100N L

C720
100 35V
MINI

R720
1K L

P720 1

-5V

X700 1 2
X700

DIAG-15V
DIAG-5V
X-AXIS

VPP
UREF-D/A
UBATT-TST

R710 1K L
R711 1K L
R712 1K L
R713 1K L
R714 1K L
R715 1K L
R716 1K L
R717 1K L

D703-A
74HCT273T-L

WR-REG2-HIGH

D8-BUF
D9-BUF
D10-BUF
D11-BUF
D12-BUF
D13-BUF
D14-BUF
D15-BUF

D-BUS-BUF
A-BUS-PERI

+15V
-15V
+5V

C740
100N B

C741
100N B

C742
100N L

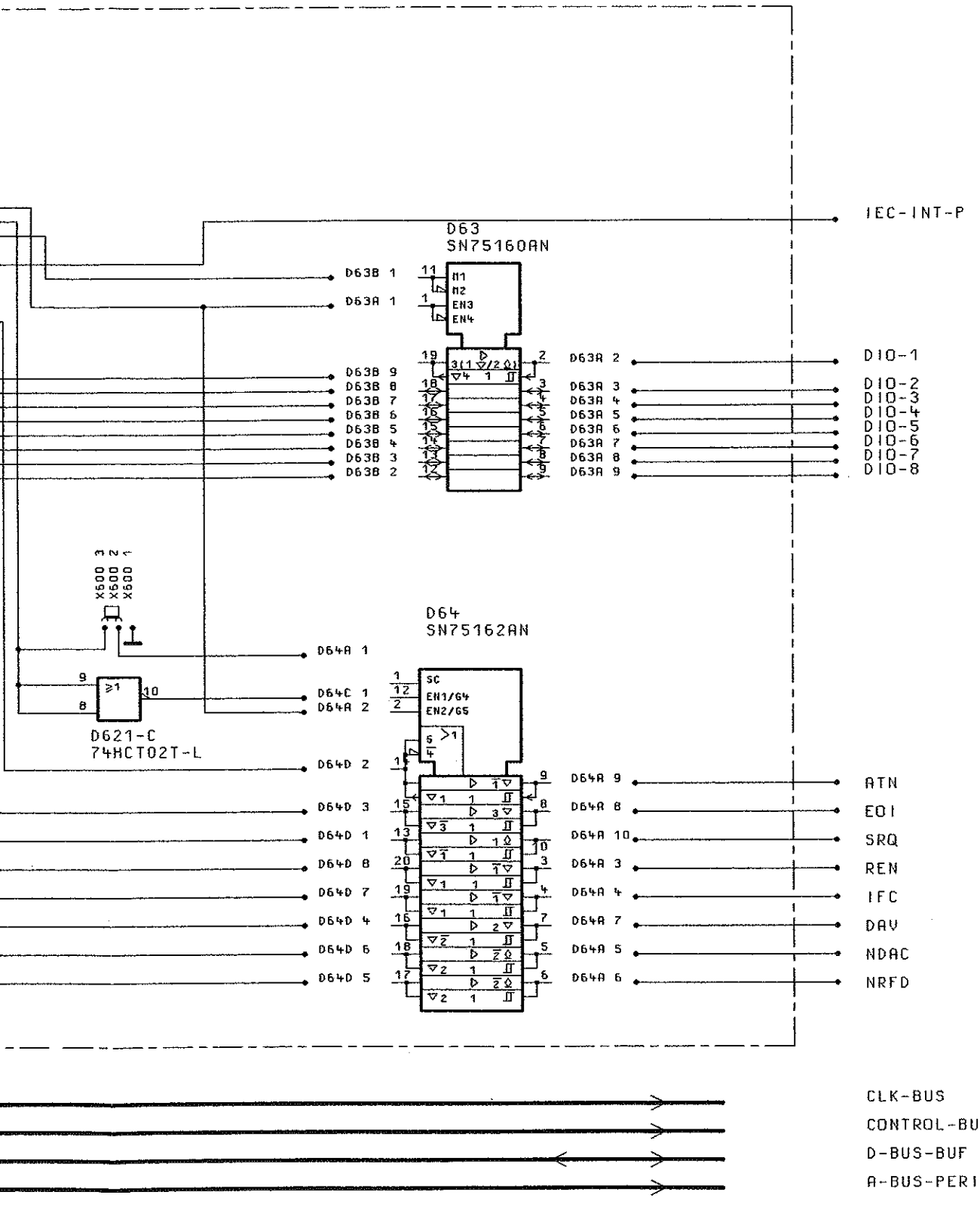
C706
100N

D701-E
DG441-L

D703-B
74HCT273T-L

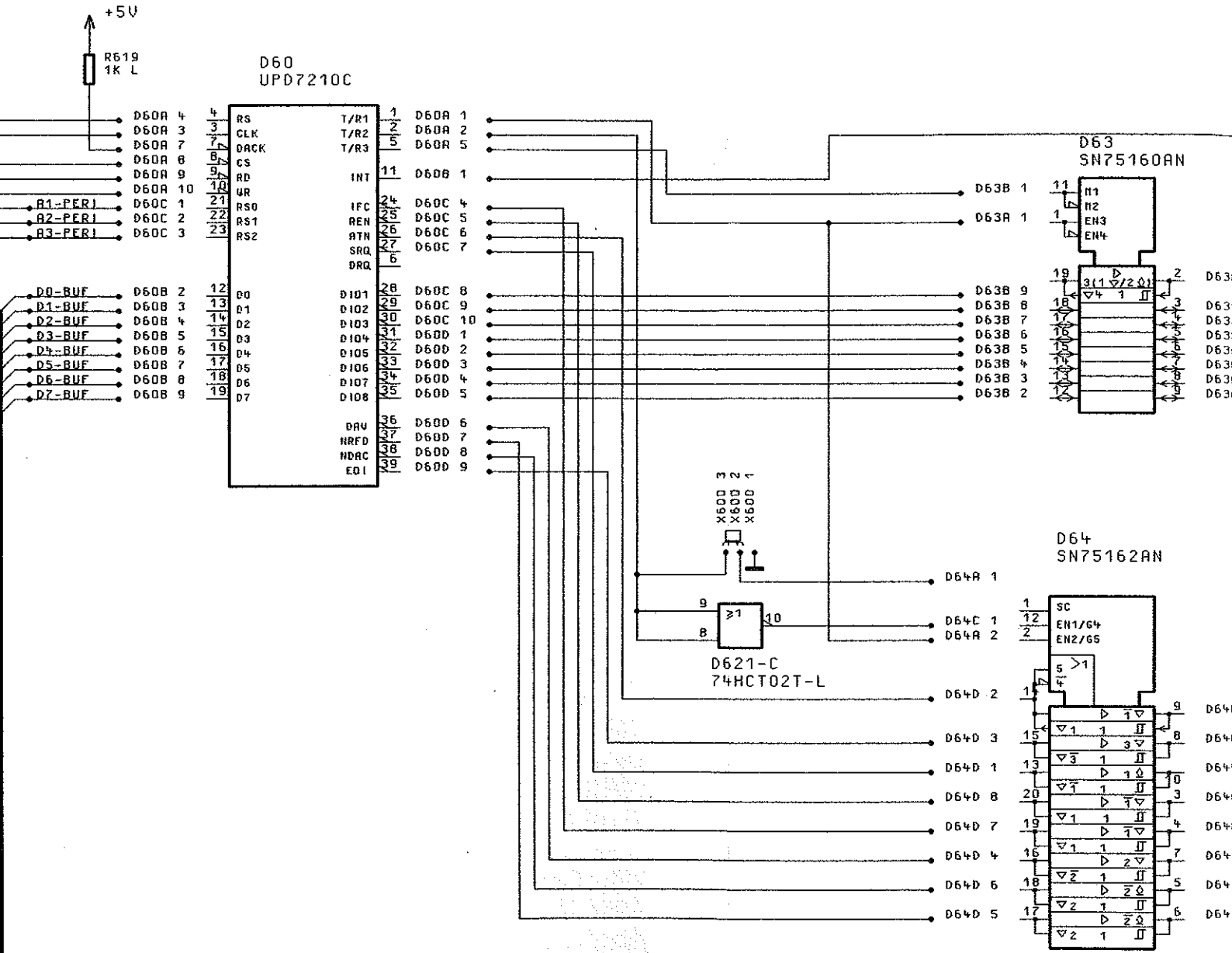
D702-C
74ACT20

CHN.-NR.

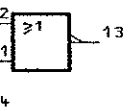


DB/		13.03.95	DR	1GPK	TAG	NAME	BENENNUNG
				BEARB.		DR	RECHNER
				GEPR.			PROCESSOR
				NORM			
				PLOTT	13.03.95		
/				ROHDE&SCHWARZ			ZEICHN.-NR. 1035.7250.01S
AEND. IND.	AENDERUNGS-MITTEILUNG	DATUM	NAME	ZU GERAET	SMP		BLATT-NR. 9+
				REG.I.V.	1035.5005	ERSTE Z.	1035.5005

-BUS

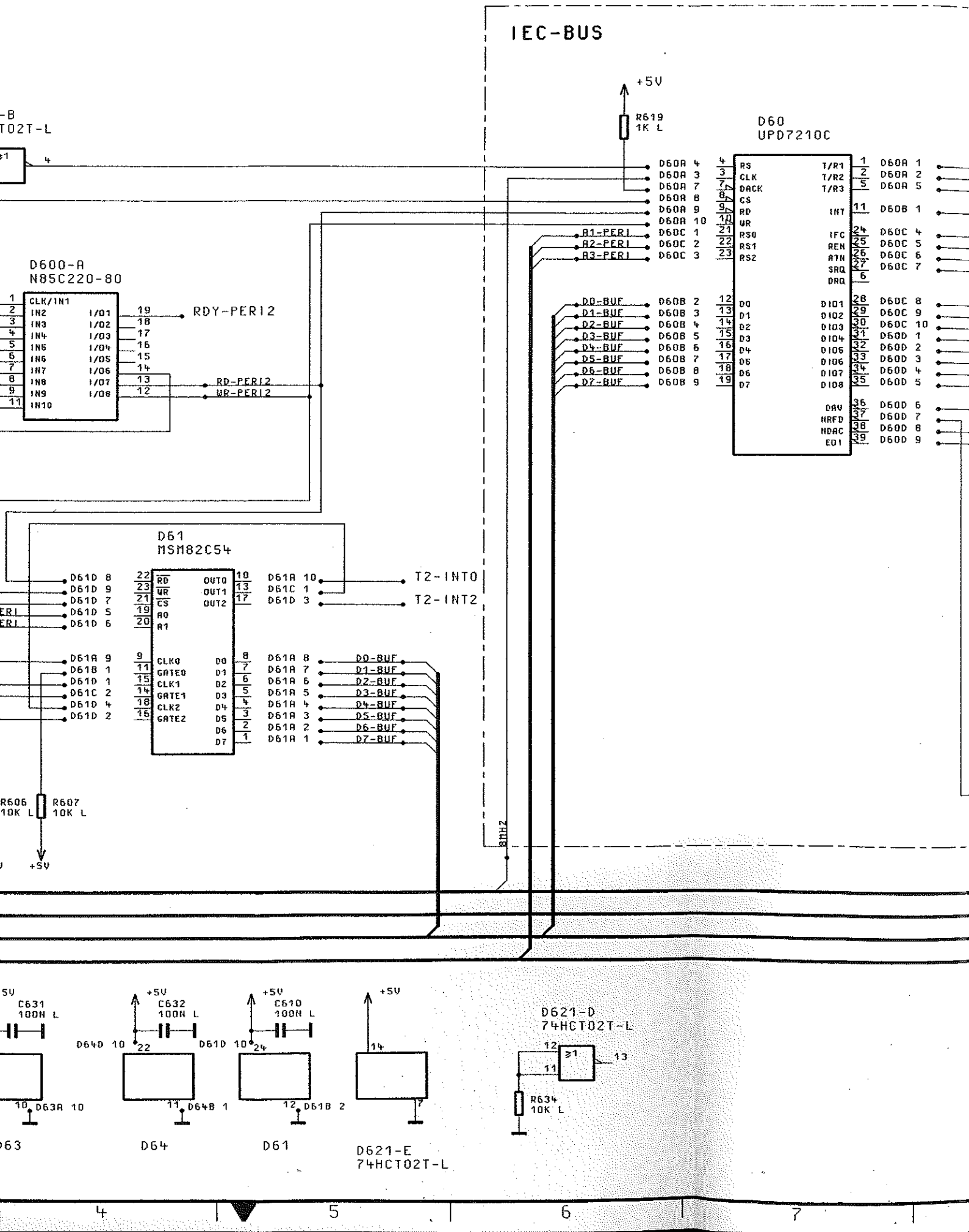


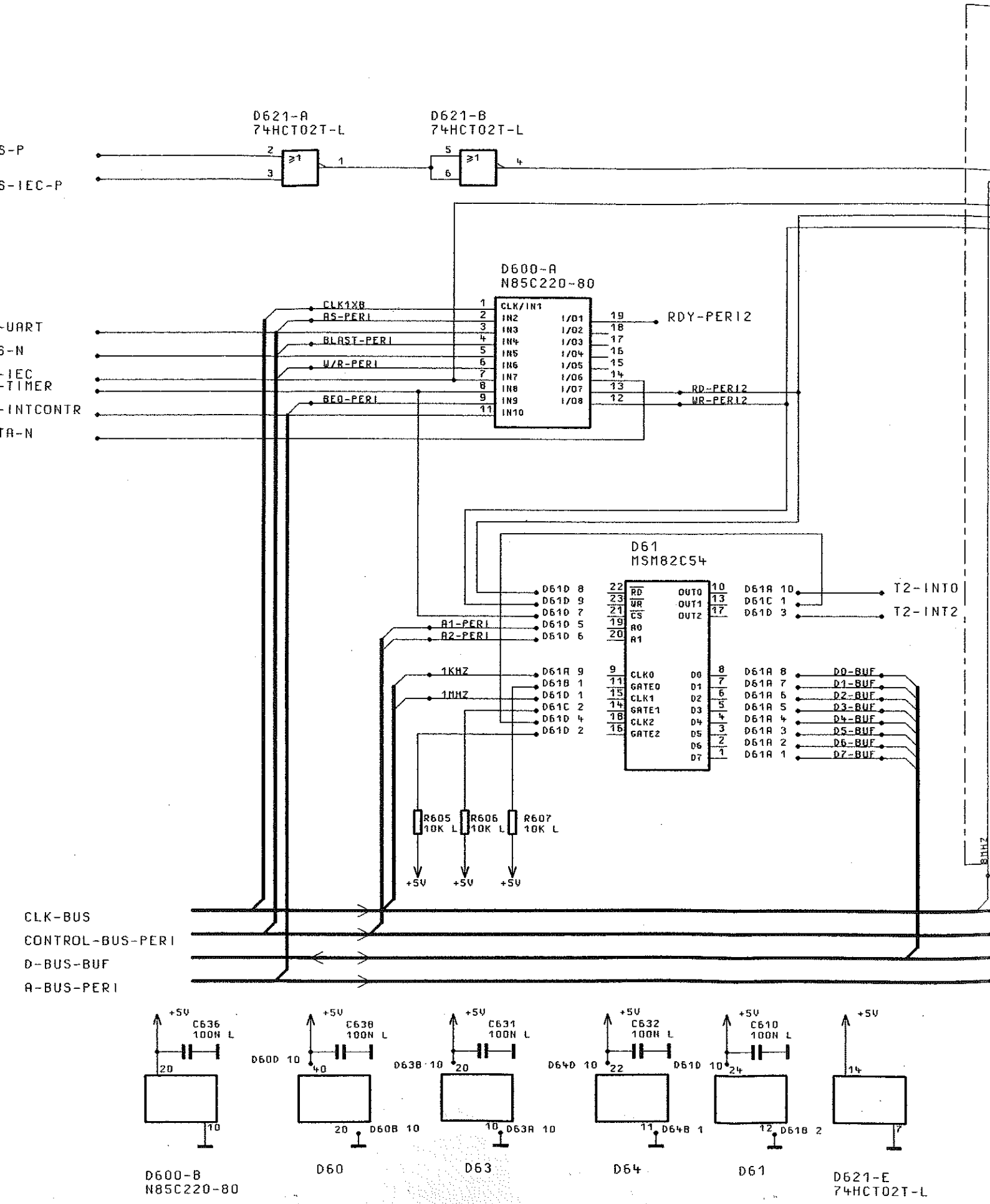
621-D
4HCT02T-L



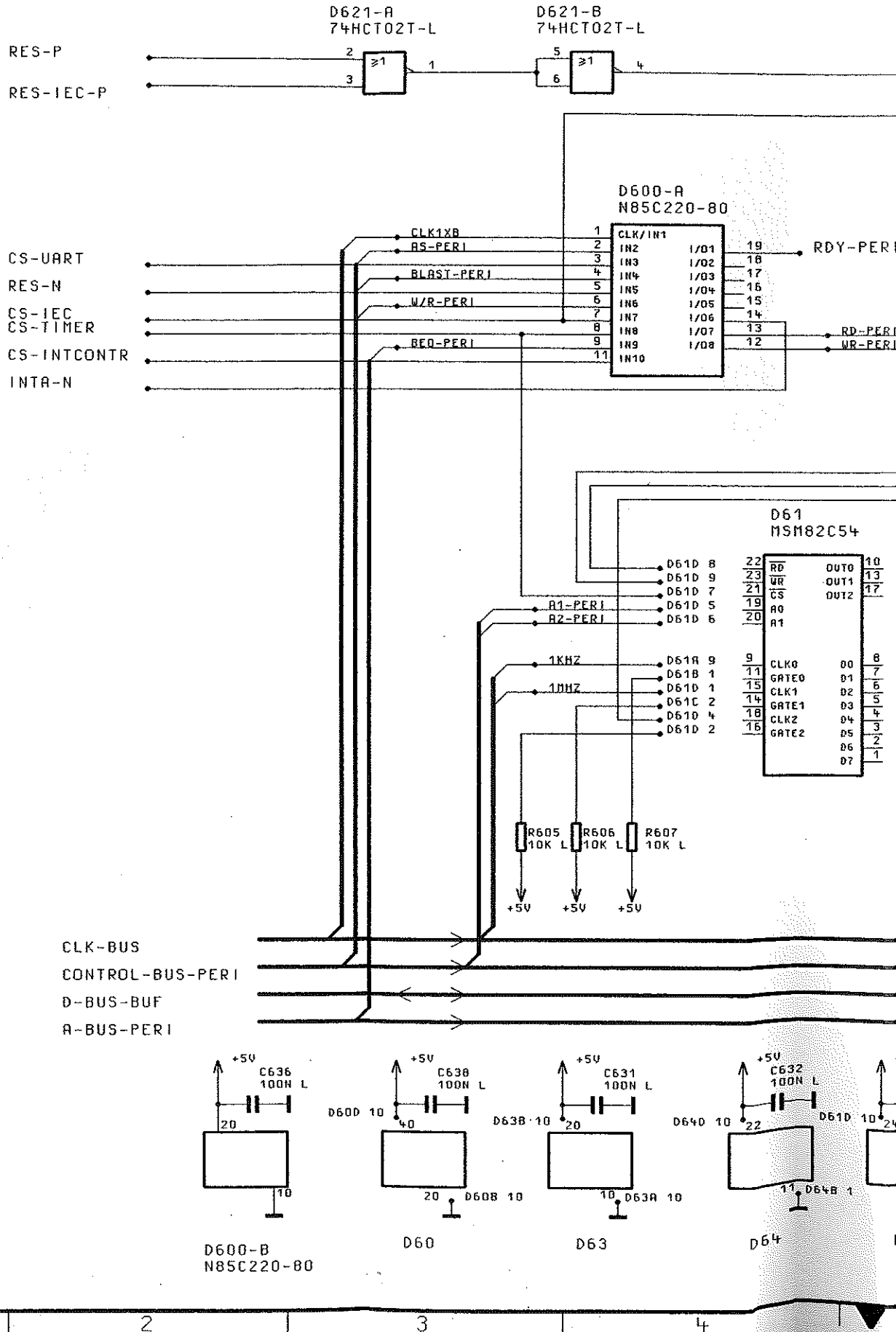
4
L

08/		13.03.95	DR	1GPK	TAG	
				BEARB.		
				GEPR.		
				NORM		
				PLOTT	13.03.95	
/						
AEND. IND.	AENDERUNGS-MITTEILUNG	DATUM	NAME	ROHDE&SCHW		
				ZU GERAET	SMP	

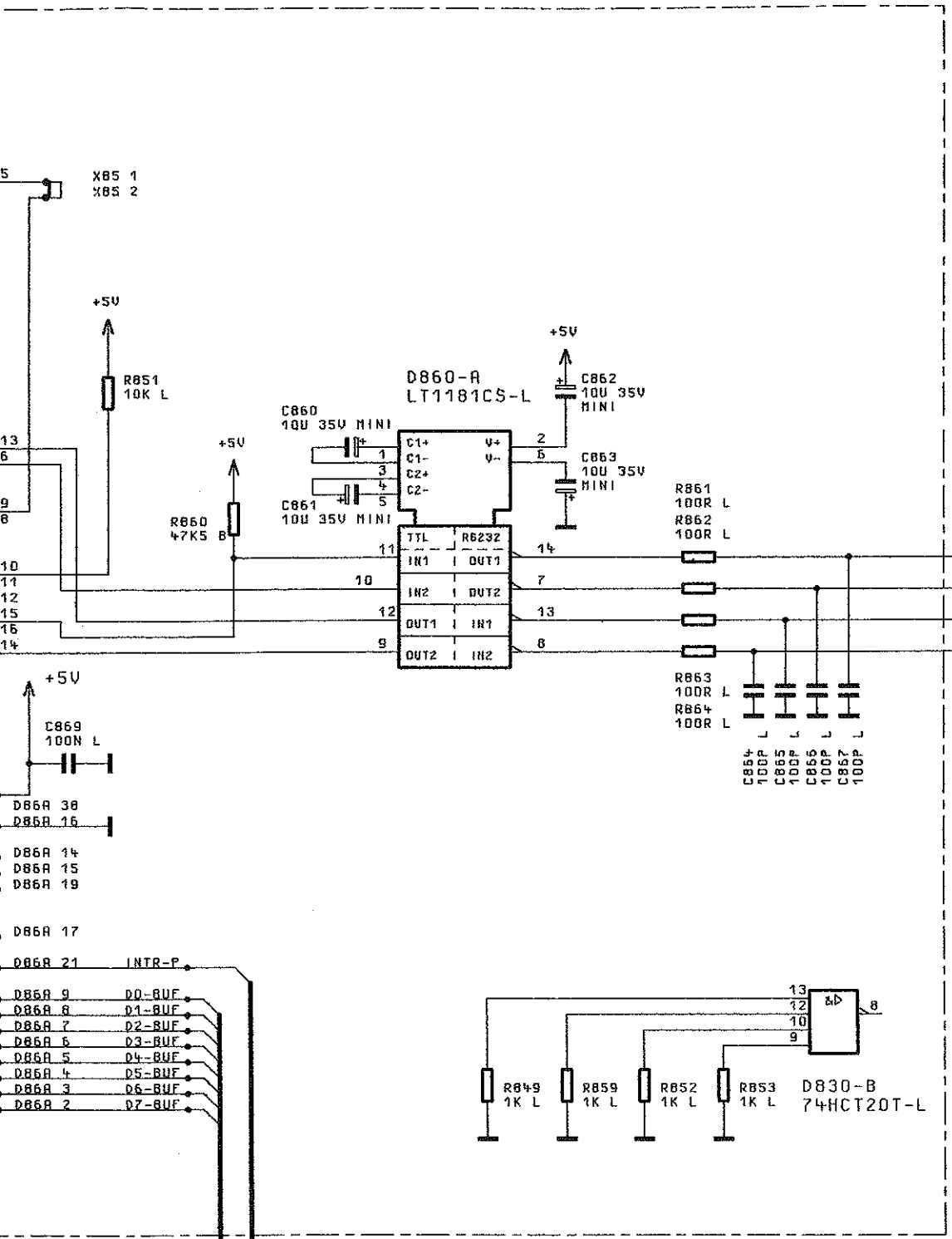

IEC-BUS
+5V
R619
1K L
D60
UPD7210C
D600-A
N85C220-80
RDY-PERI2
RD-PERI2
WR-PERI2
D61
MSM82C54
T2-INT0
T2-INT2
R606
10K L
R607
10K L
+5V
8MHZ
D621-D
74HCT02T-L
R634
10K L
D621-E
74HCT02T-L
D63
D64
D61


D621-A
74HCT02T-L
D621-B
74HCT02T-L
S-P
S-IEC-P
D600-A
N85C220-80
CLK1X8
AS-PERI
BLAST-PERI
W/R-PERI
BEO-PERI
-UART
S-N
-IEC
-TIMER
-INTCONTR
TA-N
RDY-PERI2
RD-PERI2
WR-PERI2
D61
MSM82C54
A1-PERI
A2-PERI
1KHZ
1MHZ
T2-INTO
T2-INT2
D0-BUF
D1-BUF
D2-BUF
D3-BUF
D4-BUF
D5-BUF
D6-BUF
D7-BUF
R605 10K L
R606 10K L
R607 10K L
+5V
8MHZ
CLK-BUS
CONTROL-BUS-PERI
D-BUS-BUF
A-BUS-PERI
C636 100N L
C638 100N L
C631 100N L
C632 100N L
C610 100N L
D600-B
N85C220-80
D60
D63
D64
D61
D621-E
74HCT02T-L

FUER DIESE UNTERLAGE
BEHALTEN WIR UNS ALLE RECHTE VOR



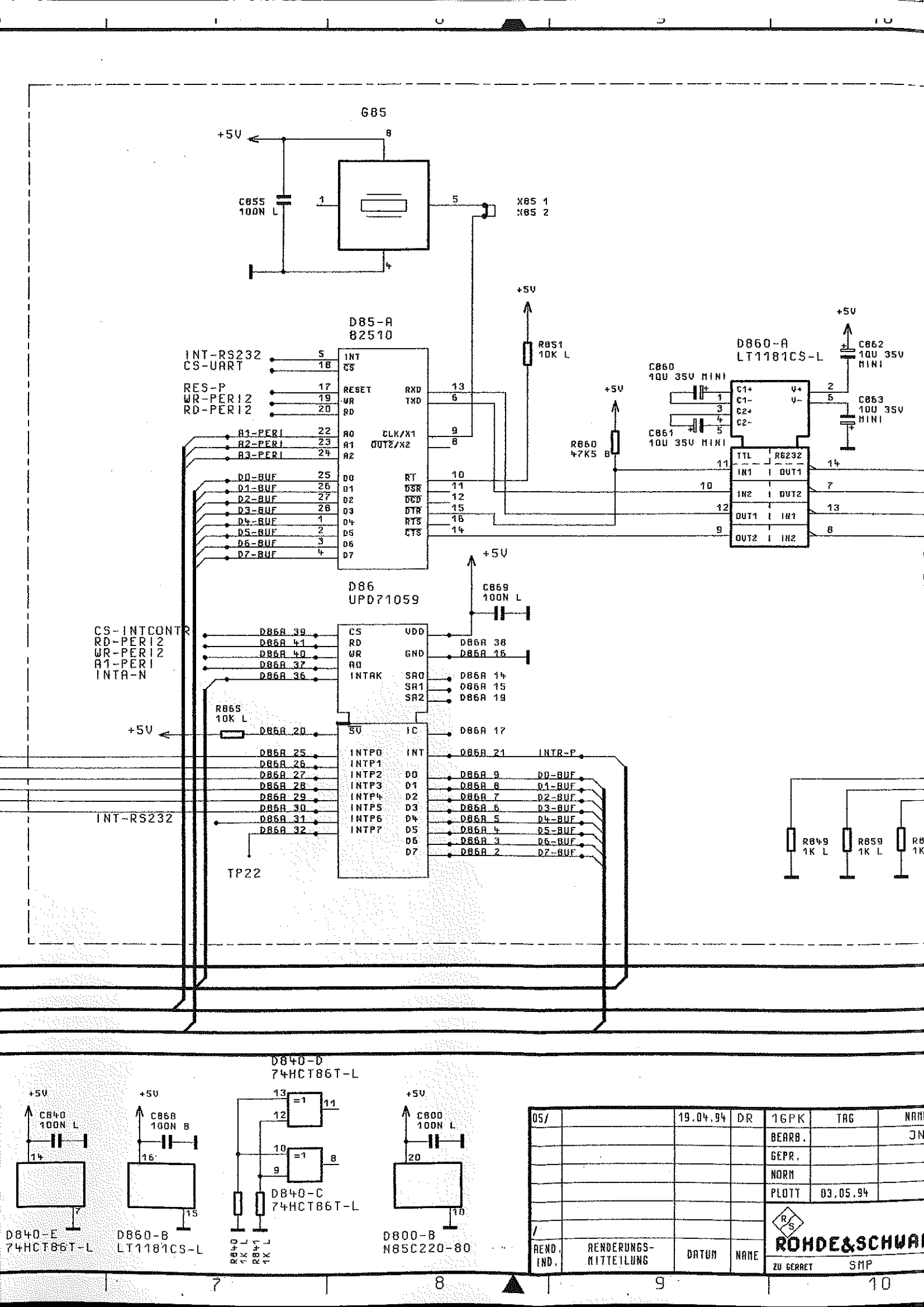
ZEICHN.-NR.



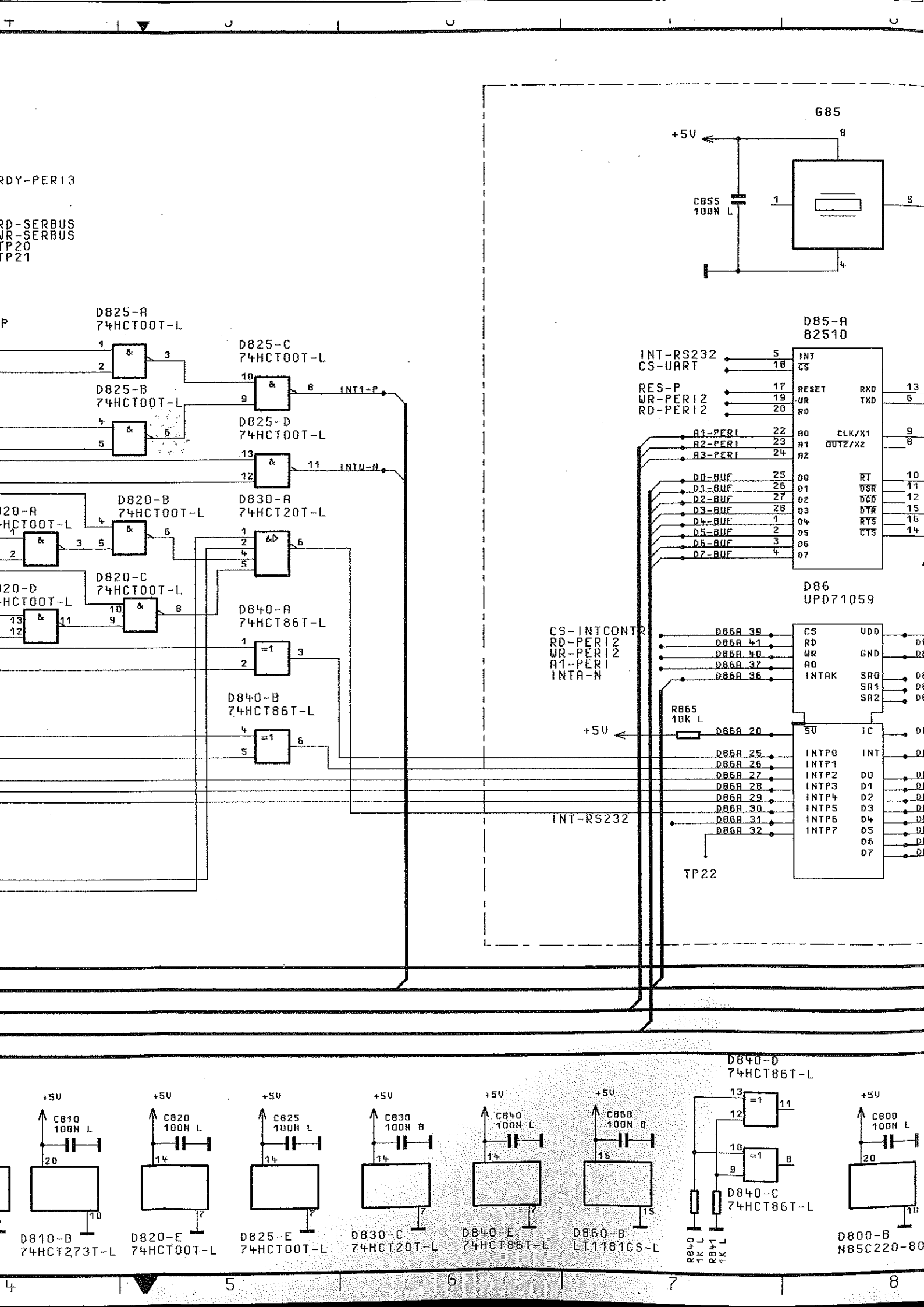
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				BEARB.		JN	RECHNER
				GEPR.			PROCESSOR
				NORM			
				PLOTT	03.05.94		
/							ZEICHN.-NR. 1035.7250.01S
REND. IND.	RENDERUNGS-MITTEILUNG	DATUM	NAME	ZU GERAET	SMP	REG.I.V. 1035.5005	ERSTE Z. 1035.5005

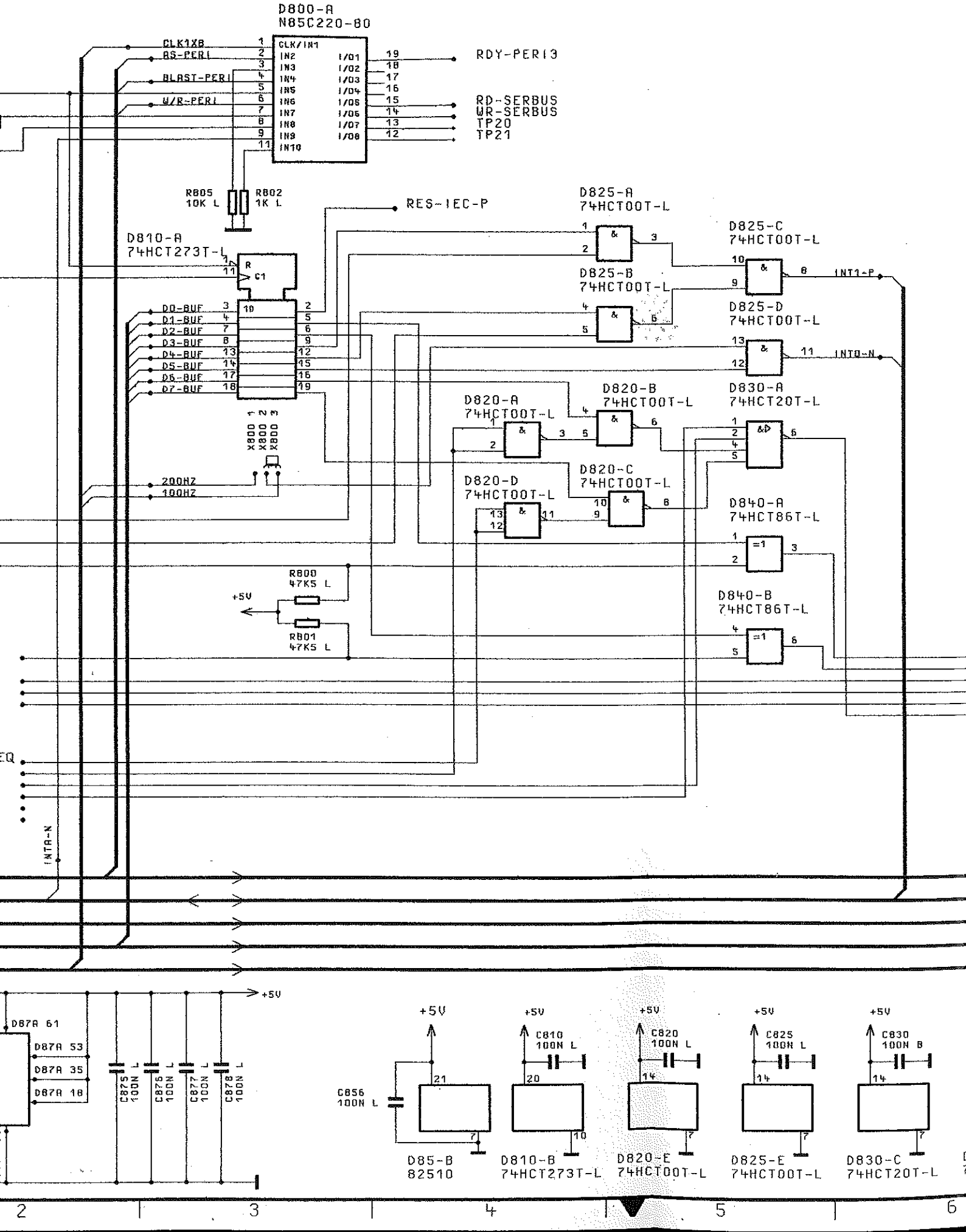
ROHDE&SCHWARZ

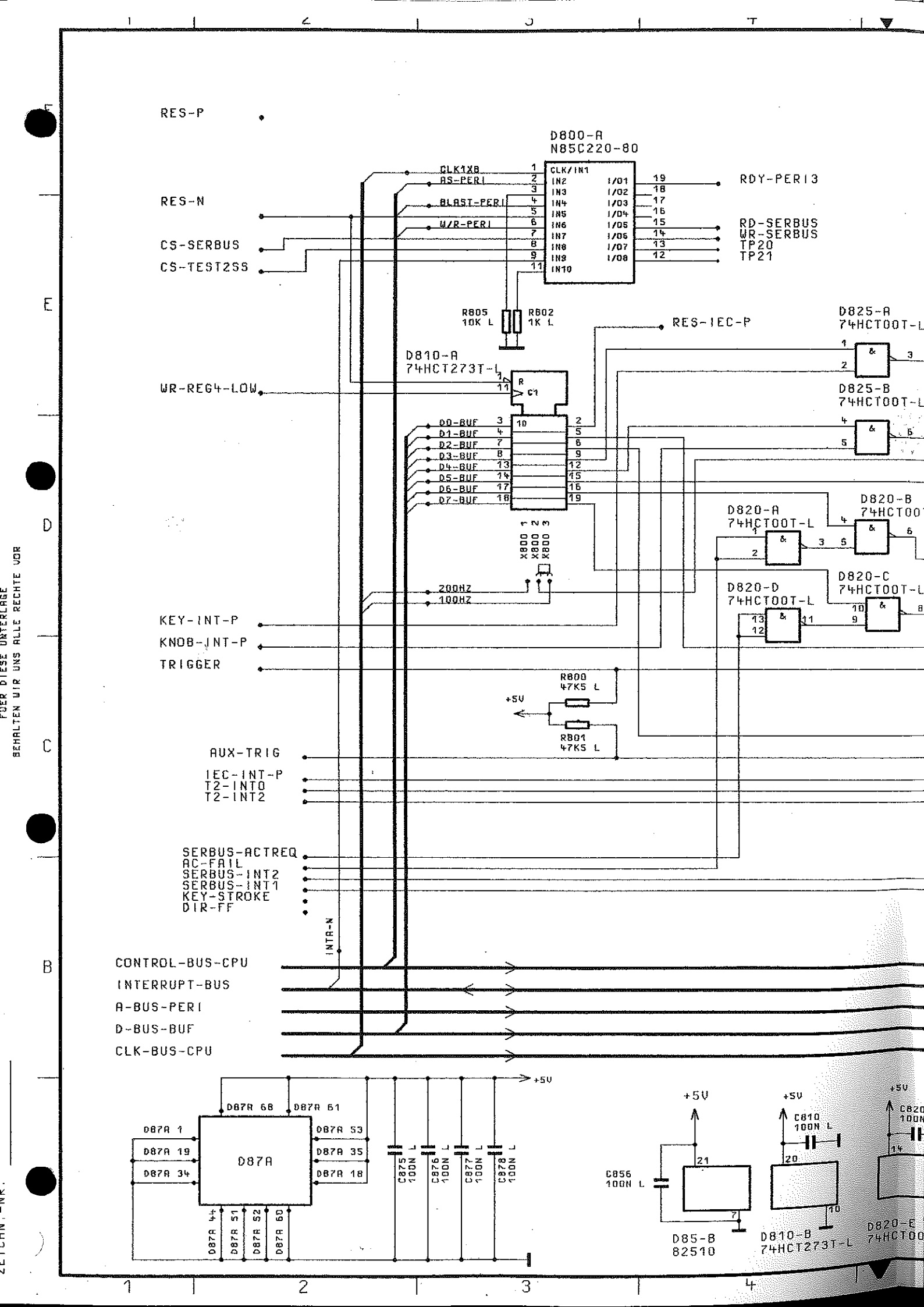
BLATT-NR. 10+ V. BL.



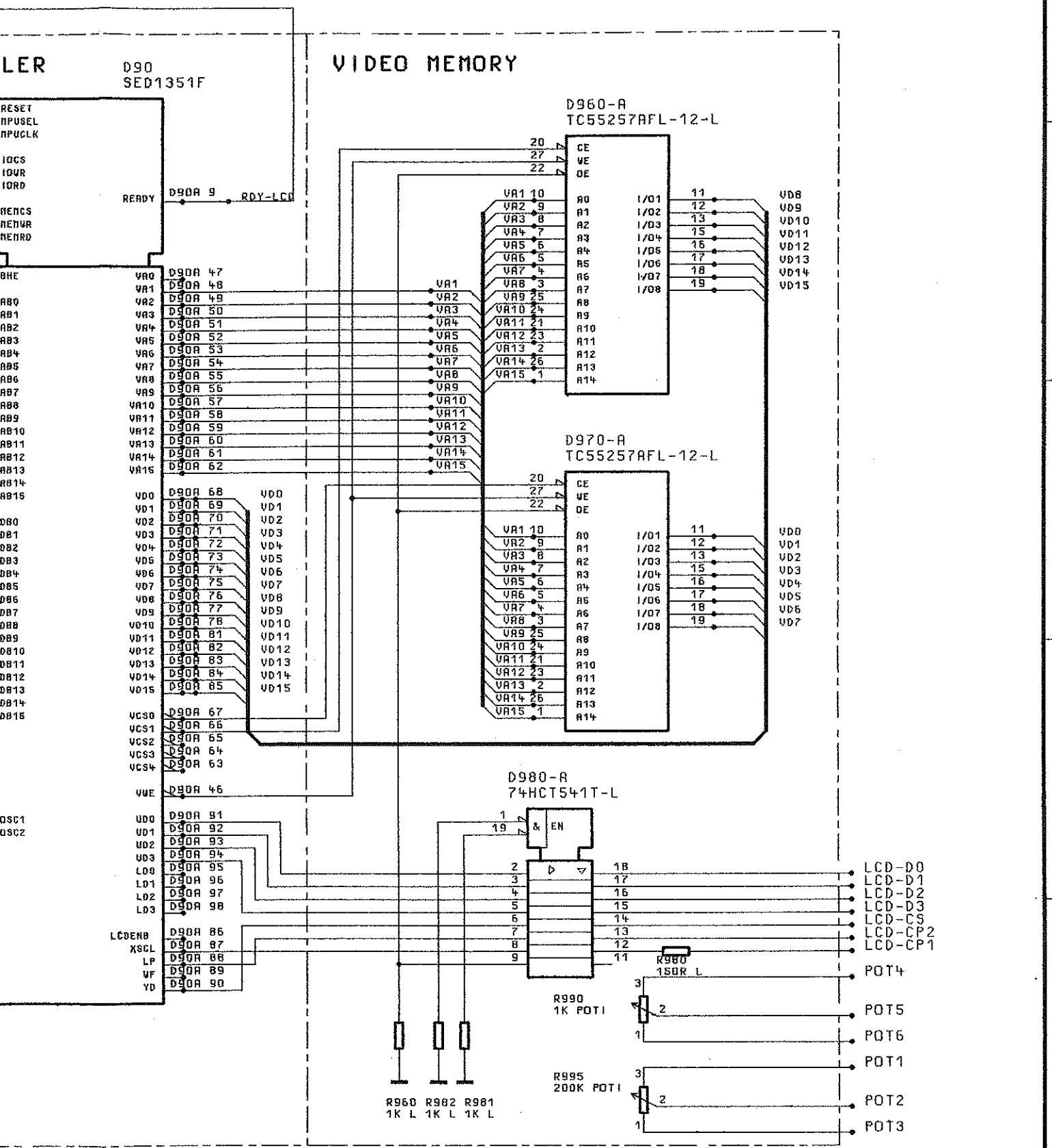
05/		19.04.94	DR	16PK	TAG	NAM
				BEARB.		JN
				GEPR.		
				NORM		
				PLOTT	03.05.94	
/						
AEND. IND.	AENDERUNGS-MITTEILUNG	DATUM	NAME	ROHDE&SCHWA		
				ZU GERAET	SMP	


RDY-PERI3
RD-SERBUS
WR-SERBUS
TP20
TP21
G85
+5V
C855 100N L
D825-A 74HCT00T-L
D825-C 74HCT00T-L
D825-B 74HCT00T-L
D825-D 74HCT00T-L
INT1-P
INT0-N
D820-B 74HCT00T-L
D830-A 74HCT20T-L
D820-C 74HCT00T-L
D840-A 74HCT86T-L
D840-B 74HCT86T-L
D85-A 82510
INT-RS232
CS-UART
RES-P
WR-PERI2
RD-PERI2
A1-PERI
A2-PERI
A3-PERI
D0-BUF
D1-BUF
D2-BUF
D3-BUF
D4-BUF
D5-BUF
D6-BUF
D7-BUF
D86 UPD71059
CS-INTCONTR
RD-PERI2
WR-PERI2
A1-PERI
INTA-N
R865 10K L
INT-RS232
TP22
D840-D 74HCT86T-L
D840-C 74HCT86T-L
C810 100N L
C820 100N L
C825 100N L
C830 100N B
C840 100N L
C860 100N B
C800 100N L
D810-B 74HCT273T-L
D820-E 74HCT00T-L
D825-E 74HCT00T-L
D830-C 74HCT20T-L
D840-E 74HCT86T-L
D860-B LT1181CS-L
D800-B N85C220-80

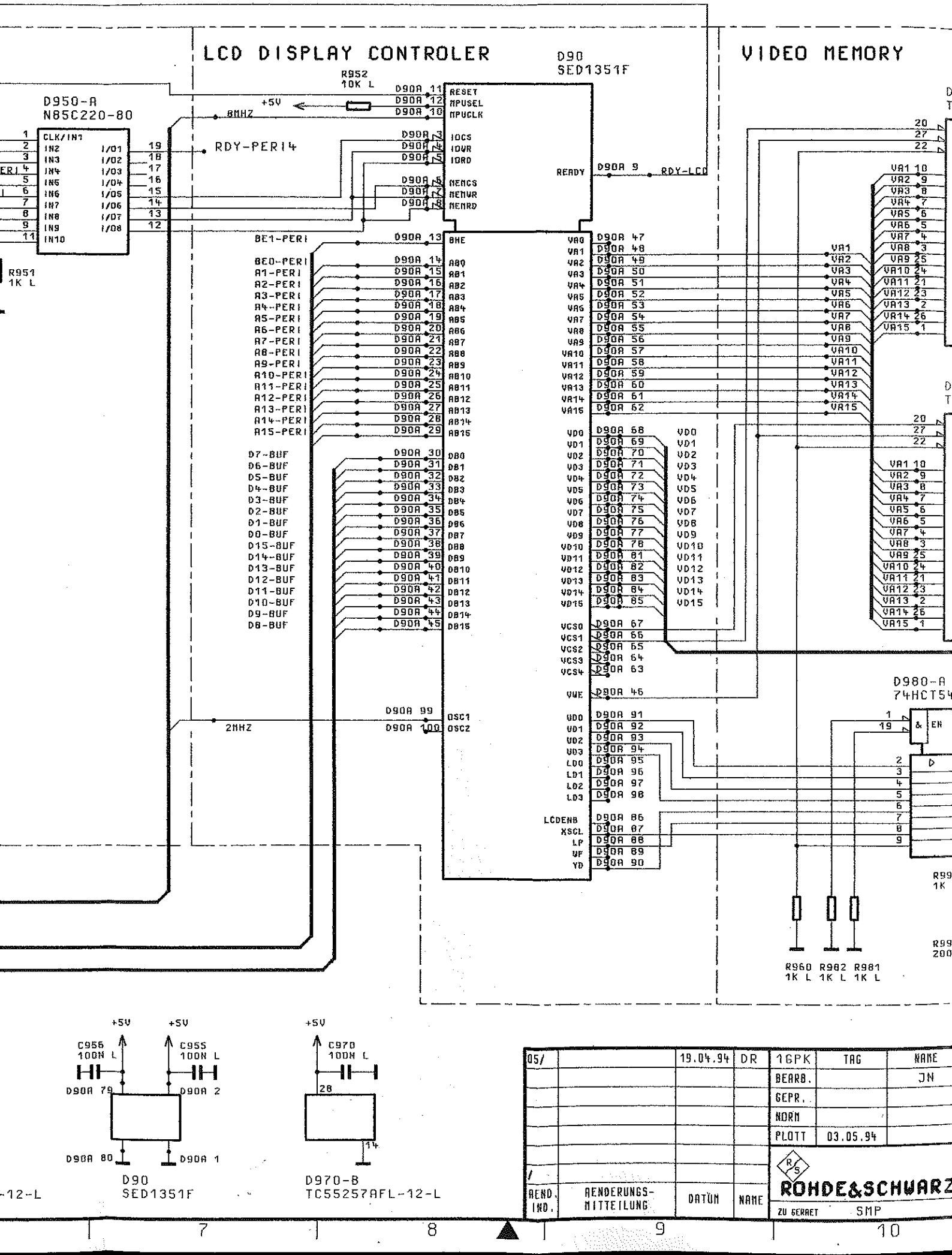

D800-A
N85C220-80
CLK1X8
AS-PERI
BLAST-PERI
W/R-PERI
RDY-PERI3
RD-SERBUS
WR-SERBUS
TP20
TP21
R805
10K L
R802
1K L
RES-IEC-P
D810-A
74HCT273T-L
D825-A
74HCT00T-L
D825-B
74HCT00T-L
D825-C
74HCT00T-L
D825-D
74HCT00T-L
INT1-P
INT0-N
D0-BUF
D1-BUF
D2-BUF
D3-BUF
D4-BUF
D5-BUF
D6-BUF
D7-BUF
X800
200HZ
100HZ
D820-A
74HCT00T-L
D820-B
74HCT00T-L
D820-C
74HCT00T-L
D820-D
74HCT00T-L
D830-A
74HCT20T-L
D840-A
74HCT86T-L
D840-B
74HCT86T-L
R800
47K5 L
R801
47K5 L
+5V
INTA-N
D87A 61
D87A 53
D87A 35
D87A 18
C875
100N L
C876
100N L
C877
100N L
C878
100N L
C856
100N L
C810
100N L
C820
100N L
C825
100N L
C830
100N B
D85-B
82510
D810-B
74HCT273T-L
D820-E
74HCT00T-L
D825-E
74HCT00T-L
D830-C
74HCT20T-L

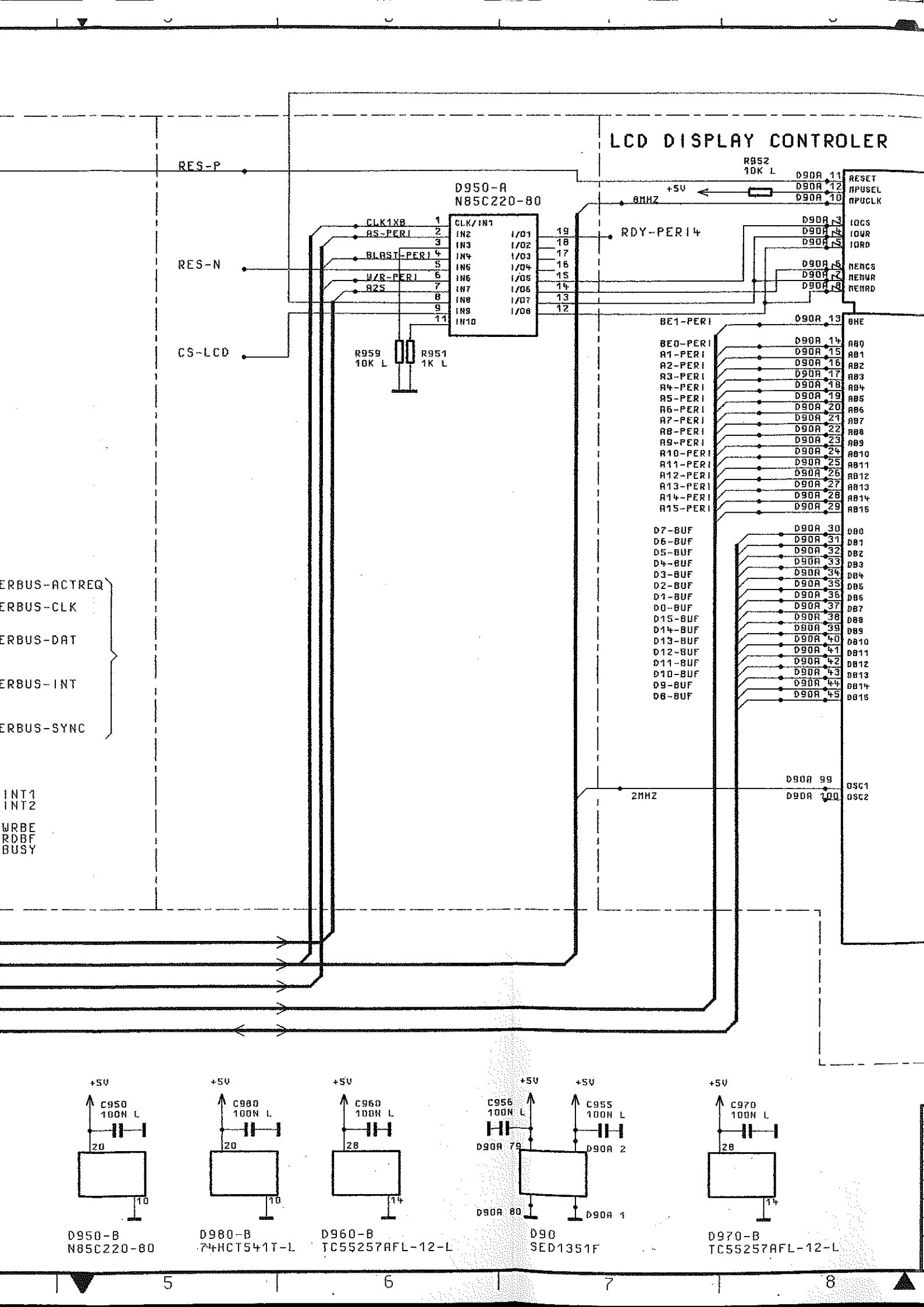


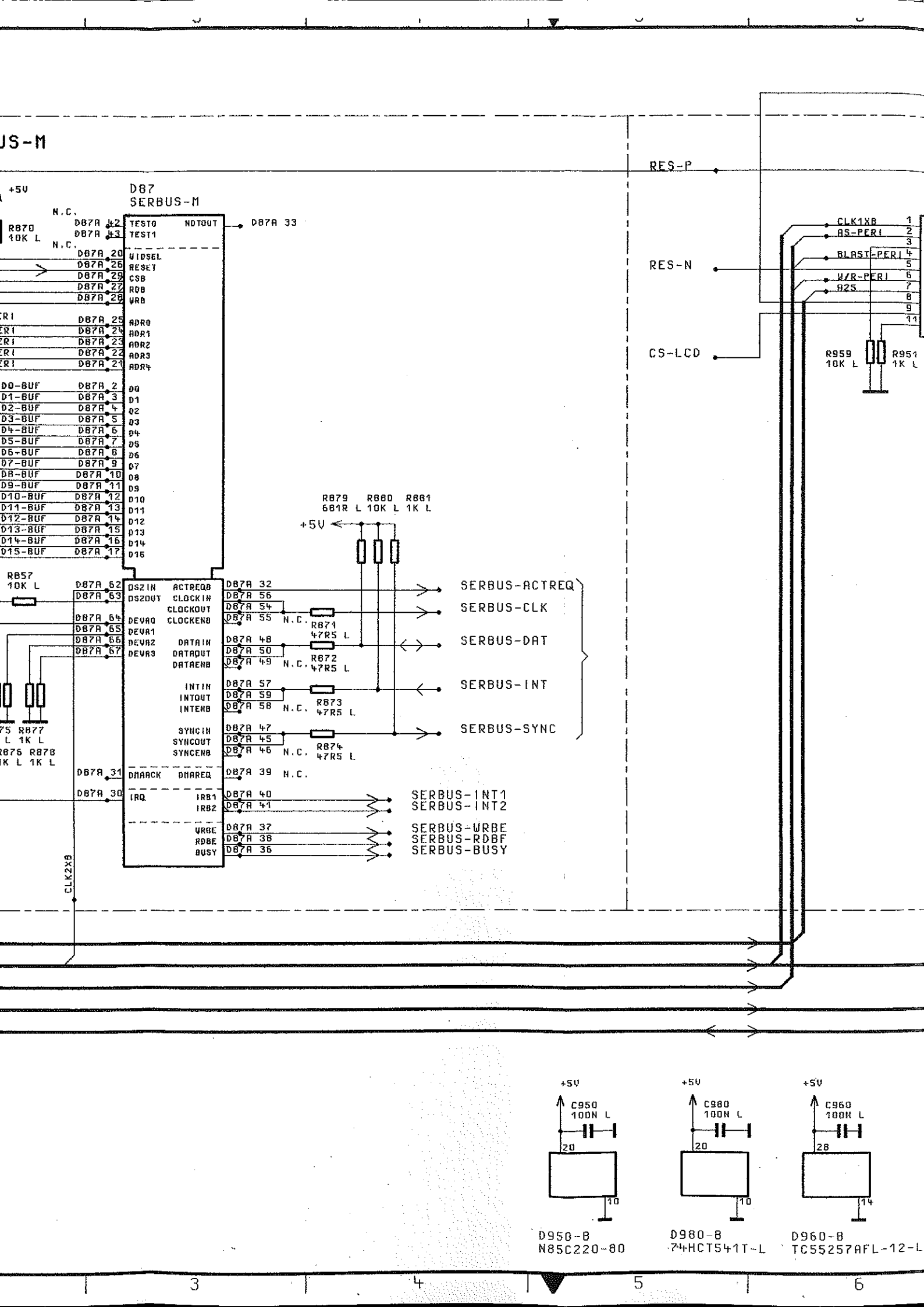
FÜR DIESE UNTERLAGE
BEHALTEN WIR UNS ALLE RECHTE VOR

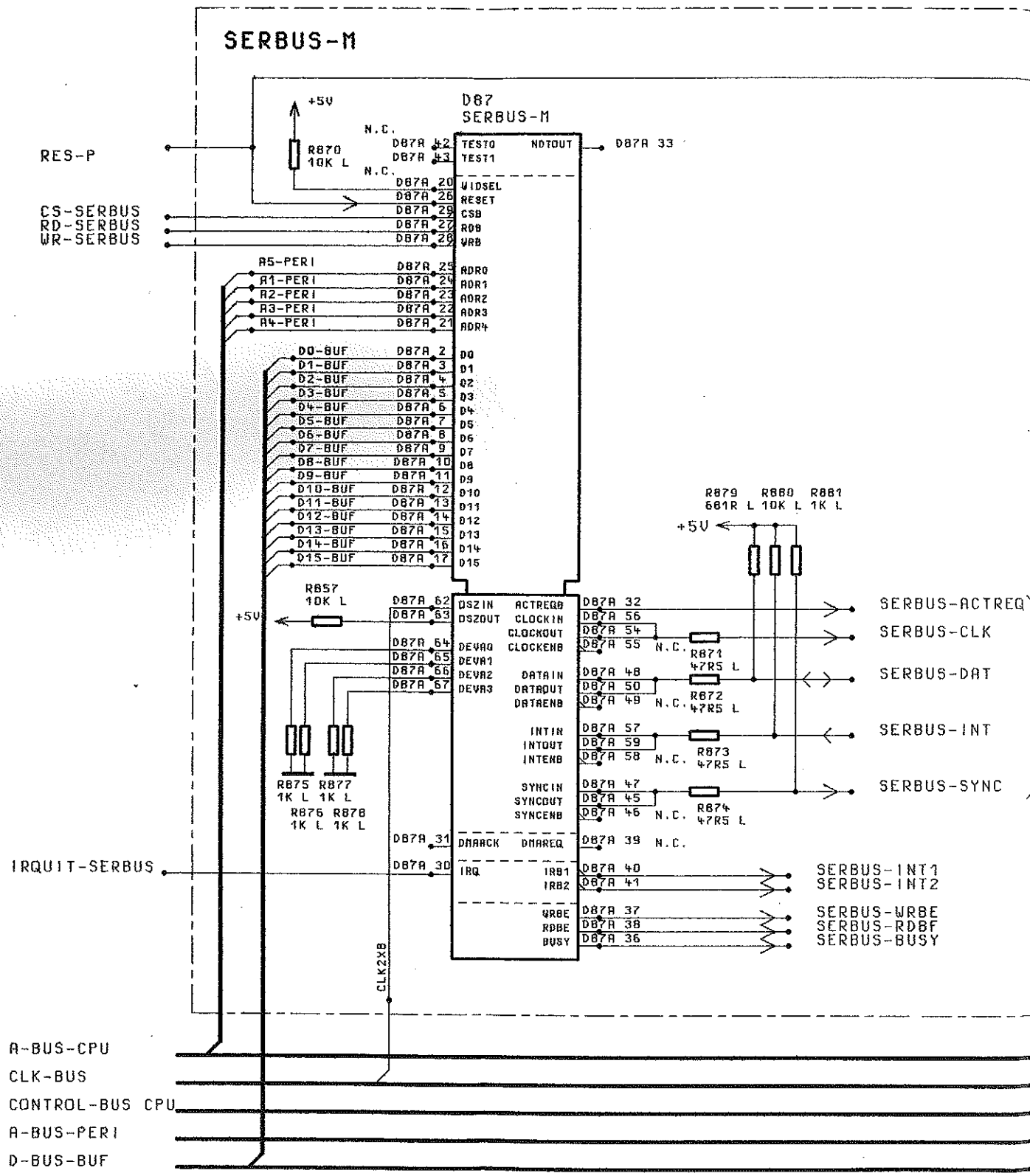


05/		19.04.94	DR	16PK	TAG	NAME	BENENNUNG	
				BEARB.		JN	RECHNER	
				GEPR.			PROCESSOR	
				NORM				
				PLOTT	03.05.94			
/				ROHDE&SCHWARZ			ZEICHN.-NR. 1035.7250.01S	BLATT-NR. 11+
REND. IND.	RENDERUNGS-MITTEILUNG	DATUM	NAME	ZU GERRET	SMP		REG.I.V. 1035.5005	ERSTE Z. 1035.5005


LCD DISPLAY CONTROLER
D90
SED1351F
VIDEO MEMORY
D950-A
N85C220-80
R952
10K L
+5V
8MHZ
RDY-PERI4
R951
1K L
RESET
MPUSEL
MPUCLK
IOCS
IOWR
IORD
MEMCS
MEMWR
MEMRD
READY
RDY-LCD
BE1-PERI
BE0-PERI
A1-PERI
A2-PERI
A3-PERI
A4-PERI
A5-PERI
A6-PERI
A7-PERI
A8-PERI
A9-PERI
A10-PERI
A11-PERI
A12-PERI
A13-PERI
A14-PERI
A15-PERI
D7-BUF
D6-BUF
D5-BUF
D4-BUF
D3-BUF
D2-BUF
D1-BUF
D0-BUF
D15-BUF
D14-BUF
D13-BUF
D12-BUF
D11-BUF
D10-BUF
D9-BUF
D8-BUF
2MHZ
D980-A
74HCT54
R960 R982 R981
1K L 1K L 1K L
+5V
C956
100N L
C955
100N L
C970
100N L
D90
SED1351F
D970-B
TC55257AFL-12-L
05/
19.04.94
DR
1GPK
TAG
NAME
BEARB.
JN
GEPR.
NORM
PLOTT
03.05.94
AEND. IND.
AENDERUNGS-MITTEILUNG
DATUM
NAME
ROHDE&SCHWARZ
ZU GERAET
SMP


LCD DISPLAY CONTROLER
RES-P
RES-N
CS-LCD
D950-A
N85C220-80
CLK1X8
AS-PERI
BLAST-PERI
W/R-PERI
A25
RDY-PERI4
R959
10K L
R951
1K L
R952
10K L
+5V
8MHZ
2MHZ
RESET
APUSEL
APUCLK
IOCS
IOWR
IORD
MEMCS
MEMWR
MEMRD
BHE
BE1-PERI
BE0-PERI
A1-PERI
A2-PERI
A3-PERI
A4-PERI
A5-PERI
A6-PERI
A7-PERI
A8-PERI
A9-PERI
A10-PERI
A11-PERI
A12-PERI
A13-PERI
A14-PERI
A15-PERI
D7-BUF
D6-BUF
D5-BUF
D4-BUF
D3-BUF
D2-BUF
D1-BUF
D0-BUF
D15-BUF
D14-BUF
D13-BUF
D12-BUF
D11-BUF
D10-BUF
D9-BUF
D8-BUF
OSC1
OSC2
ERBUS-ACTREQ
ERBUS-CLK
ERBUS-DAT
ERBUS-INT
ERBUS-SYNC
INT1
INT2
WRBE
RDBF
BUSY
C950 100N L
C980 100N L
C960 100N L
C956 100N L
C955 100N L
C970 100N L
D950-B
N85C220-80
D980-B
74HCT541T-L
D960-B
TC55257AFL-12-L
D90
SED1351F
D970-B
TC55257AFL-12-L

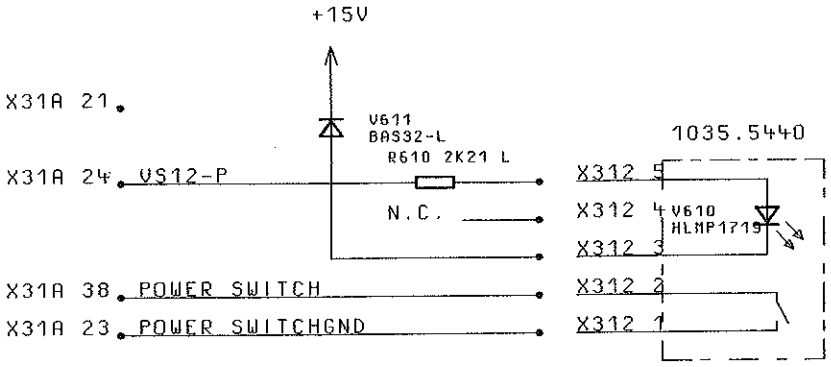

US-M
D87
SERBUS-M
+5V
R870
10K L
N.C.
D87A 42
D87A 43
TEST0
TEST1
NDTOUT
D87A 33
D87A 20
D87A 26
D87A 29
D87A 27
D87A 28
WIDSEL
RESET
CSB
RDB
WRB
D87A 25
D87A 24
D87A 23
D87A 22
D87A 21
ADR0
ADR1
ADR2
ADR3
ADR4
D0-BUF
D1-BUF
D2-BUF
D3-BUF
D4-BUF
D5-BUF
D6-BUF
D7-BUF
D8-BUF
D9-BUF
D10-BUF
D11-BUF
D12-BUF
D13-BUF
D14-BUF
D15-BUF
D87A 2
D87A 3
D87A 4
D87A 5
D87A 6
D87A 7
D87A 8
D87A 9
D87A 10
D87A 11
D87A 12
D87A 13
D87A 14
D87A 15
D87A 16
D87A 17
D0
D1
D2
D3
D4
D5
D6
D7
D8
D9
D10
D11
D12
D13
D14
D15
R879
681R L
R880
10K L
R881
1K L
+5V
R857
10K L
D87A 62
D87A 63
D87A 64
D87A 65
D87A 66
D87A 67
DSZIN
DSZOUT
DEVR0
DEVR1
DEVR2
DEVR3
ACTREQB
CLOCKIN
CLOCKOUT
CLOCKENB
DATAIN
DATAOUT
DATAENB
INTIN
INTOUT
INTENB
SYNCIN
SYNCOUT
SYNCENB
D87A 32
D87A 56
D87A 54
D87A 55
N.C.
R871
47R5 L
D87A 48
D87A 50
D87A 49
N.C.
R872
47R5 L
D87A 57
D87A 59
D87A 58
N.C.
R873
47R5 L
D87A 47
D87A 45
D87A 46
N.C.
R874
47R5 L
SERBUS-ACTREQ
SERBUS-CLK
SERBUS-DAT
SERBUS-INT
SERBUS-SYNC
D87A 31
D87A 30
DMAACK
DMAREQ
IRQ
IRB1
IRB2
VRBE
RDBE
BUSY
D87A 39
N.C.
D87A 40
D87A 41
D87A 37
D87A 38
D87A 36
SERBUS-INT1
SERBUS-INT2
SERBUS-VRBE
SERBUS-RDBF
SERBUS-BUSY
CLK2XB
R875
L
R877
1K L
R876 R878
K L 1K L
RES-P
RES-N
CS-LCD
CLK1XB
AS-PERI
BLAST-PERI
W/R-PERI
A2S
R959
10K L
R951
1K L
+5V
C950
100N L
20
10
D950-B
N85C220-80
+5V
C980
100N L
20
10
D980-B
74HCT541T-L
+5V
C960
100N L
28
14
D960-B
TC55257AFL-12-L



FUER DIESE UNTERLAGE
BEHALTEN WIR UNS ALLE RECHTE VOR

ZEICHN.-NR.

D950-
N85C2



X31A 50 N.C.
X31A 35 N.C.
X31A 49 N.C.
X31A 34 N.C.
X31A 48 N.C.

X31A 33 X-AXIS — X-AXIS
X31A 47 DIAG-5V — DIAG-5V
X31A 32 DIAG-15V — DIAG-15V
X31A 46 TRIGGER — TRIGGER
X31A 31 AUX-TRIG — AUX-TRIG
X31A 45 SERBUS-CLK — SERBUS-CLK
X31A 30 SERBUS-DAT — SERBUS-DAT
X31A 44 SERBUS-INT — SERBUS-INT
X31A 29 SERBUS-SYNC — SERBUS-SYNC
X31A 43 SYSRESET → SYSRESET
X31A 28 AC-FAIL — AC-FAIL

X31A 42 BLANK ← BLANK
X31A 27 MARKER ← MARKER
X31A 41 SWEEP-STOP → SWEEP-STOP
X31A 26 MODCTRL-IN → MODCTRL-IN
X31A 40 MODCTRL-OUT ← MODCTRL-OUT
X31A 25 RESERVE ← RESERVE
X31A 39 RES-P ← RES-P

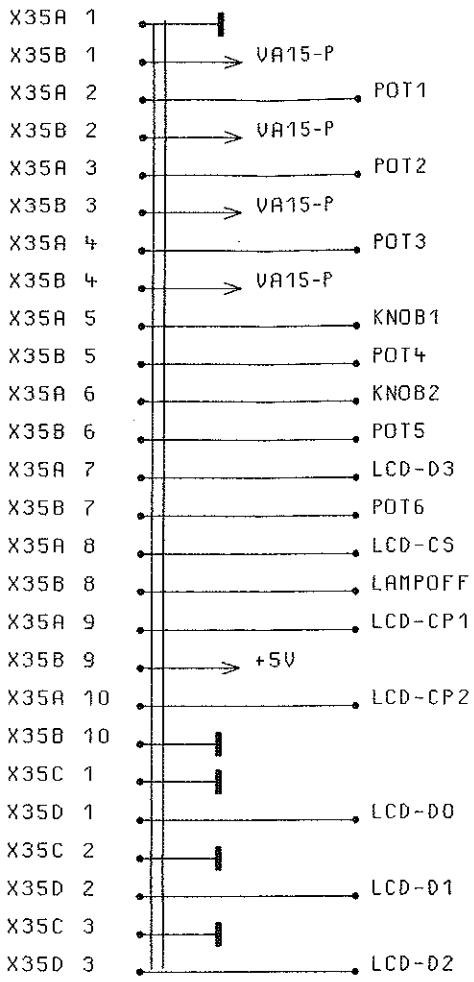
R611
10R L

BINDENDE ANGABEN UEBER VARIANTEN, TRIMMWERTE, BAUTEILWERTE UND NICHT BESTUECKTE BAUTEILE SIEHE SA.

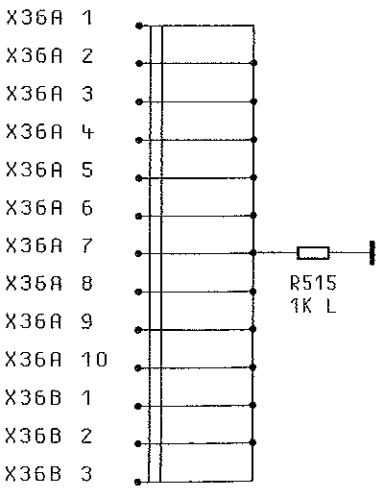
FOR BINDING INFORMATION ON MODELS, TRIMMING AND COMPONENTS VALUES AND NONFITTED COMPONENTS SEE PARTS LIST.

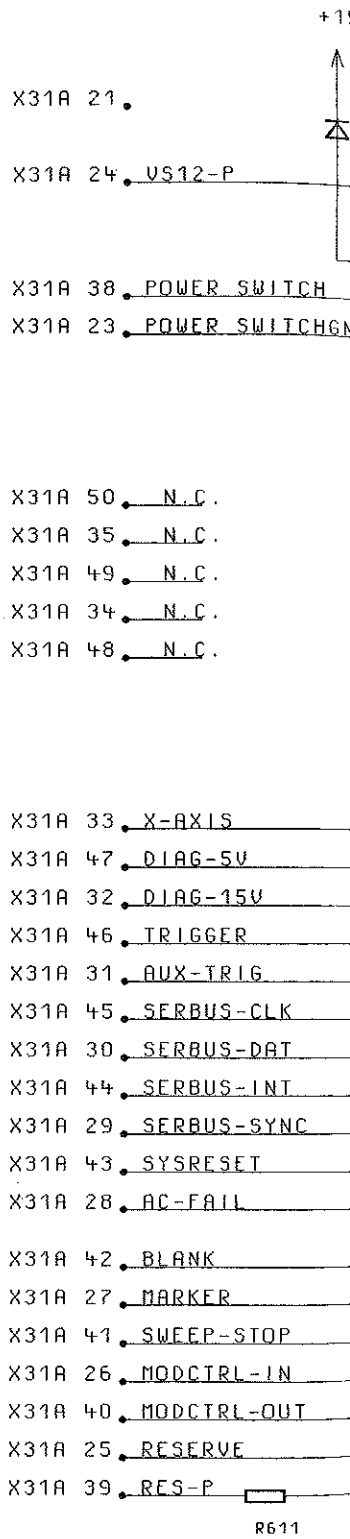
08/13		97-12-08	EI	MENP	TAG	NAME	BENENNUNG
				BEARB.		EI	RECHNER
				GEPR.			PROCESSOR
				NORM			
				PLOTT	09.12.97		
08/		13.03.95	DR	ROHDE&SCHWARZ			ZEICHN.-NR. 1035.7250.01S
AEND. IND.	AENDERUNGS-MITTEILUNG	DATUM	NAME	ZU GERAET SMP			BLATT-NR. 12+ v. 30
				REG.I.V. 1035.5005		ERSTE Z. 1035.5005	

KNOB



KEYBOARD





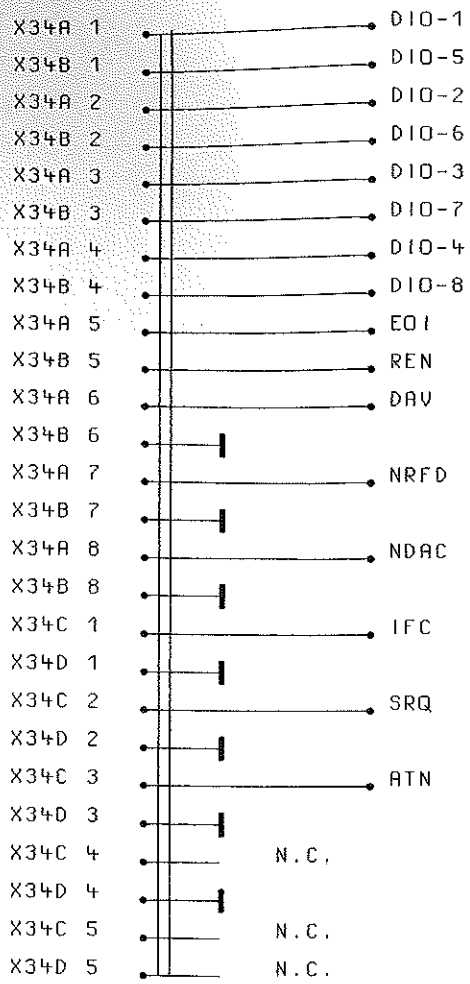
BINDENDE ANGABEN UEBER VARIANTEN, TRIMMWERTE, BAUTEILWERTE UND NICHT BESTUECKTE BAUTEILE SIEHE SA.

FOR B
TRIMM
NONFI

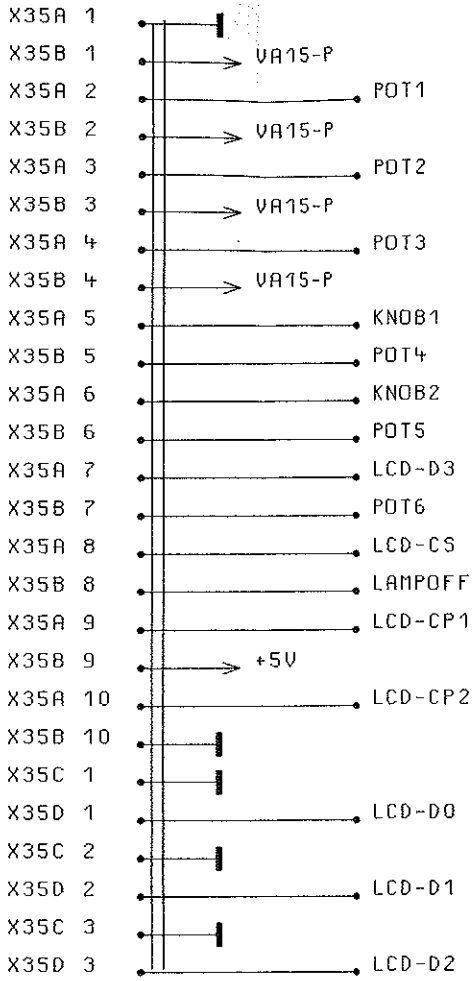
ACHTUNG: EGB!
ELEKTROSTATISCH GEFAEHRDETE BAUELEMENTE ERFORDERN EINE BESONDERE HANDHABUNG.
ATTENTION ESD!
ELECTROSTATIC SENSITIVE DEVICES REQUIRE A SPECIAL HANDLING

08/13		97-12-08	EI	MENP	TAG
				BEARB.	
				GEPR.	
				NORM	
				PLOTT	09.12.97
08/		13.03.95	DR		
AEND. IND.	AENDERUNGS-MITTEILUNG	DATUM	NAME	ROHDE&SC	
				ZU GERAET	SMP

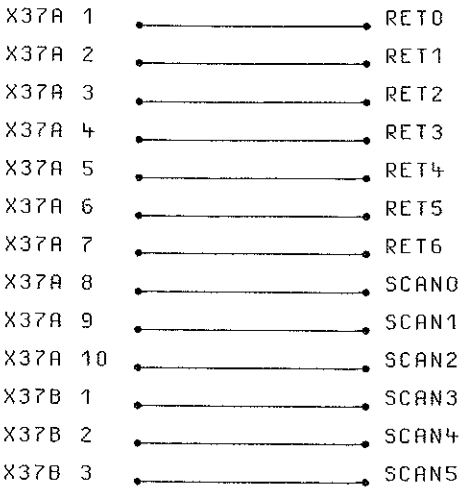
IEC625



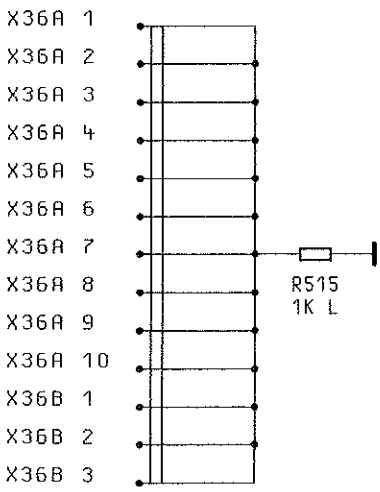
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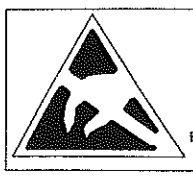


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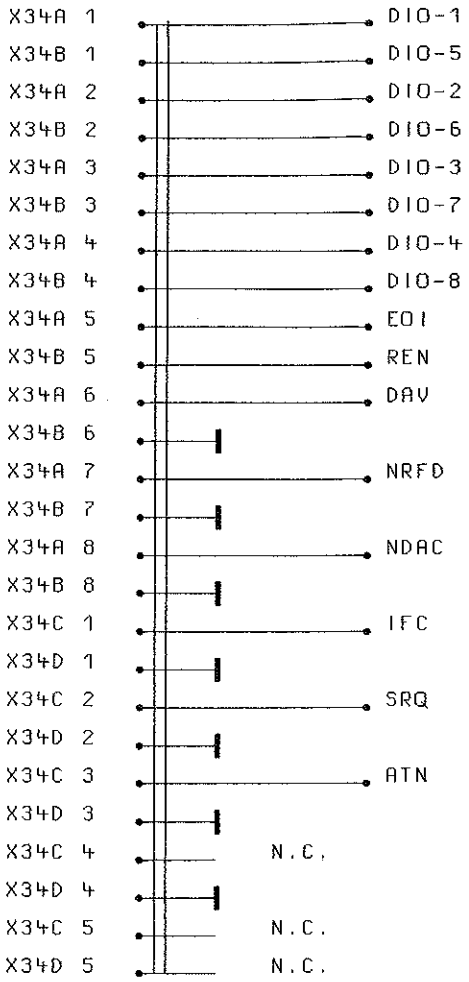


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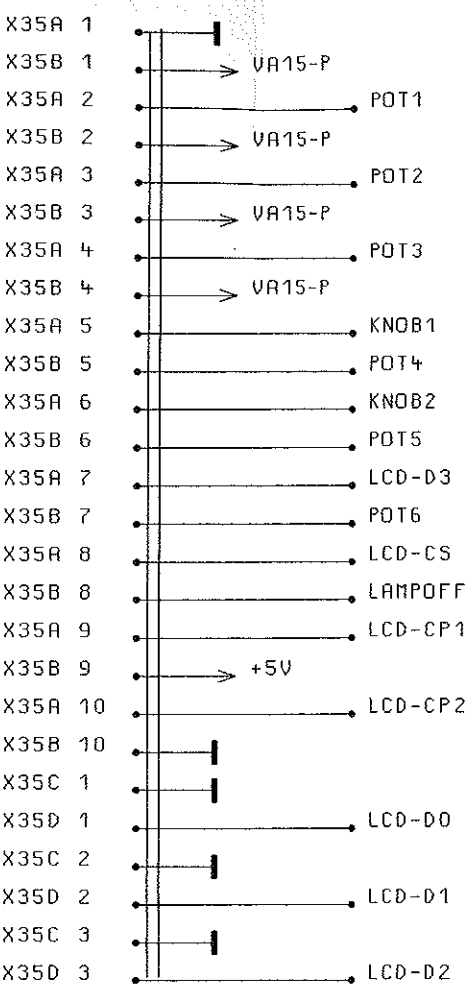




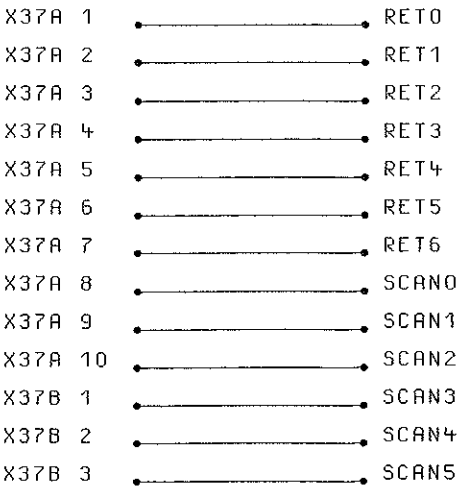
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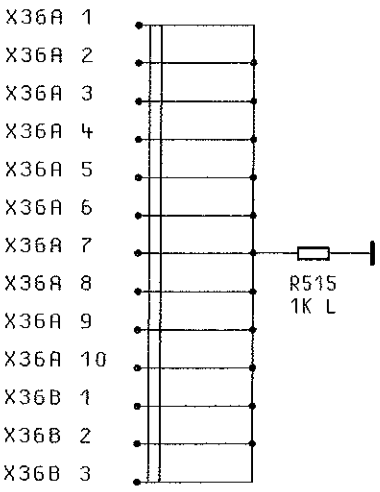
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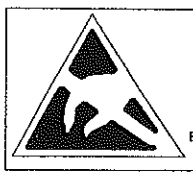


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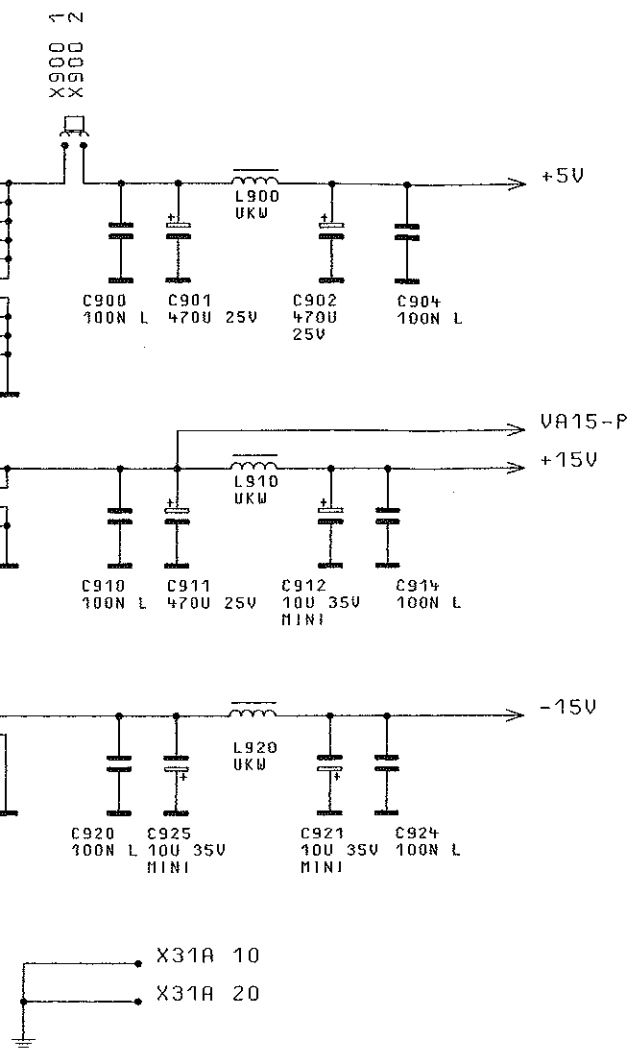


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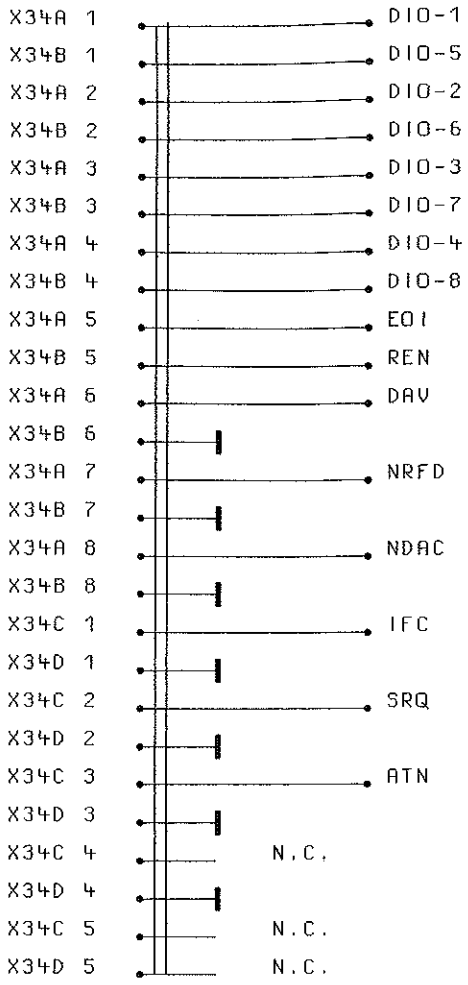




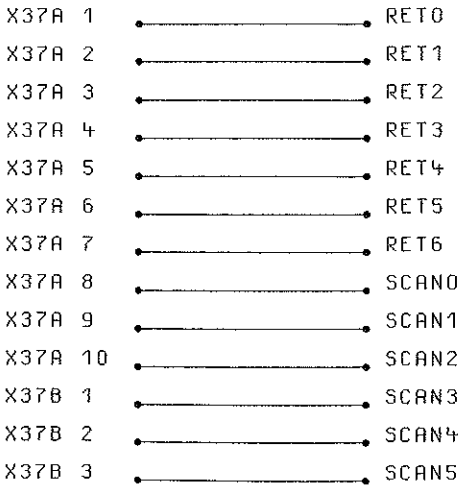
ACHTUNG: EG
ELEKTROSTATISCH GEF
BAUELEMENTE ERFORD
BESONDERE HANDH
ATTENTION ES
ELECTROSTATIC SENSIT
REQUIRE A SPECIAL



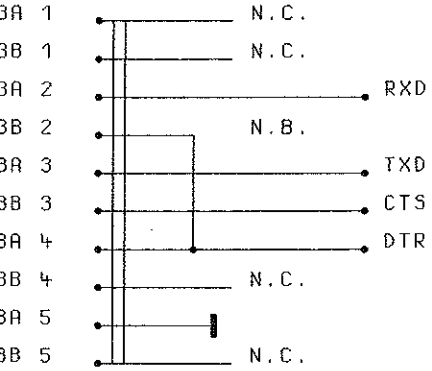
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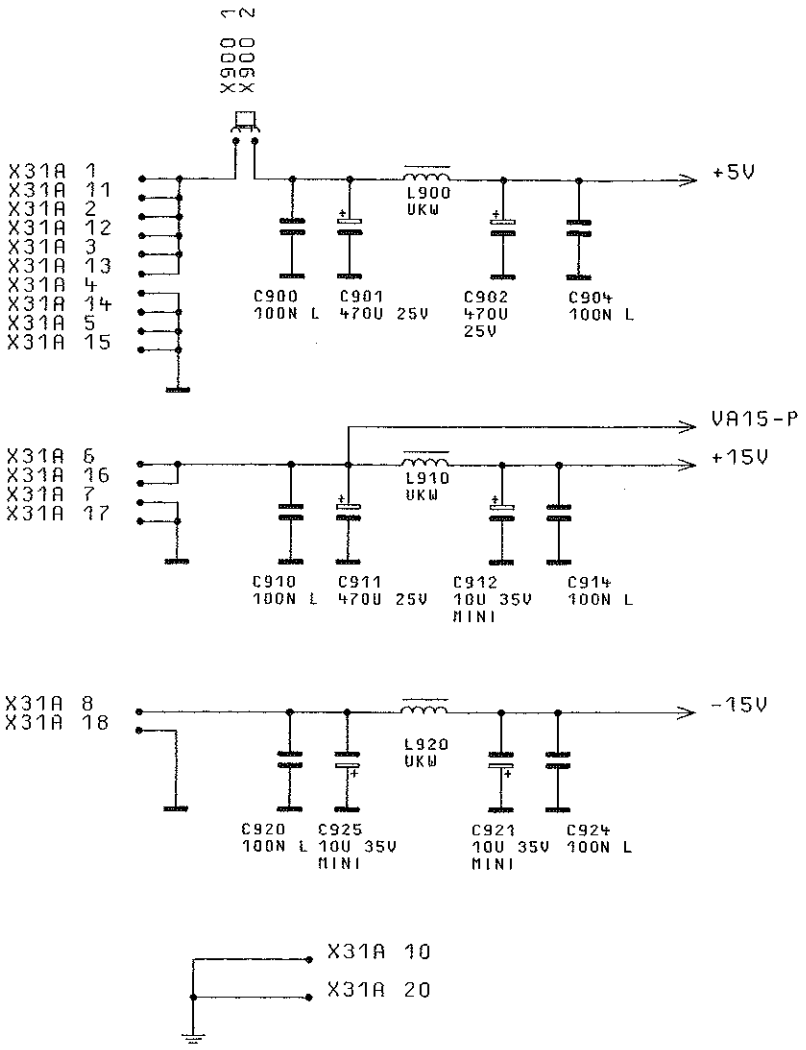


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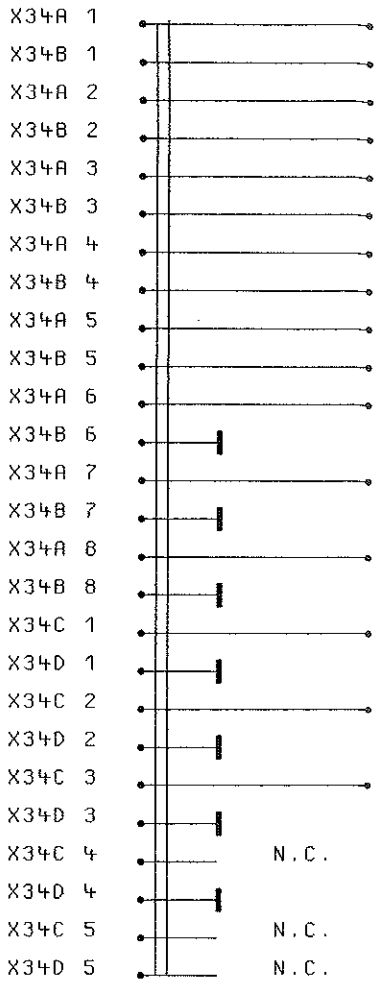


S232

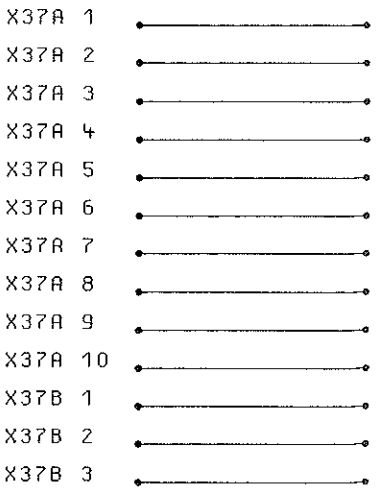




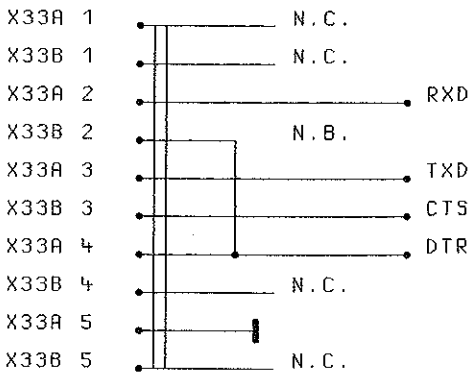
IEC625



KEYBOARD



RS232



FUER DIESE UNTERLAGE
BEHALTEN WIR UNS ALLE RECHTE VOR

ZEICHN.-NR.